

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
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2				*****
3				*
4				*Testcase IEEE SUBTRACT
5				* Test case capability includes IEEE exceptions trappable and
6				* otherwise. Test results, FPCR flags, the Condition code, and any
7				* DXC are saved for all tests.
8				*
9				*
10				* *****
11				** IMPORTANT! **
12				* *****
13				*
14				* This test uses the Hercules Diagnose X'008' interface
15				* to display messages and thus your .tst runtest script
16				* MUST contain a "DIAG8CMD ENABLE" statement within it!
17				*
18				*
19				*****
21				*****
22				*
23				* bfp-018-subtract.asm
24				*
25				* This assembly-language source file is part of the
26				* Hercules Binary Floating Point Validation Package
27				* by Stephen R. Orso
28				*
29				* Copyright 2016 by Stephen R Orso.
30				* Runtest *Compare dependency removed by Fish on 2022-08-16
31				* PADCSECT macro/usage removed by Fish on 2022-08-16
32				*
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56				* PROFITS; OR BUSINESS INTERRUPTION) HOWEVER CAUSED AND ON ANY THEORY

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				57 * OF LIABILITY, WHETHER IN CONTRACT, STRICT LIABILITY, OR TORT
				58 * (INCLUDING NEGLIGENCE OR OTHERWISE) ARISING IN ANY WAY OUT OF THE USE
				59 * OF THIS SOFTWARE, EVEN IF ADVISED OF THE POSSIBILITY OF SUCH DAMAGE.
				60 *
				61 *****
				63 *****
				64 *
				65 * Tests the following three conversion instructions
				66 * SUBTRACT (short BFP, RRE)
				67 * SUBTRACT (long BFP, RRE)
				68 * SUBTRACT (extended BFP, RRE)
				69 * SUBTRACT (short BFP, RXE)
				70 * SUBTRACT (long BFP, RXE)
				71 *
				72 * Test data is compiled into this program. The test script that runs
				73 * this program can provide alternative test data through Hercules R
				74 * commands.
				75 *
				76 * Test Case Order
				77 * 1) Short BFP basic tests, including traps and NaN propagation
				78 * 2) Short BFP finite number tests, incl. traps and scaling
				79 * 3) Short BFP FPC-controlled rounding mode exhaustive tests
				80 * 4) Long BFP basic tests, including traps and NaN propagation
				81 * 5) Long BFP finite number tests, incl. traps and scaling
				82 * 6) Long BFP FPC-controlled rounding mode exhaustive tests
				83 * 7) Extended BFP basic tests, including traps and NaN propagation
				84 * 8) Extended BFP finite number tests, incl. traps and scaling
				85 * 9) Extended BFP FPC-controlled rounding mode exhaustive tests
				86 *
				87 * Three input test sets are provided each for short, long, and
				88 * extended BFP inputs. Test values are the same for each precision
				89 * for most tests. Overflow and underflow each require precision-
				90 * dependent test values.
				91 *
				92 * Also tests the following floating point support instructions
				93 * LOAD (Short)
				94 * LOAD (Long)
				95 * LFPC (Load Floating Point Control Register)
				96 * SRNMB (Set BFP Rounding Mode 3-bit)
				97 * STORE (Short)
				98 * STORE (Long)
				99 * STFPC (Store Floating Point Control Register)
				100 *
				101 *****

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
00000000		00000000		145 USING *,R15
00000000		0001AE00		146 USING HELPERS,R12
				147 *
				148 * Above works on real iron (R15=0 after sysclear)
				149 * and in z/CMS (R15 points to start of load module)
				150 *
				152 *****
				153 *
				154 * Low core definitions, Restart PSW, and Program Check Routine.
				155 *
				156 *****
00000000		00000000	0000008E	158 ORG STRTLABL+X'8E' Program check interruption code
0000008E	0000			159 PCINTCD DS H
				160 *
		00000150	00000001	161 PCOLDPSW EQU STRTLABL+X'150' z/Arch Program check old PSW
				162 *
00000090		00000090	000001A0	163 ORG STRTLABL+X'1A0' z/Arch Restart PSW
000001A0	00000001 80000000			164 DC X'0000000180000000',AD(START)
				165 *
000001B0		000001B0	000001D0	166 ORG STRTLABL+X'1D0' z/Arch Program check NEW PSW
000001D0	00000000 00000000			167 DC X'0000000000000000',AD(PROGCHK)
				168 *
				169 * Program check routine. If Data Exception, continue execution at
				170 * the instruction following the program check. Otherwise, hard wait.
				171 * No need to collect data. All interesting DXC stuff is captured
				172 * in the FPCR.
				173 *
000001E0		000001E0	00000200	174 ORG STRTLABL+X'200'
00000200				175 PROGCHK DS 0H Program check occurred...
00000200	9507 F08F		0000008F	176 CLI PCINTCD+1,X'07' Data Exception?
00000204	A774 0004		0000020C	177 JNE PCNOTDTA ..no, hardwait (not sure if R15 is ok)
00000208	B2B2 F150		00000150	178 LPSWE PCOLDPSW ..yes, resume program execution
0000020C	900F F23C		0000023C	180 PCNOTDTA STM R0,R15,SAVEREGS Save registers
00000210	58C0 F27C		0000027C	181 L R12,AHELPERS Get address of helper subroutines
00000214	4DD0 C000		0001AE00	182 BAS R13,PGMCK Report this unexpected program check
00000218	980F F23C		0000023C	183 LM R0,R15,SAVEREGS Restore registers
0000021C	12EE			185 LTR R14,R14 Return address provided?
0000021E	077E			186 BNZR R14 Yes, return to z/CMS test rig.
00000220	B2B2 F228		00000228	187 LPSWE PROGPSW Not data exception, enter disabled wait
00000228	00020000 00000000			188 PROGPSW DC 0D'0',X'0002000000000000',XL6'00',X'DEAD' Abnormal end
00000238	B2B2 F2F8		000002F8	189 FAIL LPSWE FAILPSW Not data exception, enter disabled wait
0000023C	00000000 00000000			190 SAVEREGS DC 16F'0' Registers save area
0000027C	0001AE00			191 AHELPERS DC A(HELPERS) Address of helper subroutines

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				193 *****
				194 *
				195 * Main program. Enable Advanced Floating Point, process test cases.
				196 *
				197 *****
00000280				199 START DS 0H
00000280	B600 F308		00000308	200 STCTL R0,R0,CTLR0 Store CR0 to enable AFP
00000284	9604 F309		00000309	201 OI CTLR0+1,X'04' Turn on AFP bit
00000288	B700 F308		00000308	202 LCTL R0,R0,CTLR0 Reload updated CR0
				203 *
0000028C	41A0 F314		00000314	204 LA R10,SHORTNF Point to short BFP non-finite inputs
00000290	4DD0 F3A4		000003A4	205 BAS R13,SBFPNF Subtract short BFP non-finites
00000294	41A0 F324		00000324	206 LA R10,SHORTF Point to short BFP finite inputs
00000298	4DD0 F45E		0000045E	207 BAS R13,SBFPF Subtract short BFP finites
0000029C	41A0 F334		00000334	208 LA R10,RMSHORTS Point to short BFP rounding mode tests
000002A0	4DD0 F504		00000504	209 BAS R13,SBFPRM Subtract short BFP for rounding tests
				210 *
000002A4	41A0 F344		00000344	211 LA R10,LONGNF Point to long BFP non-finite inputs
000002A8	4DD0 F586		00000586	212 BAS R13,LBFPNF Subtract long BFP non-finites
000002AC	41A0 F354		00000354	213 LA R10,LONGF Point to long BFP finite inputs
000002B0	4DD0 F63C		0000063C	214 BAS R13,LBFPPF Subtract long BFP finites
000002B4	41A0 F364		00000364	215 LA R10,RMLONGS Point to long BFP rounding mode tests
000002B8	4DD0 F6E2		000006E2	216 BAS R13,LBFPRM Subtract long BFP for rounding tests
				217 *
000002BC	41A0 F374		00000374	218 LA R10,XTDNF Point to extended BFP non-finite inputs
000002C0	4DD0 F760		00000760	219 BAS R13,XBFPNF Subtract extended BFP non-finites
000002C4	41A0 F384		00000384	220 LA R10,XTNDF Point to ext'd BFP finite inputs
000002C8	4DD0 F7EA		000007EA	221 BAS R13,XBFPPF Subtract ext'd BFP finites
000002CC	41A0 F394		00000394	222 LA R10,RMXTNDS Point to ext'd BFP rounding mode tests
000002D0	4DD0 F860		00000860	223 BAS R13,XBFPRM Subtract ext'd BFP for rounding tests
				224 *
				225 *****
				226 * Verify test results...
				227 *****
				228 *
000002D4	58C0 F27C		0000027C	229 L R12,AHELPERS Get address of helper subroutines
000002D8	4DD0 C0A0		0001AEA0	230 BAS R13,VERISUB Go verify results
000002DC	12EE			231 LTR R14,R14 Was return address provided?
000002DE	077E			232 BNZR R14 Yes, return to z/CMS test rig.
000002E0	B2B2 F2E8		000002E8	233 LPSWE GOODPSW Load SUCCESS PSW

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
000002E8				235	DS	0D	Ensure correct alignment for PSW
000002E8	00020000	00000000		236	GOODPSW	DC	X'0002000000000000',AD(0) Normal end - disabled wait
000002F8	00020000	00000000		237	FAILPSW	DC	X'0002000000000000',XL6'00',X'0BAD' Abnormal end
				238	*		
00000308	00000000			239	CTLR0	DS	F
0000030C	00000000			240	FPCREGNT	DC	X'00000000' FPCR, trap all IEEE exceptions, zero flags
00000310	F8000000			241	FPCREGTR	DC	X'F8000000' FPCR, trap no IEEE exceptions, zero flags
				242	*		
				243	* Input values parameter list, four fullwords for each test data set		
				244	* 1) Count,		
				245	* 2) Address of inputs,		
				246	* 3) Address to place results, and		
				247	* 4) Address to place DXC/Flags/cc values.		
				248	*		
00000314				249	SHORTNF	DS	0F Input pairs for short BFP non-finite tests
00000314	0000000A			250		DC	A(SBFPNFCT)
00000318	000008CC			251		DC	A(SBFPNFIN)
0000031C	00001000			252		DC	A(SBFPNFOT)
00000320	00001700			253		DC	A(SBFPNFFL)
				254	*		
00000324				255	SHORTF	DS	0F Input pairs for short BFP finite tests
00000324	00000006			256		DC	A(SBFPCT)
00000328	000008F4			257		DC	A(SBFPIN)
0000032C	00001E00			258		DC	A(SBFPOUT)
00000330	00001F00			259		DC	A(SBFPFLGS)
				260	*		
00000334				261	RMSHORTS	DS	0F Input pairs for short BFP rounding testing
00000334	00000008			262		DC	A(SBFPRMCT)
00000338	00000924			263		DC	A(SBFPINRM)
0000033C	00002000			264		DC	A(SBFPRMO)
00000340	00002300			265		DC	A(SBFPRMOF)
				266	*		
00000344				267	LONGNF	DS	0F Input pairs for long BFP non-finite testing
00000344	0000000A			268		DC	A(LBFPNFCT)
00000348	00000964			269		DC	A(LBFPNFIN)
0000034C	00004000			270		DC	A(LBFPNFOT)
00000350	00004D00			271		DC	A(LBFPNFFL)
				272	*		
00000354				273	LONGF	DS	0F Input pairs for long BFP finite testing
00000354	00000006			274		DC	A(LBFPCT)
00000358	000009B8			275		DC	A(LBFPIN)
0000035C	00005400			276		DC	A(LBFPOUT)
00000360	00005600			277		DC	A(LBFPFLGS)
				278	*		
00000364				279	RMLONGS	DS	0F Input pairs for long BFP rounding testing
00000364	00000008			280		DC	A(LBFPRMCT)
00000368	00000A18			281		DC	A(LBFPINRM)
0000036C	00005700			282		DC	A(LBFPRMO)
00000370	00005C00			283		DC	A(LBFPRMOF)
				284	*		
00000374				285	XTNDNF	DS	0F Inputs for ext'd BFP non-finite testing
00000374	0000000A			286		DC	A(XBFPNFCT)
00000378	00000A98			287		DC	A(XBFPNFIN)
0000037C	00008000			288		DC	A(XBFPNFOT)
00000380	00008D00			289		DC	A(XBFPNFFL)
				290	*		

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				304 *****
				305 *
				306 * Perform Subtract using provided short BFP inputs. This set of tests
				307 * checks NaN propagation, operations on values that are not finite
				308 * numbers, and other basic tests. This set generates results that can
				309 * be validated against Figure 19-13 on page 19-16 of SA22-7832-10.
				310 *
				311 * That Figure has separate rows and columns for Normal and Tiny
				312 * operands. Although the results are effectively the same for Normal
				313 * and Tiny in any combination, the input data includes Normal and
				314 * Tiny values.
				315 *
				316 * Four results are generated for each input: one RRE with all
				317 * exceptions non-trappable, a second RRE with all exceptions trappable,
				318 * a third RXE with all exceptions non-trappable, a fourth RXE with all
				319 * exceptions trappable,
				320 *
				321 * The difference, FPCR, and condition code are stored for each result.
				322 *
				323 *****
000003A4				325 SBFPNF DS 0H BFP Short non-finite values tests
000003A4	9823 A000		00000000	326 LM R2,R3,0(R10) Get count and address of minuend values
000003A8	9878 A008		00000008	327 LM R7,R8,8(R10) Get address of result area and flag area.
000003AC	1222			328 LTR R2,R2 Any test cases?
000003AE	078D			329 BZR R13 ..No, return to caller
000003B0	0DC0			330 BASR R12,0 Set top of loop
				331 *
000003B2	9845 A000		00000000	332 LM R4,R5,0(R10) Get count and start of subtrahend values
				333 * ..which are the same as the minuends
000003B6	0D60			334 BASR R6,0 Set top of inner loop
				335 *
000003B8	7880 3000		00000000	336 LE FPR8,0(,R3) Get short BFP minuend
000003BC	7810 5000		00000000	337 LE FPR1,0(,R5) Get short BFP subtrahend
000003C0	B29D F30C		0000030C	338 LFPC FPCREGNT Set exceptions non-trappable
000003C4	B30B 0081			339 SEBR FPR8,FPR1 Subtract short FPR1 from FPR8 RRE
000003C8	7080 7000		00000000	340 STE FPR8,0(,R7) Store short BFP difference
000003CC	B29C 8000		00000000	341 STFPC 0(R8) Store resulting FPCR flags and DXC
000003D0	B222 0000			342 IPM R0 Get condition code and program mask
000003D4	8800 001C		0000001C	343 SRL R0,28 Isolate CC in low order byte
000003D8	4200 8003		00000003	344 STC R0,3(,R8) Save condition code in results table
				345 *
000003DC	7880 3000		00000000	346 LE FPR8,0(,R3) Get short BFP minuend
000003E0	7810 5000		00000000	347 LE FPR1,0(,R5) Get short BFP subtrahend
000003E4	B29D F310		00000310	348 LFPC FPCREGTR Set exceptions trappable
000003E8	B30B 0081			349 SEBR FPR8,FPR1 Subtract short FPR1 from FPR8 RRE
000003EC	7080 7004		00000004	350 STE FPR8,4(,R7) Store short BFP difference
000003F0	B29C 8004		00000004	351 STFPC 4(R8) Store resulting FPCR flags and DXC
000003F4	B222 0000			352 IPM R0 Get condition code and program mask
000003F8	8800 001C		0000001C	353 SRL R0,28 Isolate CC in low order byte
000003FC	4200 8007		00000007	354 STC R0,7(,R8) Save condition code in results table
				355 *
00000400	7880 3000		00000000	356 LE FPR8,0(,R3) Get short BFP minuend
00000404	7810 5000		00000000	357 LE FPR1,0(,R5) Get short BFP subtrahend
00000408	B29D F30C		0000030C	358 LFPC FPCREGNT Set exceptions non-trappable

LOC	OBJECT CODE			ADDR1	ADDR2	STMT			
0000040C	ED80	5000	000B		00000000	359	SEB	FPR8,0(,R5)	Subtract short subtrahend from FPR8 RXE
00000412	7080	7008			00000008	360	STE	FPR8,8(,R7)	Store short BFP difference
00000416	B29C	8008			00000008	361	STFPC	8(R8)	Store resulting FPCR flags and DXC
0000041A	B222	0000				362	IPM	R0	Get condition code and program mask
0000041E	8800	001C			0000001C	363	SRL	R0,28	Isolate CC in low order byte
00000422	4200	800B			0000000B	364	STC	R0,11(,R8)	Save condition code in results table
						365	*		
00000426	7880	3000			00000000	366	LE	FPR8,0(,R3)	Get short BFP minuend
0000042A	B29D	F310			00000310	367	LFPC	FPCREGTR	Set exceptions trappable
0000042E	ED80	5000	000B		00000000	368	SEB	FPR8,0(,R5)	Subtract short subtrahend from FPR8 RXE
00000434	7080	700C			0000000C	369	STE	FPR8,12(,R7)	Store short BFP difference
00000438	B29C	800C			0000000C	370	STFPC	12(R8)	Store resulting FPCR flags and DXC
0000043C	B222	0000				371	IPM	R0	Get condition code and program mask
00000440	8800	001C			0000001C	372	SRL	R0,28	Isolate CC in low order byte
00000444	4200	800F			0000000F	373	STC	R0,15(,R8)	Save condition code in results table
						374	*		
00000448	4150	5004			00000004	375	LA	R5,4(,R5)	Point to next subtrahend value
0000044C	4170	7010			00000010	376	LA	R7,4*4(,R7)	Point to next Subtract result area
00000450	4180	8010			00000010	377	LA	R8,4*4(,R8)	Point to next Subtract FPCR area
00000454	0646					378	BCTR	R4,R6	Loop through right-hand values
						379	*		
00000456	4130	3004			00000004	380	LA	R3,4(,R3)	Point to next input minuend
0000045A	062C					381	BCTR	R2,R12	Loop through left-hand values
0000045C	07FD					382	BR	R13	All converted; return.

LOC	OBJECT	CODE	ADDR1	ADDR2	STMT	
					384 *****	
					385 *	
					386 * Perform Subtract using provided short BFP input pairs. This set of	
					387 * tests triggers IEEE exceptions Overflow, Underflow, and Inexact and	
					388 * collects both trap and non-trap results.	
					389 *	
					390 * Four results are generated for each input: one RRE with all	
					391 * exceptions non-trappable, a second RRE with all exceptions trappable,	
					392 * a third RXE with all exceptions non-trappable, a fourth RXE with all	
					393 * exceptions trappable,	
					394 *	
					395 * The difference, FPCR, and condition code are stored for each result.	
					396 *	
					397 *****	
0000045E	9823	A000		00000000	399 SBFPF LM R2,R3,0(R10)	Get count and address of test input values
00000462	9878	A008		00000008	400 LM R7,R8,8(R10)	Get address of result area and flag area.
00000466	1222				401 LTR R2,R2	Any test cases?
00000468	078D				402 BZR R13	..No, return to caller
0000046A	0DC0				403 BASR R12,0	Set top of loop
					404 *	
0000046C	B29D	F30C		0000030C	405 LFPC FPCREGNT	Set exceptions non-trappable
00000470	7880	3000		00000000	406 LE FPR8,0(,R3)	Get short BFP minuend
00000474	7810	3004		00000004	407 LE FPR1,4(,R3)	Get short BFP subtrahend
00000478	B30B	0081			408 SEBR FPR8,FPR1	Subtract short FPR1 from FPR8 RRE
0000047C	7080	7000		00000000	409 STE FPR8,0(,R7)	Store short BFP difference
00000480	B29C	8000		00000000	410 STFPC 0(R8)	Store resulting FPCR flags and DXC
00000484	B222	0000			411 IPM R0	Get condition code and program mask
00000488	8800	001C		0000001C	412 SRL R0,28	Isolate CC in low order byte
0000048C	4200	8003		00000003	413 STC R0,3(,R8)	Save condition code in results table
					414 *	
00000490	B29D	F310		00000310	415 LFPC FPCREGTR	Set exceptions trappable
00000494	7880	3000		00000000	416 LE FPR8,0(,R3)	Reload short BFP minuend
					417 *	..subtrahend is still in FPR1
00000498	B30B	0081			418 SEBR FPR8,FPR1	Subtract short FPR1 from FPR8 RRE
0000049C	7080	7004		00000004	419 STE FPR8,4(,R7)	Store short BFP difference
000004A0	B29C	8004		00000004	420 STFPC 4(R8)	Store resulting FPCR flags and DXC
000004A4	B222	0000			421 IPM R0	Get condition code and program mask
000004A8	8800	001C		0000001C	422 SRL R0,28	Isolate CC in low order byte
000004AC	4200	8007		00000007	423 STC R0,7(,R8)	Save condition code in results table
					424 *	
000004B0	B29D	F30C		0000030C	425 LFPC FPCREGNT	Set exceptions non-trappable
000004B4	7880	3000		00000000	426 LE FPR8,0(,R3)	Reload short BFP minuend
000004B8	ED80	3004 000B		00000004	427 SEB FPR8,4(,R3)	Subtract short subtrahend from FPR8 RXE
000004BE	7080	7008		00000008	428 STE FPR8,8(,R7)	Store short BFP difference
000004C2	B29C	8008		00000008	429 STFPC 8(R8)	Store resulting FPCR flags and DXC
000004C6	B222	0000			430 IPM R0	Get condition code and program mask
000004CA	8800	001C		0000001C	431 SRL R0,28	Isolate CC in low order byte
000004CE	4200	800B		0000000B	432 STC R0,11(,R8)	Save condition code in results table
					433 *	
000004D2	B29D	F310		00000310	434 LFPC FPCREGTR	Set exceptions trappable
000004D6	7880	3000		00000000	435 LE FPR8,0(,R3)	Reload short BFP minuend
000004DA	ED80	3004 000B		00000004	436 SEB FPR8,4(,R3)	Subtract short subtrahend from FPR8 RXE
000004E0	7080	700C		0000000C	437 STE FPR8,12(,R7)	Store short BFP difference
000004E4	B29C	800C		0000000C	438 STFPC 12(R8)	Store resulting FPCR flags and DXC

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
000004E8	B222 0000			439	IPM	R0	Get condition code and program mask
000004EC	8800 001C		0000001C	440	SRL	R0,28	Isolate CC in low order byte
000004F0	4200 800F		0000000F	441	STC	R0,15(,R8)	Save condition code in results table
				442 *			
000004F4	4130 3008		00000008	443	LA	R3,2*4(,R3)	Point to next input value pair
000004F8	4170 7010		00000010	444	LA	R7,4*4(,R7)	Point to next difference result set
000004FC	4180 8010		00000010	445	LA	R8,4*4(,R8)	Point to next FPCR result set
00000500	062C			446	BCTR	R2,R12	Convert next input value.
00000502	07FD			447	BR	R13	All converted; return.

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				449 *****
				450 *
				451 * Perform Subtract using provided short BFP input pairs. This set of
				452 * tests exhaustively tests all rounding modes available for Subtract.
				453 * The rounding mode can only be specified in the FPC.
				454 *
				455 * All five FPC rounding modes are tested because the preceeding tests,
				456 * using rounding mode RNTE, do not often create results that require
				457 * rounding.
				458 *
				459 * Two results are generated for each input and rounding mode: one RRE
				460 * and one RXE. Traps are disabled for all rounding mode tests.
				461 *
				462 * The difference, FPCR, and condition code are stored for each test.
				463 *
				464 *****
00000504	9823 A000		00000000	466 SBFPRM LM R2,R3,0(R10) Get count and address of test input values
00000508	9878 A008		00000008	467 LM R7,R8,8(R10) Get address of result area and flag area.
0000050C	1222			468 LTR R2,R2 Any test cases?
0000050E	078D			469 BZR R13 ..No, return to caller
00000510	1711			470 XR R1,R1 Zero register 1 for use in IC/STC/indexing
00000512	0DC0			471 BASR R12,0 Set top of test case loop
				472
00000514	4150 0005		00000005	473 LA R5,FPCMCT Get count of FPC modes to be tested
00000518	0D90			474 BASR R9,0 Set top of rounding mode outer loop
				475 *
0000051A	4315 F8C3		000008C3	476 IC R1,FPCMODES-L'FPCMODES(R5) Get next FPC mode
				477 *
0000051E	B29D F30C		0000030C	478 LFPC FPCREGNT Set exceptions non-trappable, clear flags
00000522	B2B8 1000		00000000	479 SRNMB 0(R1) Set FPC Rounding Mode
00000526	7880 3000		00000000	480 LE FPR8,0(,R3) Get short BFP minuend
0000052A	7810 3004		00000004	481 LE FPR1,4(,R3) Get short BFP subtrahend
0000052E	B30B 0081			482 SEBR FPR8,FPR1 Subtract short FPR1 from FPR8 RRE
00000532	7080 7000		00000000	483 STE FPR8,0(,R7) Store short BFP difference
00000536	B29C 8000		00000000	484 STFPC 0(R8) Store resulting FPCR flags and DXC
0000053A	B222 0000			485 IPM R0 Get condition code and program mask
0000053E	8800 001C		0000001C	486 SRL R0,28 Isolate CC in low order byte
00000542	4200 8003		00000003	487 STC R0,3(,R8) Save condition code in results table
				488 *
00000546	B29D F30C		0000030C	489 LFPC FPCREGNT Set exceptions non-trappable, clear flags
0000054A	B2B8 1000		00000000	490 SRNMB 0(R1) Set FPC Rounding Mode
0000054E	7880 3000		00000000	491 LE FPR8,0(,R3) Get short BFP minuend
00000552	ED80 3004 000B		00000004	492 SEB FPR8,4(,R3) Subtract short subtrahend from FPR8 RXE
00000558	7080 7004		00000004	493 STE FPR8,4(,R7) Store short BFP difference
0000055C	B29C 8004		00000004	494 STFPC 4(R8) Store resulting FPCR flags and DXC
00000560	B222 0000			495 IPM R0 Get condition code and program mask
00000564	8800 001C		0000001C	496 SRL R0,28 Isolate CC in low order byte
00000568	4200 8007		00000007	497 STC R0,7(,R8) Save condition code in results table
				498 *
0000056C	4170 7008		00000008	499 LA R7,2*4(,R7) Point to next difference result set
00000570	4180 8008		00000008	500 LA R8,2*4(,R8) Point to next FPCR result area
				501 *
00000574	0659			502 BCTR R5,R9 Iterate to next FPC mode for this input
				503 *

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
				516 *****	
				517 *	
				518 * Perform Subtract using provided long BFP inputs. This set of tests	
				519 * checks NaN propagation, operations on values that are not finite	
				520 * numbers, and other basic tests. This set generates results that can	
				521 * be validated against Figure 19-13 on page 19-16 of SA22-7832-10.	
				522 *	
				523 * That Figure has separate rows and columns for Normal and Tiny	
				524 * operands. Although the results are effectively the same for Normal	
				525 * and Tiny in any combination, the input data includes Normal and	
				526 * Tiny values.	
				527 *	
				528 * Four results are generated for each input: one RRE with all	
				529 * exceptions non-trappable, a second RRE with all exceptions trappable,	
				530 * a third RXE with all exceptions non-trappable, a fourth RXE with all	
				531 * exceptions trappable,	
				532 *	
				533 * The difference, FPCR, and condition code are stored for each result.	
				534 *	
				535 *****	
00000586				537 LBFPNF DS 0H	BFP long non-finite values tests
00000586	9823 A000		00000000	538 LM R2,R3,0(R10)	Get count and address of minuend values
0000058A	9878 A008		00000008	539 LM R7,R8,8(R10)	Get address of result area and flag area.
0000058E	1222			540 LTR R2,R2	Any test cases?
00000590	078D			541 BZR R13	..No, return to caller
00000592	0DC0			542 BASR R12,0	Set top of loop
				543 *	
00000594	9845 A000		00000000	544 LM R4,R5,0(R10)	Get count and start of subtrahend values
				545 *	..which are the same as the minuends
00000598	0D60			546 BASR R6,0	Set top of inner loop
				547 *	
0000059A	6880 3000		00000000	548 LD FPR8,0(,R3)	Get long BFP minuend
0000059E	6810 5000		00000000	549 LD FPR1,0(,R5)	Get long BFP subtrahend
000005A2	B29D F30C		0000030C	550 LFPC FPCREGNT	Set exceptions non-trappable
000005A6	B31B 0081			551 SDBR FPR8,FPR1	Subtract long FPR1 from FPR8 RRE
000005AA	6080 7000		00000000	552 STD FPR8,0(,R7)	Store long BFP difference
000005AE	B29C 8000		00000000	553 STFPC 0(R8)	Store resulting FPCR flags and DXC
000005B2	B222 0000			554 IPM R0	Get condition code and program mask
000005B6	8800 001C		0000001C	555 SRL R0,28	Isolate CC in low order byte
000005BA	4200 8003		00000003	556 STC R0,3(,R8)	Save condition code in results table
				557 *	
000005BE	6880 3000		00000000	558 LD FPR8,0(,R3)	Get long BFP minuend
000005C2	6810 5000		00000000	559 LD FPR1,0(,R5)	Get long BFP subtrahend
000005C6	B29D F310		00000310	560 LFPC FPCREGTR	Set exceptions trappable
000005CA	B31B 0081			561 SDBR FPR8,FPR1	Subtract long subtrahend from FPR8 RRE
000005CE	6080 7008		00000008	562 STD FPR8,8(,R7)	Store long BFP remainder
000005D2	B29C 8004		00000004	563 STFPC 4(R8)	Store resulting FPCR flags and DXC
000005D6	B222 0000			564 IPM R0	Get condition code and program mask
000005DA	8800 001C		0000001C	565 SRL R0,28	Isolate CC in low order byte
000005DE	4200 8007		00000007	566 STC R0,7(,R8)	Save condition code in results table
				567 *	
000005E2	6880 3000		00000000	568 LD FPR8,0(,R3)	Get long BFP minuend
000005E6	B29D F30C		0000030C	569 LFPC FPCREGNT	Set exceptions non-trappable
000005EA	ED80 5000 001B		00000000	570 SDB FPR8,0(,R5)	Subtract long subtrahend from FPR8 RXE

LOC	OBJECT	CODE	ADDR1	ADDR2	STMT
					595 *****
					596 *
					597 * Perform Subtract using provided long BFP input pairs. This set of
					598 * tests triggers IEEE exceptions Overflow, Underflow, and Inexact and
					599 * collects non-trap and trap results.
					600 *
					601 * Four results are generated for each input: one RRE with all
					602 * exceptions non-trappable, a second RRE with all exceptions trappable,
					603 * a third RXE with all exceptions non-trappable, a fourth RXE with all
					604 * exceptions trappable,
					605 *
					606 * The difference, FPCR, and condition code are stored for each result.
					607 *
					608 *****
0000063C	9823	A000		00000000	610 LBFPP LM R2,R3,0(R10) Get count and address of test input values
00000640	9878	A008		00000008	611 LM R7,R8,8(R10) Get address of result area and flag area.
00000644	1222				612 LTR R2,R2 Any test cases?
00000646	078D				613 BZR R13 ..No, return to caller
00000648	0DC0				614 BASR R12,0 Set top of loop
					615 *
0000064A	B29D	F30C		0000030C	616 LFPC FPCREGNT Set exceptions non-trappable
0000064E	6880	3000		00000000	617 LD FPR8,0(,R3) Get long BFP minuend
00000652	6810	3008		00000008	618 LD FPR1,8(,R3) Get long BFP subtrahend
00000656	B31B	0081			619 SDBR FPR8,FPR1 Subtract long FPR1 from FPR8 RRE
0000065A	6080	7000		00000000	620 STD FPR8,0(,R7) Store long BFP difference
0000065E	B29C	8000		00000000	621 STFPC 0(R8) Store resulting FPCR flags and DXC
00000662	B222	0000			622 IPM R0 Get condition code and program mask
00000666	8800	001C		0000001C	623 SRL R0,28 Isolate CC in low order byte
0000066A	4200	8003		00000003	624 STC R0,3(,R8) Save condition code in results table
					625 *
0000066E	B29D	F310		00000310	626 LFPC FPCREGTR Set exceptions trappable
00000672	6880	3000		00000000	627 LD FPR8,0(,R3) Reload long BFP minuend
					628 * ..subtrahend is still in FPR1
00000676	B31B	0081			629 SDBR FPR8,FPR1 Subtract long FPR1 from FPR8 RRE
0000067A	6080	7008		00000008	630 STD FPR8,8(,R7) Store long BFP difference
0000067E	B29C	8004		00000004	631 STFPC 4(R8) Store resulting FPCR flags and DXC
00000682	B222	0000			632 IPM R0 Get condition code and program mask
00000686	8800	001C		0000001C	633 SRL R0,28 Isolate CC in low order byte
0000068A	4200	8007		00000007	634 STC R0,7(,R8) Save condition code in results table
					635 *
0000068E	B29D	F30C		0000030C	636 LFPC FPCREGNT Set exceptions non-trappable
00000692	6880	3000		00000000	637 LD FPR8,0(,R3) Reload long BFP minuend
00000696	ED80	3008	001B	00000008	638 SDB FPR8,8(,R3) Subtract long subtrahend from FPR8 RXE
0000069C	6080	7010		00000010	639 STD FPR8,16(,R7) Store long BFP difference
000006A0	B29C	8008		00000008	640 STFPC 8(R8) Store resulting FPCR flags and DXC
000006A4	B222	0000			641 IPM R0 Get condition code and program mask
000006A8	8800	001C		0000001C	642 SRL R0,28 Isolate CC in low order byte
000006AC	4200	800B		0000000B	643 STC R0,11(,R8) Save condition code in results table
					644 *
000006B0	B29D	F310		00000310	645 LFPC FPCREGTR Set exceptions trappable
000006B4	6880	3000		00000000	646 LD FPR8,0(,R3) Reload long BFP minuend
000006B8	ED80	3008	001B	00000008	647 SDB FPR8,8(,R3) Subtract long subtrahend from FPR8 RXE
000006BE	6080	7018		00000018	648 STD FPR8,24(,R7) Store long BFP difference
000006C2	B29C	800C		0000000C	649 STFPC 12(R8) Store resulting FPCR flags and DXC

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
000006C6	B222 0000			650	IPM	R0	Get condition code and program mask
000006CA	8800 001C		0000001C	651	SRL	R0,28	Isolate CC in low order byte
000006CE	4200 800F		0000000F	652	STC	R0,15(,R8)	Save condition code in results table
				653 *			
000006D2	4130 3010		00000010	654	LA	R3,2*8(,R3)	Point to next input value pair
000006D6	4170 7020		00000020	655	LA	R7,4*8(,R7)	Point to next quotient result pair
000006DA	4180 8010		00000010	656	LA	R8,4*4(,R8)	Point to next FPCR result area
000006DE	062C			657	BCTR	R2,R12	Convert next input value.
000006E0	07FD			658	BR	R13	All converted; return.

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				660 *****
				661 *
				662 * Perform Subtract using provided long BFP input pairs. This set of
				663 * tests exhaustively tests all rounding modes available for Subtract.
				664 * The rounding mode can only be specified in the FPC.
				665 *
				666 * All five FPC rounding modes are tested because the preceeding tests,
				667 * using rounding mode RNTE, do not often create results that require
				668 * rounding.
				669 *
				670 * Two results are generated for each input and rounding mode: one RRE
				671 * and one RXE. Traps are disabled for all rounding mode tests.
				672 *
				673 * The difference, FPCR, and condition code are stored for each result.
				674 *
				675 *****
000006E2	9823 A000		00000000	677 LBFPRM LM R2,R3,0(R10) Get count and address of test input values
000006E6	9878 A008		00000008	678 LM R7,R8,8(R10) Get address of result area and flag area.
000006EA	1222			679 LTR R2,R2 Any test cases?
000006EC	078D			680 BZR R13 ..No, return to caller
000006EE	1711			681 XR R1,R1 Zero register 1 for use in IC/STC/indexing
000006F0	0DC0			682 BASR R12,0 Set top of test case loop
				683
000006F2	4150 0005		00000005	684 LA R5,FPCMCT Get count of FPC modes to be tested
000006F6	0D90			685 BASR R9,0 Set top of rounding mode loop
				686 *
000006F8	4315 F8C3		000008C3	687 IC R1,FPCMODES-L'FPCMODES(R5) Get next FPC mode
				688 *
000006FC	B29D F30C		0000030C	689 LFPC FPCREGNT Set exceptions non-trappable, clear flags
00000700	B2B8 1000		00000000	690 SRNMB 0(R1) Set FPC Rounding Mode
00000704	6880 3000		00000000	691 LD FPR8,0(,R3) Get long BFP minuend
00000708	6810 3008		00000008	692 LD FPR1,8(,R3) Get long BFP subtrahend
0000070C	B31B 0081			693 SDBR FPR8,FPR1 Subtract long FPR1 from FPR8 RRE
00000710	6080 7000		00000000	694 STD FPR8,0(,R7) Store long BFP difference
00000714	B29C 8000		00000000	695 STFPC 0(R8) Store resulting FPCR flags and DXC
00000718	B222 0000			696 IPM R0 Get condition code and program mask
0000071C	8800 001C		0000001C	697 SRL R0,28 Isolate CC in low order byte
00000720	4200 8003		00000003	698 STC R0,3(,R8) Save condition code in results table
				699 *
00000724	B29D F30C		0000030C	700 LFPC FPCREGNT Set exceptions non-trappable, clear flags
00000728	B2B8 1000		00000000	701 SRNMB 0(R1) Set FPC Rounding Mode
0000072C	6880 3000		00000000	702 LD FPR8,0(,R3) Reload long BFP minuend
00000730	ED80 3008 001B		00000008	703 SDB FPR8,8(,R3) Subtract long subtrahend from FPR8 RXE
00000736	6080 7008		00000008	704 STD FPR8,8(,R7) Store long BFP difference
0000073A	B29C 8004		00000004	705 STFPC 4(R8) Store resulting FPCR flags and DXC
0000073E	B222 0000			706 IPM R0 Get condition code and program mask
00000742	8800 001C		0000001C	707 SRL R0,28 Isolate CC in low order byte
00000746	4200 8007		00000007	708 STC R0,7(,R8) Save condition code in results table
				709 *
0000074A	4170 7010		00000010	710 LA R7,2*8(,R7) Point to next difference result set
0000074E	4180 8008		00000008	711 LA R8,2*4(,R8) Point to next FPCR result area
				712 *
00000752	0659			713 BCTR R5,R9 Iterate to next FPC mode
				714 *

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				726 *****
				727 *
				728 * Perform Subtract using provided extended BFP inputs. This set of
				729 * tests checks NaN propagation, operations on values that are not
				730 * finite numbers, and other basic tests. This set generates results
				731 * that can be validated against Figure 19-13 on page 19-16 of
				732 * SA22-7832-10.
				733 *
				734 * That Figure has separate rows and columns for Normal and Tiny
				735 * operands. Although the results are effectively the same for Normal
				736 * and Tiny in any combination, the input data includes Normal and
				737 * Tiny values.
				738 *
				739 * Two results are generated for each input: one RRE with all
				740 * exceptions non-trappable, and a second RRE with all exceptions
				741 * trappable. Extended BFP Subtract does not have an RRE format.
				742 *
				743 * The difference, FPCR, and condition code are stored for each result.
				744 *
				745 *****
00000760				747 XBFPNF DS 0H BFP extended non-finite values tests
00000760	9823 A000		00000000	748 LM R2,R3,0(R10) Get count and address of minuend values
00000764	9878 A008		00000008	749 LM R7,R8,8(R10) Get address of result area and flag area.
00000768	1222			750 LTR R2,R2 Any test cases?
0000076A	078D			751 BZR R13 ..No, return to caller
0000076C	0DC0			752 BASR R12,0 Set top of loop
				753 *
0000076E	9845 A000		00000000	754 LM R4,R5,0(R10) Get count and start of subtrahend values
				755 * ..which are the same as the minuends
00000772	0D60			756 BASR R6,0 Set top of inner loop
				757 *
00000774	6880 3000		00000000	758 LD FPR8,0(,R3) Get extended BFP minuend part 1
00000778	68A0 3008		00000008	759 LD FPR10,8(,R3) Get extended BFP minuend part 2
0000077C	6810 5000		00000000	760 LD FPR1,0(,R5) Get extended BFP subtrahend part 1
00000780	6830 5008		00000008	761 LD FPR3,8(,R5) Get extended BFP subtrahend part 2
00000784	B29D F30C		0000030C	762 LFPC FPCREGNT Set exceptions non-trappable
00000788	B34B 0081			763 SXBR FPR8,FPR1 Subtract extended FPR1-3 from FPR8-10 RRE
0000078C	6080 7000		00000000	764 STD FPR8,0(,R7) Store extended BFP difference part 1
00000790	60A0 7008		00000008	765 STD FPR10,8(,R7) Store extended BFP difference part 2
00000794	B29C 8000		00000000	766 STFPC 0(R8) Store resulting FPCR flags and DXC
00000798	B222 0000			767 IPM R0 Get condition code and program mask
0000079C	8800 001C		0000001C	768 SRL R0,28 Isolate CC in low order byte
000007A0	4200 8003		00000003	769 STC R0,3(,R8) Save condition code in results table
				770 *
000007A4	68D0 3000		00000000	771 LD FPR13,0(,R3) Get extended BFP minuend part 1
000007A8	68F0 3008		00000008	772 LD FPR15,8(,R3) Get extended BFP minuend part 2
000007AC	6810 5000		00000000	773 LD FPR1,0(,R5) Get extended BFP subtrahend part 1
000007B0	6830 5008		00000008	774 LD FPR3,8(,R5) Get extended BFP subtrahend part 2
000007B4	B29D F310		00000310	775 LFPC FPCREGTR Set exceptions trappable
000007B8	B34B 00D1			776 SXBR FPR13,FPR1 Subtract extended FPR1-3 from FPR13-15 RRE
000007BC	60D0 7010		00000010	777 STD FPR13,16(,R7) Store extended BFP difference part 1
000007C0	60F0 7018		00000018	778 STD FPR15,24(,R7) Store extended BFP difference part 2
000007C4	B29C 8004		00000004	779 STFPC 4(R8) Store resulting FPCR flags and DXC
000007C8	B222 0000			780 IPM R0 Get condition code and program mask

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				793 *****
				794 *
				795 * Perform Subtract using provided extended BFP input pairs. This set
				796 * of tests triggers IEEE exceptions Overflow, Underflow, and Inexact
				797 * and collects results when the exceptions do not result in a trap and
				798 * when they do.
				799 *
				800 * Two results are generated for each input: one RRE with all
				801 * exceptions non-trappable and a second RRE with all exceptions
				802 * trappable. There is no RXE format for Subtract in extended
				803 * precision.
				804 *
				805 * The difference, FPCR, and condition code are stored for each result.
				806 *
				807 *****
000007EA	9823 A000		00000000	809 XBFPF LM R2,R3,0(R10) Get count and address of test input values
000007EE	9878 A008		00000008	810 LM R7,R8,8(R10) Get address of result area and flag area.
000007F2	1222			811 LTR R2,R2 Any test cases?
000007F4	078D			812 BZR R13 ..No, return to caller
000007F6	0DC0			813 BASR R12,0 Set top of loop
				814 *
000007F8	B29D F30C		0000030C	815 LFPC FPCREGNT Set exceptions non-trappable
000007FC	68D0 3000		00000000	816 LD FPR13,0(,R3) Get extended BFP minuend part 1
00000800	68F0 3008		00000008	817 LD FPR15,8(,R3) Get extended BFP minuend part 2
00000804	6810 3010		00000010	818 LD FPR1,16(,R3) Get extended BFP subtrahend part 1
00000808	6830 3018		00000018	819 LD FPR3,24(,R3) Get extended BFP subtrahend part 2
0000080C	B34B 00D1			820 SXBR FPR13,FPR1 Subtract extended FPR1-3 from FPR13-15 RRE
00000810	60D0 7000		00000000	821 STD FPR13,0(,R7) Store extended BFP difference part 1
00000814	60F0 7008		00000008	822 STD FPR15,8(,R7) Store extended BFP difference part 2
00000818	B29C 8000		00000000	823 STFPC 0(R8) Store resulting FPCR flags and DXC
0000081C	B222 0000			824 IPM R0 Get condition code and program mask
00000820	8800 001C		0000001C	825 SRL R0,28 Isolate CC in low order byte
00000824	4200 8003		00000003	826 STC R0,3(,R8) Save condition code in results table
				827 *
00000828	B29D F310		00000310	828 LFPC FPCREGTR Set exceptions trappable
0000082C	68D0 3000		00000000	829 LD FPR13,0(,R3) Reload extended BFP minuend part 1
00000830	68F0 3008		00000008	830 LD FPR15,8(,R3) Reload extended BFP minuend part 2
				831 *
				832 SXBR FPR13,FPR1 ..subtrahend is still in FPR1-FPR3
00000834	B34B 00D1			833 STD FPR13,16(,R7) Subtract extended FPR1-3 from FPR13-15 RRE
00000838	60D0 7010		00000010	834 STD FPR15,24(,R7) Store extended BFP difference part 1
0000083C	60F0 7018		00000018	835 STFPC 4(R8) Store extended BFP difference part 2
00000840	B29C 8004		00000004	836 IPM R0 Store resulting FPCR flags and DXC
00000844	B222 0000			837 SRL R0,28 Get condition code and program mask
00000848	8800 001C		0000001C	838 STC R0,7(,R8) Isolate CC in low order byte
0000084C	4200 8007		00000007	839 * Save condition code in results table
				840 LA R3,32(,R3) Point to next input value pair
00000850	4130 3020		00000020	841 LA R7,32(,R7) Point to next quotient result pair
00000854	4170 7020		00000020	842 LA R8,16(,R8) Point to next FPCR result area
00000858	4180 8010		00000010	843 BCTR R2,R12 Convert next input value.
0000085C	062C			844 *
0000085E	07FD			845 BR R13 All converted; return.

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				847 *****
				848 *
				849 * Perform Subtract using provided extended BFP input pairs. This set
				850 * of tests exhaustively tests all rounding modes available for
				851 * Subtract. The rounding mode can only be specified in the FPC.
				852 *
				853 * All five FPC rounding modes are tested because the preceeding tests,
				854 * using rounding mode RNTE, do not often create results that require
				855 * rounding.
				856 *
				857 * Two results are generated for each input and rounding mode: one RRE
				858 * and one RXE. Traps are disabled for all rounding mode tests.
				859 *
				860 * The difference, FPCR, and condition code are stored for each result.
				861 *
				862 *****
00000860	9823 A000		00000000	864 XBFPRM LM R2,R3,0(R10) Get count and address of test input values
00000864	9878 A008		00000008	865 LM R7,R8,8(R10) Get address of result area and flag area.
00000868	1222			866 LTR R2,R2 Any test cases?
0000086A	078D			867 BZR R13 ..No, return to caller
0000086C	1711			868 XR R1,R1 Zero register 1 for use in IC/STC/indexing
0000086E	0DC0			869 BASR R12,0 Set top of test case loop
				870
00000870	4150 0005		00000005	871 LA R5,FPCMCT Get count of FPC modes to be tested
00000874	0D90			872 BASR R9,0 Set top of rounding mode loop
				873 *
00000876	4315 F8C3		000008C3	874 IC R1,FPCMODES-L'FPCMODES(R5) Get next FPC mode
				875 *
0000087A	B29D F30C		0000030C	876 LFPC FPCREGNT Set exceptions non-trappable, clear flags
0000087E	B2B8 1000		00000000	877 SRNMB 0(R1) Set FPC Rounding Mode
00000882	68D0 3000		00000000	878 LD FPR13,0(,R3) Get extended BFP minuend part 1
00000886	68F0 3008		00000008	879 LD FPR15,8(,R3) Get extended BFP minuend part 2
0000088A	6810 3010		00000010	880 LD FPR1,16(,R3) Get extended BFP subtrahend part 1
0000088E	6830 3018		00000018	881 LD FPR3,24(,R3) Get extended BFP subtrahend part 2
00000892	B34B 00D1			882 SXBR FPR13,FPR1 Subtract extended FPR1-3 from FPR13-15 RRE
00000896	60D0 7000		00000000	883 STD FPR13,0(,R7) Store extended BFP difference part 1
0000089A	60F0 7008		00000008	884 STD FPR15,8(,R7) Store extended BFP difference part 2
0000089E	B29C 8000		00000000	885 STFPC 0(R8) Store resulting FPCR flags and DXC
000008A2	B222 0000			886 IPM R0 Get condition code and program mask
000008A6	8800 001C		0000001C	887 SRL R0,28 Isolate CC in low order byte
000008AA	4200 8003		00000003	888 STC R0,3(,R8) Save condition code in results table
				889 *
000008AE	4170 7010		00000010	890 LA R7,16(,R7) Point to next difference result set
000008B2	4180 8004		00000004	891 LA R8,4(,R8) Point to next FPCR result area
				892 *
000008B6	0659			893 BCTR R5,R9 Iterate to next FPC mode
				894 *
				895 * End of FPC modes to be tested. Advance to next test case. We will
				896 * skip eight bytes of FPCR result area so that each set of five result
				897 * FPCR contents pairs starts at a memory address ending in zero for the
				898 * convenience of memory dump review.
				899 *
000008B8	4130 3020		00000020	900 LA R3,2*16(,R3) Point to next input value pair
000008BC	4180 800C		0000000C	901 LA R8,12(,R8) Skip to start of next FPCR result area

LOC	OBJECT	CODE	ADDR1	ADDR2	STMT
					906 *****
					907 *
					908 * Table of FPC rounding modes to test difference rounding modes.
					909 *
					910 * The Set BFP Rounding Mode does allow specification of the FPC
					911 * rounding mode as an address, so we shall index into a table of
					912 * BFP rounding modes without bothering with Execute.
					913 *
					914 *****
					916 *
					917 * Rounding modes that may be set in the FPCR. The FPCR controls
					918 * rounding of the difference.
					919 *
					920 * These are indexed directly by the loop counter, which counts down.
					921 * So the modes are listed in reverse order here.
					922 *
000008C4					923 FPCMODES DS 0C
000008C4	07				924 DC AL1(7) RFS, Round for shorter precision
000008C5	03				925 DC AL1(3) RM, Round to -infinity
000008C6	02				926 DC AL1(2) RP, Round to +infinity
000008C7	01				927 DC AL1(1) RZ, Round to zero
000008C8	00				928 DC AL1(0) RNTE, Round to Nearest, ties to even
			00000005	00000001	929 FPCMCT EQU *-FPCMODES Count of FPC Modes to be tested
					930 *

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				932 *****
				933 *
				934 * Short BFP test data sets for Subtract testing.
				935 *
				936 * The first test data set is used for tests of basic functionality,
				937 * NaN propagation, and results from operations involving other than
				938 * finite numbers.
				939 *
				940 * The second test data set is used for testing boundary conditions
				941 * using two finite non-zero values. Each possible condition code
				942 * and type of result (normal, scaled, etc) is created by members of
				943 * this test data set.
				944 *
				945 * The third test data set is used for exhaustive testing of final
				946 * results across the five rounding modes available for the Subtract
				947 * instruction.
				948 *
				949 *****
				951 *****
				952 *
				953 * First input test data set, to test operations using non-finite or
				954 * zero inputs. Member values chosen to validate Figure 19-13 on page
				955 * 19-16 of SA22-7832-10. Each value in this table is tested against
				956 * every other value in the table. Ten entries means 100 result sets.
				957 *
				958 *****
000008CC				960 SBFPNFIN DS 0F Inputs for short BFP non-finite tests
000008CC	FF800000			961 DC X'FF800000' -inf
000008D0	C0000000			962 DC X'C0000000' -2.0
000008D4	80010000			963 DC X'80010000' -Dnice
000008D8	80000000			964 DC X'80000000' -0
000008DC	00000000			965 DC X'00000000' +0
000008E0	00010000			966 DC X'00010000' -Dnice
000008E4	40000000			967 DC X'40000000' +2.0
000008E8	7F800000			968 DC X'7F800000' +inf
000008EC	FFCB0000			969 DC X'FFCB0000' -QNaN
000008F0	7F8A0000			970 DC X'7F8A0000' +SNaN
	0000000A	00000001		971 SBFPNFCT EQU (*-SBFPNFIN)/4 Count of short BFP in list
				973 *****
				974 *
				975 * Second input test data set. These are finite pairs intended to
				976 * trigger overflow, underflow, and inexact exceptions. Each pair is
				977 * added twice, once non-trappable and once trappable. Trappable
				978 * overflow or underflow yields a scaled result. Trappable inexact
				979 * will show whether the Incremented DXC code is returned.
				980 *
				981 * The following test cases are required:
				982 * 1. Overflow
				983 * 2. Underflow - normal inputs

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				984 * 3. Underflow - subnormal inputs
				985 * 4. Normal - from subnormal inputs
				986 * 5. Inexact - incremented
				987 * 6. Inexact - truncated
				988 *
				989 *****
000008F4				991 SBFPIN DS 0F Inputs for short BFP finite tests
				992 *
				993 * Overflow on subtraction
				994 *
000008F4	7F7FFFFFFF			995 DC X'7F7FFFFFFF' +Nmax
000008F8	FF7FFFFFFF			996 DC X'FF7FFFFFFF' -Nmax
				997 *
				998 * Underflow from difference of normals. We will subtract a small
				999 * normal from a slightly larger small normal to generate a subnormal.
				1000 *
000008FC	00FFFFFFF			1001 DC X'00FFFFFFF' Very small normal number
00000900	00800000			1002 DC X'00800000' Smaller normal
				1003 *
				1004 * Underflow from difference of subnormals.
				1005 *
00000904	00040000			1006 DC X'00040000' Subnormal, < +Dmax
00000908	00000F0F			1007 DC X'00000F0F' Smaller subnormal
				1008 *
				1009 * Normal result from difference of subnormals.
				1010 * The result will be greater than +Nmin
				1011 *
0000090C	007FFFFFFF			1012 DC X'007FFFFFFF' +Dmax
00000910	80000001			1013 DC X'80000001' -Dmin, result will be +Nmin
				1014 *
				1015 * Subtract a value from 1.0 such that the added digits are to the right
				1016 * of the right-most bit in the stored significand. The result will be
				1017 * inexact, and incremented will be determined by the value of the
				1018 * bits in the subtrahend.
				1019 *
00000914	3F800000			1020 DC X'3F800000' Minuend +1, aka 1.0b0
00000918	32800000			1021 DC X'32800000' Subtrahend 1.b-26
				1022 *..Above subtrahend is 1.490116119384765625E-8
				1023 *..nearest is away from zero, incremented.
				1024 *
0000091C	3F800000			1025 DC X'3F800000' Minuend +1, aka 1.0b0
00000920	33100000			1026 DC X'33100000' Subtrahend 1.001b-25
				1027 *..Above subtrahend is 3.35276126861572265625E-8
				1028 *..nearest is toward zero, truncated
				1029 *
	00000006	00000001		1030 SBFPCT EQU (*-SBFPIN)/4/2 Count of short BFP in list
				1032 *****
				1033 *
				1034 * Third input test data set. These are finite pairs intended to
				1035 * test all combinations of rounding mode for the difference and the
				1036 * remainder. Values are chosen to create a requirement to round

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				1037 * to the target precision after the computation and to generate
				1038 * varying results depending on the rounding mode in the FPCR.
				1039 *
				1040 * The result set will have cases that represent each of the following
				1041 *
				1042 * 1. Positive, nearest magnitude is toward zero.
				1043 * 2. Negative, nearest magnitude is toward zero.
				1044 * 3. Positive, nearest magnitude is away from zero.
				1045 * 4. Negative, nearest magnitude is away from zero.
				1046 * 5. Positive, tie, nearest even has greater magnitude
				1047 * 6. Negative, tie, nearest even has greater magnitude
				1048 * 7. Positive, tie, nearest even has lower magnitude
				1049 * 8. Negative, tie, nearest even has lower magnitude
				1050 *
				1051 * Round For Shorter precision correctness can be determined from the
				1052 * above test cases.
				1053 *
				1054 *****
00000924				1056 SBFPINRM DS 0F Inputs for short BFP rounding testing
				1057 *
				1058 * Subtract a value from 1.0 such that the added digits are to the right
				1059 * of the right-most bit in the stored significand. The result will be
				1060 * inexact, and incremented will be determined by the value of the
				1061 * bits in the subtrahend.
				1062 *
00000924	3F800000			1063 DC X'3F800000' Minuend +1, aka 1.0b0
00000928	33100000			1064 DC X'33100000' Subtrahend 1.001b-25
0000092C	BF800000			1065 DC X'BF800000' Minuend -1, aka -1.0b0
00000930	B3100000			1066 DC X'B3100000' Subtrahend 1.001b-25
				1067 *..Above subtrahend is 3.35276126861572265625E-8
				1068 *..nearest is toward zero, truncated
				1069 *
00000934	3F800000			1070 DC X'3F800000' Minuend +1, aka +1.0b0
00000938	32800000			1071 DC X'32800000' Subtrahend 1.b-26
0000093C	BF800000			1072 DC X'BF800000' Minuend -1, aka -1.0b0
00000940	B2800000			1073 DC X'B2800000' Subtrahend -1.b-26
				1074 *..Above subtrahend is 1.490116119384765625E-8
				1075 *..nearest is away from zero, incremented.
				1076 *
00000944	3F800000			1077 DC X'3F800000' Minuend +1, aka +1.0b0
00000948	33C00000			1078 DC X'33C00000' Subtrahend +1.1b-24
0000094C	BF800000			1079 DC X'BF800000' Minuend -1, aka -1.0b0
00000950	B3C00000			1080 DC X'B3C00000' Subtrahend -1.1b-24
				1081 *..Above subtrahend is 8.94069671630859375E-8
				1082 *..nearest is a tie, nearest even has lower magnitude
				1083 *
00000954	3F800000			1084 DC X'3F800000' Minuend +1, aka +1.0b0
00000958	33000000			1085 DC X'33000000' Subtrahend +1.0b-25
0000095C	BF800000			1086 DC X'BF800000' Minuend -1, aka -1.0b0
00000960	B3000000			1087 DC X'B3000000' Subtrahend -1.0b-25
				1088 *..Above subtrahend is 2.98023223876953125E-8
				1089 *..nearest is a tie, nearest even has greater magnitude
				1090 *
	00000008	00000001		1091 SBFPRMCT EQU (*-SBFPINRM)/4/2 Count of short BFP rounding tests

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				1093 *****
				1094 *
				1095 * Long BFP test data sets for Add testing.
				1096 *
				1097 * The first test data set is used for tests of basic functionality,
				1098 * NaN propagation, and results from operations involving other than
				1099 * finite numbers.
				1100 *
				1101 * The second test data set is used for testing boundary conditions
				1102 * using two finite non-zero values. Each possible condition code
				1103 * and type of result (normal, scaled, etc) is created by members of
				1104 * this test data set.
				1105 *
				1106 * The third test data set is used for exhaustive testing of final
				1107 * results across the five rounding modes available for the Add
				1108 * instruction.
				1109 *
				1110 *****
				1112 *****
				1113 *
				1114 * First input test data set, to test operations using non-finite or
				1115 * zero inputs. Member values chosen to validate Figure 19-13 on page
				1116 * 19-16 of SA22-7832-10. Each value in this table is tested against
				1117 * every other value in the table. Ten entries means 100 result sets.
				1118 *
				1119 *****
00000964				1121 LBFPNFIN DS 0F Inputs for long BFP testing
00000964	FFF00000	00000000		1122 DC X'FFF0000000000000' -inf
0000096C	C0000000	00000000		1123 DC X'C000000000000000' -2.0
00000974	80010000	00000000		1124 DC X'8001000000000000' -Dnice
0000097C	80000000	00000000		1125 DC X'8000000000000000' -0
00000984	00000000	00000000		1126 DC X'0000000000000000' +0
0000098C	00010000	00000000		1127 DC X'0001000000000000' +Dnice
00000994	40000000	00000000		1128 DC X'4000000000000000' +2.0
0000099C	7FF00000	00000000		1129 DC X'7FF0000000000000' +inf
000009A4	FFF8B000	00000000		1130 DC X'FFF8B00000000000' -QNaN
000009AC	7FF0A000	00000000		1131 DC X'7FF0A00000000000' +SNaN
	0000000A	00000001		1132 LBFPNFCT EQU (*-LBFPNFIN)/8 Count of long BFP in list
				1134 *****
				1135 *
				1136 * Second input test data set. These are finite pairs intended to
				1137 * trigger overflow, underflow, and inexact exceptions. Each pair is
				1138 * added twice, once non-trappable and once trappable. Trappable
				1139 * overflow or underflow yields a scaled result. Trappable inexact
				1140 * will show whether the Incremented DXC code is returned.
				1141 *
				1142 * The following test cases are required:
				1143 * 1. Overflow
				1144 * 2. Underflow - normal inputs

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				1145 * 3. Underflow - subnormal inputs
				1146 * 4. Normal - from subnormal inputs
				1147 * 5. Inexact - incremented
				1148 * 6. Inexact - truncated
				1149 *
				1150 *****
000009B8				1152 LBFPIN DS 0D Inputs for long BFP finite tests
				1153 *
				1154 * Overflow on subtraction
				1155 *
000009B8	7FFFFFFF FFFFFFFF			1156 DC X'7FFFFFFF' +Nmax
000009C0	FFFFFFFF FFFFFFFF			1157 DC X'FFFFFFFF' +Nmax
				1158 *
				1159 * Underflow from difference of normals. We wil subtract a small
				1160 * normal from a slightly larger normal to generate a subnormal.
				1161 *
000009C8	001FFFFF FFFFFFFF			1162 DC X'001FFFFF' Very small normal number
000009D0	00100000 00000000			1163 DC X'00100000' Smaller normal negative
				1164 *
				1165 * Underflow from difference of subnormals.
				1166 *
000009D8	00080000 00000000			1167 DC X'00080000' Subnormal, < +Dmax
000009E0	0000F0F0 00000000			1168 DC X'0000F0F0' Smaller subnormal
				1169 *
				1170 * Normal result from difference of subnormals.
				1171 * The result will be greater than +Nmin
				1172 *
000009E8	000FFFFF FFFFFFFF			1173 DC X'000FFFFF' +Dmax
000009F0	80000000 00000001			1174 DC X'80000000' +Dmin, result will be +Nmin
				1175 *
				1176 * Subtract a value from 1.0 such that the added digits are to the right
				1177 * of the right-most bit in the stored significand. The result will be
				1178 * inexact, and incremented will be determined by the value of the
				1179 * bits in the subtrahend.
				1180 *
000009F8	3FF00000 00000000			1181 DC X'3FF00000' Minuend +1, aka 1.0b0
00000A00	3C800000 00000000			1182 DC X'3C800000' Subtrahend 1.0b-55
				1183 *..Above subtrahend is 2.7755756156289135105907917022705078125E-17
				1184 *..nearest is away from zero, incremented.
				1185 *
00000A08	3FF00000 00000000			1186 DC X'3FF00000' Minuend +1, aka 1.0b0
00000A10	3C920000 00000000			1187 DC X'3C920000' Subtrahend +1.001b-54
				1188 *..Above subtrahend is 6.2450045135165055398829281330108642578125E-17
				1189 *..nearest is toward zero, truncated.
				1190 *
	00000006	00000001		1191 LBFPCT EQU (*-LBFPIN)/8/2 Count of long BFP in list
				1193 *****
				1194 *
				1195 * Third input test data set. These are finite pairs intended to
				1196 * test all combinations of rounding mode for the difference and the
				1197 * remainder. Values are chosen to create a requirement to round

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				1198 * to the target precision after the computation and to generate
				1199 * varying results depending on the rounding mode in the FPCR.
				1200 *
				1201 * The result set will have cases that represent each of the following
				1202 *
				1203 * 1. Positive, nearest magnitude is toward zero.
				1204 * 2. Negative, nearest magnitude is toward zero.
				1205 * 3. Positive, nearest magnitude is away from zero.
				1206 * 4. Negative, nearest magnitude is away from zero.
				1207 * 5. Positive, tie, nearest even has greater magnitude
				1208 * 6. Negative, tie, nearest even has greater magnitude
				1209 * 7. Positive, tie, nearest even has lower magnitude
				1210 * 8. Negative, tie, nearest even has lower magnitude
				1211 *
				1212 * Round For Shorter precision correctness can be determined from the
				1213 * above test cases.
				1214 *
				1215 *****
00000A18				1217 LBFPINRM DS 0F
				1218 *
				1219 * Subtract a value from 1.0 such that the added digits are to the right
				1220 * of the right-most bit in the stored significand. The result will be
				1221 * inexact, and incremented will be determined by the value of the
				1222 * bits in the subtrahend.
				1223 *
00000A18	3FF00000	00000000		1224 DC X'3FF0000000000000' Minuend +1, aka +1.0b0
00000A20	3C920000	00000000		1225 DC X'3C92000000000000' Subtrahend +1.001b-54
00000A28	BFF00000	00000000		1226 DC X'BFF00000000000000' Minuend -1, aka -1.0b0
00000A30	BC920000	00000000		1227 DC X'BC92000000000000' Subtrahend +1.001b-54
				1228 *..Above subtrahend is 6.2450045135165055398829281330108642578125E-17
				1229 *.. ...30859375E-16,nearest is toward zero, truncated.
				1230 *
00000A38	3FF00000	00000000		1231 DC X'3FF0000000000000' Minuend +1, aka +1.0b0
00000A40	3C800000	00000000		1232 DC X'3C80000000000000' Subtrahend 1.0b-55
00000A48	BFF00000	00000000		1233 DC X'BFF00000000000000' Minuend -1, aka -1.0b0
00000A50	BC800000	00000000		1234 DC X'BC80000000000000' Subtrahend 1.0b-55
				1235 *..Above subtrahend is 2.77555756156289135105907917022705078125E-17
				1236 *..nearest is away from zero, incremented.
				1237 *
00000A58	3FF00000	00000000		1238 DC X'3FF0000000000000' Minuend +1, aka +1.0b0
00000A60	3CA80000	00000000		1239 DC X'3CA8000000000000' Subtrahend +1.1b-53
00000A68	BFF00000	00000000		1240 DC X'BFF00000000000000' Minuend -1, aka -1.0b0
00000A70	BCA80000	00000000		1241 DC X'BCA8000000000000' Subtrahend -1.1b-53
				1242 *..Above subtrahend is 1.66533453693773481063544750213623046875E-16
				1243 *..nearest is a tie, nearest even has lower magnitude
				1244 *
00000A78	3FF00000	00000000		1245 DC X'3FF0000000000000' Minuend +1, aka +1.0b0
00000A80	3C900000	00000000		1246 DC X'3C90000000000000' Subtrahend +1.0b-54
00000A88	BFF00000	00000000		1247 DC X'BFF00000000000000' Minuend -1, aka -1.0b0
00000A90	BC900000	00000000		1248 DC X'BC90000000000000' Subtrahend -1.0b-54
				1249 *..Above subtrahend is 5.5511151231257827021181583404541015625E-17
				1250 *..nearest is a tie, nearest even has greater magnitude
				1251 *
	00000008	00000001		1252 LBFP RMCT EQU (*-LBFPINRM)/8/2 Count of long BFP rounding tests

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				1254 *****
				1255 *
				1256 * Extended BFP test data sets for Add testing.
				1257 *
				1258 * The first test data set is used for tests of basic functionality,
				1259 * NaN propagation, and results from operations involving other than
				1260 * finite numbers.
				1261 *
				1262 * The second test data set is used for testing boundary conditions
				1263 * using two finite non-zero values. Each possible condition code
				1264 * and type of result (normal, scaled, etc) is created by members of
				1265 * this test data set.
				1266 *
				1267 * The third test data set is used for exhaustive testing of final
				1268 * results across the five rounding modes available for the Add
				1269 * instruction.
				1270 *
				1271 *****
				1273 *****
				1274 *
				1275 * First input test data set, to test operations using non-finite or
				1276 * zero inputs. Member values chosen to validate Figure 19-13 on page
				1277 * 19-16 of SA22-7832-10. Each value in this table is tested against
				1278 * every other value in the table. Ten entries means 100 result sets.
				1279 *
				1280 *****
00000A98				1282 XBFPNFIN DS 0F Inputs for extended BFP testing
00000A98	FFFF0000	00000000		1283 DC X'FFFF0000000000000000000000000000' -inf
00000AA8	C0000000	00000000		1284 DC X'C0000000000000000000000000000000' -2.0
00000AB8	80001000	00000000		1285 DC X'80001000000000000000000000000000' -Dnice
00000AC8	80000000	00000000		1286 DC X'80000000000000000000000000000000' -0
00000AD8	00000000	00000000		1287 DC X'00000000000000000000000000000000' +0
00000AE8	00001000	00000000		1288 DC X'00001000000000000000000000000000' +Dnice
00000AF8	40000000	00000000		1289 DC X'40000000000000000000000000000000' +2.0
00000B08	7FFF0000	00000000		1290 DC X'7FFF0000000000000000000000000000' +inf
00000B18	FFFF8B00	00000000		1291 DC X'FFFF8B00000000000000000000000000' -QNaN
00000B28	7FFF0A00	00000000		1292 DC X'7FFF0A00000000000000000000000000' +SNaN
	0000000A	00000001		1293 XBFPNFCT EQU (*-XBFPNFIN)/16 Count of extended BFP in list
				1295 *****
				1296 *
				1297 * Second input test data set. These are finite pairs intended to
				1298 * trigger overflow, underflow, and inexact exceptions. Each pair is
				1299 * added twice, once non-trappable and once trappable. Trappable
				1300 * overflow or underflow yields a scaled result. Trappable inexact
				1301 * will show whether the Incremented DXC code is returned.
				1302 *
				1303 * The following test cases are required:
				1304 * The following test cases are required:
				1305 * 1. Overflow

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				1306 * 2. Underflow - normal inputs
				1307 * 3. Underflow - subnormal inputs
				1308 * 4. Normal - from subnormal inputs
				1309 * 5. Inexact - incremented
				1310 * 6. Inexact - truncated
				1311 *
				1312 *****
00000B38				1314 XBFPIN DS 0F Inputs for extended BFP finite tests
				1315 *
				1316 * Overflow on subtraction
				1317 *
00000B38	7FFFFFFF FFFFFFFF			1318 DC X'7FFFFFFF' +Nmax
00000B48	FFFFFFFF FFFFFFFF			1319 DC X'FFFFFFFF' +Nmax
				1320 *
				1321 * Underflow from difference of normals. We will subtract a small
				1322 * normal from a slightly larger normal to generate a subnormal.
				1323 *
00000B58	0001FFFF FFFFFFFF			1324 DC X'0001FFFF' Very small normal
00000B68	00010000 00000000			1325 DC X'00010000' Smaller normal
				1326 *
				1327 * Underflow from difference of subnormals.
				1328 *
00000B78	00008000 00000000			1329 DC X'00008000' Subnormal, < +Dmax
00000B88	00000F0F 00000000			1330 DC X'00000F0F' Smaller subnormal
				1331 *
				1332 * Normal result from difference of subnormals.
				1333 * The result will be greater than +Nmin
				1334 *
00000B98	0000FFFF FFFFFFFF			1335 DC X'0000FFFF' +Dmax
00000BA8	80000000 00000000			1336 DC X'80000000' -Dmin
				1337 * ...result will be +Nmin
				1338 *
				1339 * Subtract a value from 1.0 such that the added digits are to the right
				1340 * of the right-most bit in the stored significand. The result will be
				1341 * inexact, and incremented will be determined by the value of the
				1342 * bits in the subtrahend.
				1343 *
00000BB8	3FFF0000 00000000			1344 DC X'3FFF0000' +1, aka 1.0b0
00000BC8	3F8C0000 00000000			1345 DC X'3F8C0000' 1.0b-115
				1346 * ..Above subtrahend is 2.407412430484044816319972428231159148172627...
				1347 * ...06026923524404992349445819854736328125E-35
				1348 * ..nearest is away from zero, incremented.
				1349 *
00000BD8	3FFF0000 00000000			1350 DC X'3FFF0000' +1, aka 1.0b0
00000BE8	3F8D2000 00000000			1351 DC X'3F8D2000' 1.001b-114
				1352 * ..Above subtrahend is 5.416677968589100836719937963520108083388410...
				1353 * ...8856057792991123278625309467315673828125E-35
				1354 * ..nearest is toward zero, truncated
				1355 *
	00000006 00000001			1356 XBFPCT EQU (*-XBFPIN)/16/2 Count of extended BFP in list
				1358 *****

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
				1423 *****	
				1424 *	ACTUAL results saved here
				1425 *****	
				1426 *	
				1427 *	Locations for ACTUAL results
				1428 *	
		00001000	00000001	1429 SBFPNFOT EQU	Short non-finite BFP results
				1430 *	..room for 110 tests, 100 used
		00001700	00000001	1431 SBFPNFFL EQU	FPCR flags and DXC from short BFP
				1432 *	..room for 110 tests, 100 used
				1433 *	
		00001E00	00000001	1434 SBFPOUT EQU	Short BFP finite results
				1435 *	..room for 16 tests, 6 used
		00001F00	00000001	1436 SBFPFLGS EQU	FPCR flags and DXC from short BFP
				1437 *	..room for 16 tests, 6 used
				1438 *	
		00002000	00000001	1439 SBFPRMO EQU	Short BFP rounding mode test results
				1440 *	..Room for 16, 8 used.
		00002300	00000001	1441 SBFPRMOF EQU	Short BFP rounding mode FPCR results
				1442 *	..Room for 16, 8 used.
				1443 *	..next location starts at X'2500'
				1444 *	
		00004000	00000001	1445 LBFPNFOT EQU	Long non-finite BFP results
				1446 *	..room for 100 tests, 100 used
		00004D00	00000001	1447 LBFPNFFL EQU	FPCR flags and DXC from long BFP
				1448 *	..room for 100 tests, 100 used
				1449 *	
		00005400	00000001	1450 LBFPOUT EQU	Long BFP finite results
				1451 *	..room for 16 tests, 6 used
		00005600	00000001	1452 LBFPFLGS EQU	FPCR flags and DXC from long BFP
				1453 *	..room for 16 tests, 6 used
				1454 *	
		00005700	00000001	1455 LBFPRMO EQU	Long BFP rounding mode test results
				1456 *	..Room for 16, 8 used.
		00005C00	00000001	1457 LBFPRMOF EQU	Long BFP rounding mode FPCR results
				1458 *	..Room for 16, 8 used.
				1459 *	..next location starts at X'5E00'
				1460 *	
		00008000	00000001	1461 XBFPNFOT EQU	Extended non-finite BFP results
				1462 *	..room for 100 tests, 100 used
		00008D00	00000001	1463 XBFPNFFL EQU	FPCR flags and DXC from ext'd BFP
				1464 *	..room for 100 tests, 100 used
				1465 *	
		00009400	00000001	1466 XBFPOUT EQU	Extended BFP finite results
				1467 *	..room for 16 tests, 6 used
		00009600	00000001	1468 XBFPFLGS EQU	FPCR flags and DXC from ext'd BFP
				1469 *	..room for 16 tests, 6 used
				1470 *	
		00009700	00000001	1471 XBFPRMO EQU	Ext'd BFP rounding mode test results
				1472 *	..Room for 16, 8 used.
		00009C00	00000001	1473 XBFPRMOF EQU	Ext'd BFP rounding mode FPCR results
				1474 *	..Room for 16, 8 used.
				1475 *	..next location starts at X'9E00'
				1476 *	

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				1478 *****
				1479 * EXPECTED results
				1480 *****
				1481 *
00000CF8		00000CF8	0000B000	1482 ORG STRTLABL+X'B000' (past end of actual results)
				1483 *
		0000B000	00000001	1484 SBFPNFOT_GOOD EQU *
0000B000	E2C5C2D9	61E2C5C2		1485 DC CL48' SEBR/SEB NF -inf/-inf'
0000B030	7FC00000	FF800000		1486 DC XL16' 7FC00000FF8000007FC00000FF800000'
0000B040	E2C5C2D9	61E2C5C2		1487 DC CL48' SEBR/SEB NF -inf/-2.0'
0000B070	FF800000	FF800000		1488 DC XL16' FF800000FF800000FF800000FF800000'
0000B080	E2C5C2D9	61E2C5C2		1489 DC CL48' SEBR/SEB NF -inf/-Dnice'
0000B0B0	FF800000	FF800000		1490 DC XL16' FF800000FF800000FF800000FF800000'
0000B0C0	E2C5C2D9	61E2C5C2		1491 DC CL48' SEBR/SEB NF -inf/-0'
0000B0F0	FF800000	FF800000		1492 DC XL16' FF800000FF800000FF800000FF800000'
0000B100	E2C5C2D9	61E2C5C2		1493 DC CL48' SEBR/SEB NF -inf/+0'
0000B130	FF800000	FF800000		1494 DC XL16' FF800000FF800000FF800000FF800000'
0000B140	E2C5C2D9	61E2C5C2		1495 DC CL48' SEBR/SEB NF -inf/+Dnice'
0000B170	FF800000	FF800000		1496 DC XL16' FF800000FF800000FF800000FF800000'
0000B180	E2C5C2D9	61E2C5C2		1497 DC CL48' SEBR/SEB NF -inf/+2.0'
0000B1B0	FF800000	FF800000		1498 DC XL16' FF800000FF800000FF800000FF800000'
0000B1C0	E2C5C2D9	61E2C5C2		1499 DC CL48' SEBR/SEB NF -inf/+inf'
0000B1F0	FF800000	FF800000		1500 DC XL16' FF800000FF800000FF800000FF800000'
0000B200	E2C5C2D9	61E2C5C2		1501 DC CL48' SEBR/SEB NF -inf/-QNaN'
0000B230	FFCB0000	FFCB0000		1502 DC XL16' FFCB0000FFCB0000FFCB0000FFCB0000'
0000B240	E2C5C2D9	61E2C5C2		1503 DC CL48' SEBR/SEB NF -inf/+SNaN'
0000B270	7FCA0000	FF800000		1504 DC XL16' 7FCA0000FF8000007FCA0000FF800000'
0000B280	E2C5C2D9	61E2C5C2		1505 DC CL48' SEBR/SEB NF -2.0/-inf'
0000B2B0	7F800000	7F800000		1506 DC XL16' 7F8000007F8000007F8000007F800000'
0000B2C0	E2C5C2D9	61E2C5C2		1507 DC CL48' SEBR/SEB NF -2.0/-2.0'
0000B2F0	00000000	00000000		1508 DC XL16' 00000000000000000000000000000000'
0000B300	E2C5C2D9	61E2C5C2		1509 DC CL48' SEBR/SEB NF -2.0/-Dnice'
0000B330	C0000000	C0000000		1510 DC XL16' C0000000C0000000C0000000C0000000'
0000B340	E2C5C2D9	61E2C5C2		1511 DC CL48' SEBR/SEB NF -2.0/-0'
0000B370	C0000000	C0000000		1512 DC XL16' C0000000C0000000C0000000C0000000'
0000B380	E2C5C2D9	61E2C5C2		1513 DC CL48' SEBR/SEB NF -2.0/+0'
0000B3B0	C0000000	C0000000		1514 DC XL16' C0000000C0000000C0000000C0000000'
0000B3C0	E2C5C2D9	61E2C5C2		1515 DC CL48' SEBR/SEB NF -2.0/+Dnice'
0000B3F0	C0000000	C0000000		1516 DC XL16' C0000000C0000000C0000000C0000000'
0000B400	E2C5C2D9	61E2C5C2		1517 DC CL48' SEBR/SEB NF -2.0/+2.0'
0000B430	C0800000	C0800000		1518 DC XL16' C0800000C0800000C0800000C0800000'
0000B440	E2C5C2D9	61E2C5C2		1519 DC CL48' SEBR/SEB NF -2.0/+inf'
0000B470	FF800000	FF800000		1520 DC XL16' FF800000FF800000FF800000FF800000'
0000B480	E2C5C2D9	61E2C5C2		1521 DC CL48' SEBR/SEB NF -2.0/-QNaN'
0000B4B0	FFCB0000	FFCB0000		1522 DC XL16' FFCB0000FFCB0000FFCB0000FFCB0000'
0000B4C0	E2C5C2D9	61E2C5C2		1523 DC CL48' SEBR/SEB NF -2.0/+SNaN'
0000B4F0	7FCA0000	C0000000		1524 DC XL16' 7FCA0000C00000007FCA0000C0000000'
0000B500	E2C5C2D9	61E2C5C2		1525 DC CL48' SEBR/SEB NF -Dnice/-inf'
0000B530	7F800000	7F800000		1526 DC XL16' 7F8000007F8000007F8000007F800000'
0000B540	E2C5C2D9	61E2C5C2		1527 DC CL48' SEBR/SEB NF -Dnice/-2.0'
0000B570	40000000	40000000		1528 DC XL16' 40000000400000004000000040000000'
0000B580	E2C5C2D9	61E2C5C2		1529 DC CL48' SEBR/SEB NF -Dnice/-Dnice'
0000B5B0	00000000	00000000		1530 DC XL16' 00000000000000000000000000000000'
0000B5C0	E2C5C2D9	61E2C5C2		1531 DC CL48' SEBR/SEB NF -Dnice/-0'
0000B5F0	80010000	DD000000		1532 DC XL16' 80010000DD00000080010000DD000000'
0000B600	E2C5C2D9	61E2C5C2		1533 DC CL48' SEBR/SEB NF -Dnice/+0'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
0000B630	80010000 DD000000			1534 DC XL16'80010000DD00000080010000DD000000'
0000B640	E2C5C2D9 61E2C5C2			1535 DC CL48'SEBR/SEB NF -Dnice/+Dnice'
0000B670	80020000 DD800000			1536 DC XL16'80020000DD80000080020000DD800000'
0000B680	E2C5C2D9 61E2C5C2			1537 DC CL48'SEBR/SEB NF -Dnice/+2.0'
0000B6B0	C0000000 C0000000			1538 DC XL16'C0000000C0000000C0000000C0000000'
0000B6C0	E2C5C2D9 61E2C5C2			1539 DC CL48'SEBR/SEB NF -Dnice/+inf'
0000B6F0	FF800000 FF800000			1540 DC XL16'FF800000FF800000FF800000FF800000'
0000B700	E2C5C2D9 61E2C5C2			1541 DC CL48'SEBR/SEB NF -Dnice/-QNaN'
0000B730	FFCB0000 FFCB0000			1542 DC XL16'FFCB0000FFCB0000FFCB0000FFCB0000'
0000B740	E2C5C2D9 61E2C5C2			1543 DC CL48'SEBR/SEB NF -Dnice/+SNaN'
0000B770	7FCA0000 80010000			1544 DC XL16'7FCA0000800100007FCA000080010000'
0000B780	E2C5C2D9 61E2C5C2			1545 DC CL48'SEBR/SEB NF -0/-inf'
0000B7B0	7F800000 7F800000			1546 DC XL16'7F8000007F8000007F8000007F800000'
0000B7C0	E2C5C2D9 61E2C5C2			1547 DC CL48'SEBR/SEB NF -0/-2.0'
0000B7F0	40000000 40000000			1548 DC XL16'40000000400000004000000040000000'
0000B800	E2C5C2D9 61E2C5C2			1549 DC CL48'SEBR/SEB NF -0/-Dnice'
0000B830	00010000 5D000000			1550 DC XL16'000100005D000000000100005D000000'
0000B840	E2C5C2D9 61E2C5C2			1551 DC CL48'SEBR/SEB NF -0/-0'
0000B870	00000000 00000000			1552 DC XL16'00000000000000000000000000000000'
0000B880	E2C5C2D9 61E2C5C2			1553 DC CL48'SEBR/SEB NF -0/+0'
0000B8B0	80000000 80000000			1554 DC XL16'80000000800000008000000080000000'
0000B8C0	E2C5C2D9 61E2C5C2			1555 DC CL48'SEBR/SEB NF -0/+Dnice'
0000B8F0	80010000 DD000000			1556 DC XL16'80010000DD00000080010000DD000000'
0000B900	E2C5C2D9 61E2C5C2			1557 DC CL48'SEBR/SEB NF -0/+2.0'
0000B930	C0000000 C0000000			1558 DC XL16'C0000000C0000000C0000000C0000000'
0000B940	E2C5C2D9 61E2C5C2			1559 DC CL48'SEBR/SEB NF -0/+inf'
0000B970	FF800000 FF800000			1560 DC XL16'FF800000FF800000FF800000FF800000'
0000B980	E2C5C2D9 61E2C5C2			1561 DC CL48'SEBR/SEB NF -0/-QNaN'
0000B9B0	FFCB0000 FFCB0000			1562 DC XL16'FFCB0000FFCB0000FFCB0000FFCB0000'
0000B9C0	E2C5C2D9 61E2C5C2			1563 DC CL48'SEBR/SEB NF -0/+SNaN'
0000B9F0	7FCA0000 80000000			1564 DC XL16'7FCA0000800000007FCA000080000000'
0000BA00	E2C5C2D9 61E2C5C2			1565 DC CL48'SEBR/SEB NF +0/-inf'
0000BA30	7F800000 7F800000			1566 DC XL16'7F8000007F8000007F8000007F800000'
0000BA40	E2C5C2D9 61E2C5C2			1567 DC CL48'SEBR/SEB NF +0/-2.0'
0000BA70	40000000 40000000			1568 DC XL16'40000000400000004000000040000000'
0000BA80	E2C5C2D9 61E2C5C2			1569 DC CL48'SEBR/SEB NF +0/-Dnice'
0000BAB0	00010000 5D000000			1570 DC XL16'000100005D000000000100005D000000'
0000BAC0	E2C5C2D9 61E2C5C2			1571 DC CL48'SEBR/SEB NF +0/-0'
0000BAF0	00000000 00000000			1572 DC XL16'00000000000000000000000000000000'
0000BB00	E2C5C2D9 61E2C5C2			1573 DC CL48'SEBR/SEB NF +0/+0'
0000BB30	00000000 00000000			1574 DC XL16'00000000000000000000000000000000'
0000BB40	E2C5C2D9 61E2C5C2			1575 DC CL48'SEBR/SEB NF +0/+Dnice'
0000BB70	80010000 DD000000			1576 DC XL16'80010000DD00000080010000DD000000'
0000BB80	E2C5C2D9 61E2C5C2			1577 DC CL48'SEBR/SEB NF +0/+2.0'
0000BBB0	C0000000 C0000000			1578 DC XL16'C0000000C0000000C0000000C0000000'
0000BBC0	E2C5C2D9 61E2C5C2			1579 DC CL48'SEBR/SEB NF +0/+inf'
0000BBF0	FF800000 FF800000			1580 DC XL16'FF800000FF800000FF800000FF800000'
0000BC00	E2C5C2D9 61E2C5C2			1581 DC CL48'SEBR/SEB NF +0/-QNaN'
0000BC30	FFCB0000 FFCB0000			1582 DC XL16'FFCB0000FFCB0000FFCB0000FFCB0000'
0000BC40	E2C5C2D9 61E2C5C2			1583 DC CL48'SEBR/SEB NF +0/+SNaN'
0000BC70	7FCA0000 00000000			1584 DC XL16'7FCA0000000000007FCA000000000000'
0000BC80	E2C5C2D9 61E2C5C2			1585 DC CL48'SEBR/SEB NF +Dnice/-inf'
0000BCB0	7F800000 7F800000			1586 DC XL16'7F8000007F8000007F8000007F800000'
0000BCC0	E2C5C2D9 61E2C5C2			1587 DC CL48'SEBR/SEB NF +Dnice/-2.0'
0000BCF0	40000000 40000000			1588 DC XL16'40000000400000004000000040000000'
0000BD00	E2C5C2D9 61E2C5C2			1589 DC CL48'SEBR/SEB NF +Dnice/-Dnice'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
0000BD30	00020000	5D800000		1590 DC XL16'000200005D800000000200005D800000'
0000BD40	E2C5C2D9	61E2C5C2		1591 DC CL48'SEBR/SEB NF +Dnice/-0'
0000BD70	00010000	5D000000		1592 DC XL16'000100005D000000000100005D000000'
0000BD80	E2C5C2D9	61E2C5C2		1593 DC CL48'SEBR/SEB NF +Dnice/+0'
0000BDB0	00010000	5D000000		1594 DC XL16'000100005D000000000100005D000000'
0000BDC0	E2C5C2D9	61E2C5C2		1595 DC CL48'SEBR/SEB NF +Dnice/+Dnice'
0000BDF0	00000000	00000000		1596 DC XL16'000000000000000000000000000000'
0000BE00	E2C5C2D9	61E2C5C2		1597 DC CL48'SEBR/SEB NF +Dnice/+2.0'
0000BE30	C0000000	C0000000		1598 DC XL16'C0000000C0000000C0000000C0000000'
0000BE40	E2C5C2D9	61E2C5C2		1599 DC CL48'SEBR/SEB NF +Dnice/+inf'
0000BE70	FF800000	FF800000		1600 DC XL16'FF800000FF800000FF800000FF800000'
0000BE80	E2C5C2D9	61E2C5C2		1601 DC CL48'SEBR/SEB NF +Dnice/-QNaN'
0000BEB0	FFCB0000	FFCB0000		1602 DC XL16'FFCB0000FFCB0000FFCB0000FFCB0000'
0000BEC0	E2C5C2D9	61E2C5C2		1603 DC CL48'SEBR/SEB NF +Dnice/+SNaN'
0000BEF0	7FCA0000	00010000		1604 DC XL16'7FCA0000000100007FCA000000010000'
0000BF00	E2C5C2D9	61E2C5C2		1605 DC CL48'SEBR/SEB NF +2.0/-inf'
0000BF30	7F800000	7F800000		1606 DC XL16'7F8000007F8000007F8000007F800000'
0000BF40	E2C5C2D9	61E2C5C2		1607 DC CL48'SEBR/SEB NF +2.0/-2.0'
0000BF70	40800000	40800000		1608 DC XL16'40800000408000004080000040800000'
0000BF80	E2C5C2D9	61E2C5C2		1609 DC CL48'SEBR/SEB NF +2.0/-Dnice'
0000BFB0	40000000	40000000		1610 DC XL16'40000000400000004000000040000000'
0000BFC0	E2C5C2D9	61E2C5C2		1611 DC CL48'SEBR/SEB NF +2.0/-0'
0000BFF0	40000000	40000000		1612 DC XL16'40000000400000004000000040000000'
0000C000	E2C5C2D9	61E2C5C2		1613 DC CL48'SEBR/SEB NF +2.0/+0'
0000C030	40000000	40000000		1614 DC XL16'40000000400000004000000040000000'
0000C040	E2C5C2D9	61E2C5C2		1615 DC CL48'SEBR/SEB NF +2.0/+Dnice'
0000C070	40000000	40000000		1616 DC XL16'40000000400000004000000040000000'
0000C080	E2C5C2D9	61E2C5C2		1617 DC CL48'SEBR/SEB NF +2.0/+2.0'
0000C0B0	00000000	00000000		1618 DC XL16'000000000000000000000000000000'
0000C0C0	E2C5C2D9	61E2C5C2		1619 DC CL48'SEBR/SEB NF +2.0/+inf'
0000C0F0	FF800000	FF800000		1620 DC XL16'FF800000FF800000FF800000FF800000'
0000C100	E2C5C2D9	61E2C5C2		1621 DC CL48'SEBR/SEB NF +2.0/-QNaN'
0000C130	FFCB0000	FFCB0000		1622 DC XL16'FFCB0000FFCB0000FFCB0000FFCB0000'
0000C140	E2C5C2D9	61E2C5C2		1623 DC CL48'SEBR/SEB NF +2.0/+SNaN'
0000C170	7FCA0000	40000000		1624 DC XL16'7FCA0000400000007FCA000040000000'
0000C180	E2C5C2D9	61E2C5C2		1625 DC CL48'SEBR/SEB NF +inf/-inf'
0000C1B0	7F800000	7F800000		1626 DC XL16'7F8000007F8000007F8000007F800000'
0000C1C0	E2C5C2D9	61E2C5C2		1627 DC CL48'SEBR/SEB NF +inf/-2.0'
0000C1F0	7F800000	7F800000		1628 DC XL16'7F8000007F8000007F8000007F800000'
0000C200	E2C5C2D9	61E2C5C2		1629 DC CL48'SEBR/SEB NF +inf/-Dnice'
0000C230	7F800000	7F800000		1630 DC XL16'7F8000007F8000007F8000007F800000'
0000C240	E2C5C2D9	61E2C5C2		1631 DC CL48'SEBR/SEB NF +inf/-0'
0000C270	7F800000	7F800000		1632 DC XL16'7F8000007F8000007F8000007F800000'
0000C280	E2C5C2D9	61E2C5C2		1633 DC CL48'SEBR/SEB NF +inf/+0'
0000C2B0	7F800000	7F800000		1634 DC XL16'7F8000007F8000007F8000007F800000'
0000C2C0	E2C5C2D9	61E2C5C2		1635 DC CL48'SEBR/SEB NF +inf/+Dnice'
0000C2F0	7F800000	7F800000		1636 DC XL16'7F8000007F8000007F8000007F800000'
0000C300	E2C5C2D9	61E2C5C2		1637 DC CL48'SEBR/SEB NF +inf/+2.0'
0000C330	7F800000	7F800000		1638 DC XL16'7F8000007F8000007F8000007F800000'
0000C340	E2C5C2D9	61E2C5C2		1639 DC CL48'SEBR/SEB NF +inf/+inf'
0000C370	7FC00000	7F800000		1640 DC XL16'7FC000007F8000007FC000007F800000'
0000C380	E2C5C2D9	61E2C5C2		1641 DC CL48'SEBR/SEB NF +inf/-QNaN'
0000C3B0	FFCB0000	FFCB0000		1642 DC XL16'FFCB0000FFCB0000FFCB0000FFCB0000'
0000C3C0	E2C5C2D9	61E2C5C2		1643 DC CL48'SEBR/SEB NF +inf/+SNaN'
0000C3F0	7FCA0000	7F800000		1644 DC XL16'7FCA00007F8000007FCA00007F800000'
0000C400	E2C5C2D9	61E2C5C2		1645 DC CL48'SEBR/SEB NF -QNaN/-inf'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
0000C430	FFCB0000	FFCB0000		1646 DC XL16'FFCB0000FFCB0000FFCB0000FFCB0000'
0000C440	E2C5C2D9	61E2C5C2		1647 DC CL48'SEBR/SEB NF -QNaN/-2.0'
0000C470	FFCB0000	FFCB0000		1648 DC XL16'FFCB0000FFCB0000FFCB0000FFCB0000'
0000C480	E2C5C2D9	61E2C5C2		1649 DC CL48'SEBR/SEB NF -QNaN/-Dnice'
0000C4B0	FFCB0000	FFCB0000		1650 DC XL16'FFCB0000FFCB0000FFCB0000FFCB0000'
0000C4C0	E2C5C2D9	61E2C5C2		1651 DC CL48'SEBR/SEB NF -QNaN/-0'
0000C4F0	FFCB0000	FFCB0000		1652 DC XL16'FFCB0000FFCB0000FFCB0000FFCB0000'
0000C500	E2C5C2D9	61E2C5C2		1653 DC CL48'SEBR/SEB NF -QNaN/+0'
0000C530	FFCB0000	FFCB0000		1654 DC XL16'FFCB0000FFCB0000FFCB0000FFCB0000'
0000C540	E2C5C2D9	61E2C5C2		1655 DC CL48'SEBR/SEB NF -QNaN/+Dnice'
0000C570	FFCB0000	FFCB0000		1656 DC XL16'FFCB0000FFCB0000FFCB0000FFCB0000'
0000C580	E2C5C2D9	61E2C5C2		1657 DC CL48'SEBR/SEB NF -QNaN/+2.0'
0000C5B0	FFCB0000	FFCB0000		1658 DC XL16'FFCB0000FFCB0000FFCB0000FFCB0000'
0000C5C0	E2C5C2D9	61E2C5C2		1659 DC CL48'SEBR/SEB NF -QNaN/+inf'
0000C5F0	FFCB0000	FFCB0000		1660 DC XL16'FFCB0000FFCB0000FFCB0000FFCB0000'
0000C600	E2C5C2D9	61E2C5C2		1661 DC CL48'SEBR/SEB NF -QNaN/-QNaN'
0000C630	FFCB0000	FFCB0000		1662 DC XL16'FFCB0000FFCB0000FFCB0000FFCB0000'
0000C640	E2C5C2D9	61E2C5C2		1663 DC CL48'SEBR/SEB NF -QNaN/+SNaN'
0000C670	7FCA0000	FFCB0000		1664 DC XL16'7FCA0000FFCB00007FCA0000FFCB0000'
0000C680	E2C5C2D9	61E2C5C2		1665 DC CL48'SEBR/SEB NF +SNaN/-inf'
0000C6B0	7FCA0000	7F8A0000		1666 DC XL16'7FCA00007F8A00007FCA00007F8A0000'
0000C6C0	E2C5C2D9	61E2C5C2		1667 DC CL48'SEBR/SEB NF +SNaN/-2.0'
0000C6F0	7FCA0000	7F8A0000		1668 DC XL16'7FCA00007F8A00007FCA00007F8A0000'
0000C700	E2C5C2D9	61E2C5C2		1669 DC CL48'SEBR/SEB NF +SNaN/-Dnice'
0000C730	7FCA0000	7F8A0000		1670 DC XL16'7FCA00007F8A00007FCA00007F8A0000'
0000C740	E2C5C2D9	61E2C5C2		1671 DC CL48'SEBR/SEB NF +SNaN/-0'
0000C770	7FCA0000	7F8A0000		1672 DC XL16'7FCA00007F8A00007FCA00007F8A0000'
0000C780	E2C5C2D9	61E2C5C2		1673 DC CL48'SEBR/SEB NF +SNaN/+0'
0000C7B0	7FCA0000	7F8A0000		1674 DC XL16'7FCA00007F8A00007FCA00007F8A0000'
0000C7C0	E2C5C2D9	61E2C5C2		1675 DC CL48'SEBR/SEB NF +SNaN/+Dnice'
0000C7F0	7FCA0000	7F8A0000		1676 DC XL16'7FCA00007F8A00007FCA00007F8A0000'
0000C800	E2C5C2D9	61E2C5C2		1677 DC CL48'SEBR/SEB NF +SNaN/+2.0'
0000C830	7FCA0000	7F8A0000		1678 DC XL16'7FCA00007F8A00007FCA00007F8A0000'
0000C840	E2C5C2D9	61E2C5C2		1679 DC CL48'SEBR/SEB NF +SNaN/+inf'
0000C870	7FCA0000	7F8A0000		1680 DC XL16'7FCA00007F8A00007FCA00007F8A0000'
0000C880	E2C5C2D9	61E2C5C2		1681 DC CL48'SEBR/SEB NF +SNaN/-QNaN'
0000C8B0	7FCA0000	7F8A0000		1682 DC XL16'7FCA00007F8A00007FCA00007F8A0000'
0000C8C0	E2C5C2D9	61E2C5C2		1683 DC CL48'SEBR/SEB NF +SNaN/+SNaN'
0000C8F0	7FCA0000	7F8A0000		1684 DC XL16'7FCA00007F8A00007FCA00007F8A0000'
		00000064	00000001	1685 SBFPNFOT_NUM EQU (*-SBFPNFOT_GOOD)/64
				1686 *
				1687 *
		0000C900	00000001	1688 SBFPNFFL_GOOD EQU *
0000C900	E2C5C2D9	61E2C5C2		1689 DC CL48'SEBR/SEB NF -inf/-inf FPCR'
0000C930	00800003	F8008003		1690 DC XL16'00800003F800800300800003F8008003'
0000C940	E2C5C2D9	61E2C5C2		1691 DC CL48'SEBR/SEB NF -inf/-2.0 FPCR'
0000C970	00000001	F8000001		1692 DC XL16'00000001F800000100000001F8000001'
0000C980	E2C5C2D9	61E2C5C2		1693 DC CL48'SEBR/SEB NF -inf/-Dnice FPCR'
0000C9B0	00000001	F8000001		1694 DC XL16'00000001F800000100000001F8000001'
0000C9C0	E2C5C2D9	61E2C5C2		1695 DC CL48'SEBR/SEB NF -inf/-0 FPCR'
0000C9F0	00000001	F8000001		1696 DC XL16'00000001F800000100000001F8000001'
0000CA00	E2C5C2D9	61E2C5C2		1697 DC CL48'SEBR/SEB NF -inf/+0 FPCR'
0000CA30	00000001	F8000001		1698 DC XL16'00000001F800000100000001F8000001'
0000CA40	E2C5C2D9	61E2C5C2		1699 DC CL48'SEBR/SEB NF -inf/+Dnice FPCR'
0000CA70	00000001	F8000001		1700 DC XL16'00000001F800000100000001F8000001'
0000CA80	E2C5C2D9	61E2C5C2		1701 DC CL48'SEBR/SEB NF -inf/+2.0 FPCR'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
0000CAB0	00000001 F8000001			1702 DC XL16'00000001F800000100000001F8000001'
0000CAC0	E2C5C2D9 61E2C5C2			1703 DC CL48'SEBR/SEB NF -inf/+inf FPCR'
0000CAF0	00000001 F8000001			1704 DC XL16'00000001F800000100000001F8000001'
0000CB00	E2C5C2D9 61E2C5C2			1705 DC CL48'SEBR/SEB NF -inf/-QNaN FPCR'
0000CB30	00000003 F8000003			1706 DC XL16'00000003F800000300000003F8000003'
0000CB40	E2C5C2D9 61E2C5C2			1707 DC CL48'SEBR/SEB NF -inf/+SNaN FPCR'
0000CB70	00800003 F8008003			1708 DC XL16'00800003F800800300800003F8008003'
0000CB80	E2C5C2D9 61E2C5C2			1709 DC CL48'SEBR/SEB NF -2.0/-inf FPCR'
0000CBB0	00000002 F8000002			1710 DC XL16'00000002F800000200000002F8000002'
0000CBC0	E2C5C2D9 61E2C5C2			1711 DC CL48'SEBR/SEB NF -2.0/-2.0 FPCR'
0000CBF0	00000000 F8000000			1712 DC XL16'00000000F800000000000000F8000000'
0000CC00	E2C5C2D9 61E2C5C2			1713 DC CL48'SEBR/SEB NF -2.0/-Dnice FPCR'
0000CC30	00080001 F8000C01			1714 DC XL16'00080001F8000C0100080001F8000C01'
0000CC40	E2C5C2D9 61E2C5C2			1715 DC CL48'SEBR/SEB NF -2.0/-0 FPCR'
0000CC70	00000001 F8000001			1716 DC XL16'00000001F800000100000001F8000001'
0000CC80	E2C5C2D9 61E2C5C2			1717 DC CL48'SEBR/SEB NF -2.0/+0 FPCR'
0000CCB0	00000001 F8000001			1718 DC XL16'00000001F800000100000001F8000001'
0000CCC0	E2C5C2D9 61E2C5C2			1719 DC CL48'SEBR/SEB NF -2.0/+Dnice FPCR'
0000CCF0	00080001 F8000801			1720 DC XL16'00080001F800080100080001F8000801'
0000CD00	E2C5C2D9 61E2C5C2			1721 DC CL48'SEBR/SEB NF -2.0/+2.0 FPCR'
0000CD30	00000001 F8000001			1722 DC XL16'00000001F800000100000001F8000001'
0000CD40	E2C5C2D9 61E2C5C2			1723 DC CL48'SEBR/SEB NF -2.0/+inf FPCR'
0000CD70	00000001 F8000001			1724 DC XL16'00000001F800000100000001F8000001'
0000CD80	E2C5C2D9 61E2C5C2			1725 DC CL48'SEBR/SEB NF -2.0/-QNaN FPCR'
0000CDB0	00000003 F8000003			1726 DC XL16'00000003F800000300000003F8000003'
0000CDC0	E2C5C2D9 61E2C5C2			1727 DC CL48'SEBR/SEB NF -2.0/+SNaN FPCR'
0000CDF0	00800003 F8008003			1728 DC XL16'00800003F800800300800003F8008003'
0000CE00	E2C5C2D9 61E2C5C2			1729 DC CL48'SEBR/SEB NF -Dnice/-inf FPCR'
0000CE30	00000002 F8000002			1730 DC XL16'00000002F800000200000002F8000002'
0000CE40	E2C5C2D9 61E2C5C2			1731 DC CL48'SEBR/SEB NF -Dnice/-2.0 FPCR'
0000CE70	00080002 F8000C02			1732 DC XL16'00080002F8000C0200080002F8000C02'
0000CE80	E2C5C2D9 61E2C5C2			1733 DC CL48'SEBR/SEB NF -Dnice/-Dnice FPCR'
0000CEB0	00000000 F8000000			1734 DC XL16'00000000F800000000000000F8000000'
0000CEC0	E2C5C2D9 61E2C5C2			1735 DC CL48'SEBR/SEB NF -Dnice/-0 FPCR'
0000CEF0	00000001 F8001001			1736 DC XL16'00000001F800100100000001F8001001'
0000CF00	E2C5C2D9 61E2C5C2			1737 DC CL48'SEBR/SEB NF -Dnice/+0 FPCR'
0000CF30	00000001 F8001001			1738 DC XL16'00000001F800100100000001F8001001'
0000CF40	E2C5C2D9 61E2C5C2			1739 DC CL48'SEBR/SEB NF -Dnice/+Dnice FPCR'
0000CF70	00000001 F8001001			1740 DC XL16'00000001F800100100000001F8001001'
0000CF80	E2C5C2D9 61E2C5C2			1741 DC CL48'SEBR/SEB NF -Dnice/+2.0 FPCR'
0000CFB0	00080001 F8000801			1742 DC XL16'00080001F800080100080001F8000801'
0000CFC0	E2C5C2D9 61E2C5C2			1743 DC CL48'SEBR/SEB NF -Dnice/+inf FPCR'
0000CFF0	00000001 F8000001			1744 DC XL16'00000001F800000100000001F8000001'
0000D000	E2C5C2D9 61E2C5C2			1745 DC CL48'SEBR/SEB NF -Dnice/-QNaN FPCR'
0000D030	00000003 F8000003			1746 DC XL16'00000003F800000300000003F8000003'
0000D040	E2C5C2D9 61E2C5C2			1747 DC CL48'SEBR/SEB NF -Dnice/+SNaN FPCR'
0000D070	00800003 F8008003			1748 DC XL16'00800003F800800300800003F8008003'
0000D080	E2C5C2D9 61E2C5C2			1749 DC CL48'SEBR/SEB NF -0/-inf FPCR'
0000D0B0	00000002 F8000002			1750 DC XL16'00000002F800000200000002F8000002'
0000D0C0	E2C5C2D9 61E2C5C2			1751 DC CL48'SEBR/SEB NF -0/-2.0 FPCR'
0000D0F0	00000002 F8000002			1752 DC XL16'00000002F800000200000002F8000002'
0000D100	E2C5C2D9 61E2C5C2			1753 DC CL48'SEBR/SEB NF -0/-Dnice FPCR'
0000D130	00000002 F8001002			1754 DC XL16'00000002F800100200000002F8001002'
0000D140	E2C5C2D9 61E2C5C2			1755 DC CL48'SEBR/SEB NF -0/-0 FPCR'
0000D170	00000000 F8000000			1756 DC XL16'00000000F800000000000000F8000000'
0000D180	E2C5C2D9 61E2C5C2			1757 DC CL48'SEBR/SEB NF -0/+0 FPCR'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
0000D1B0	00000000 F8000000			1758 DC XL16'00000000F800000000000000F8000000'
0000D1C0	E2C5C2D9 61E2C5C2			1759 DC CL48'SEBR/SEB NF -0/+Dnice FPCR'
0000D1F0	00000001 F8001001			1760 DC XL16'00000001F800100100000001F8001001'
0000D200	E2C5C2D9 61E2C5C2			1761 DC CL48'SEBR/SEB NF -0/+2.0 FPCR'
0000D230	00000001 F8000001			1762 DC XL16'00000001F800000100000001F8000001'
0000D240	E2C5C2D9 61E2C5C2			1763 DC CL48'SEBR/SEB NF -0/+inf FPCR'
0000D270	00000001 F8000001			1764 DC XL16'00000001F800000100000001F8000001'
0000D280	E2C5C2D9 61E2C5C2			1765 DC CL48'SEBR/SEB NF -0/-QNaN FPCR'
0000D2B0	00000003 F8000003			1766 DC XL16'00000003F800000300000003F8000003'
0000D2C0	E2C5C2D9 61E2C5C2			1767 DC CL48'SEBR/SEB NF -0/+SNaN FPCR'
0000D2F0	00800003 F8008003			1768 DC XL16'00800003F800800300800003F8008003'
0000D300	E2C5C2D9 61E2C5C2			1769 DC CL48'SEBR/SEB NF +0/-inf FPCR'
0000D330	00000002 F8000002			1770 DC XL16'00000002F800000200000002F8000002'
0000D340	E2C5C2D9 61E2C5C2			1771 DC CL48'SEBR/SEB NF +0/-2.0 FPCR'
0000D370	00000002 F8000002			1772 DC XL16'00000002F800000200000002F8000002'
0000D380	E2C5C2D9 61E2C5C2			1773 DC CL48'SEBR/SEB NF +0/-Dnice FPCR'
0000D3B0	00000002 F8001002			1774 DC XL16'00000002F800100200000002F8001002'
0000D3C0	E2C5C2D9 61E2C5C2			1775 DC CL48'SEBR/SEB NF +0/-0 FPCR'
0000D3F0	00000000 F8000000			1776 DC XL16'00000000F800000000000000F8000000'
0000D400	E2C5C2D9 61E2C5C2			1777 DC CL48'SEBR/SEB NF +0/+0 FPCR'
0000D430	00000000 F8000000			1778 DC XL16'00000000F800000000000000F8000000'
0000D440	E2C5C2D9 61E2C5C2			1779 DC CL48'SEBR/SEB NF +0/+Dnice FPCR'
0000D470	00000001 F8001001			1780 DC XL16'00000001F800100100000001F8001001'
0000D480	E2C5C2D9 61E2C5C2			1781 DC CL48'SEBR/SEB NF +0/+2.0 FPCR'
0000D4B0	00000001 F8000001			1782 DC XL16'00000001F800000100000001F8000001'
0000D4C0	E2C5C2D9 61E2C5C2			1783 DC CL48'SEBR/SEB NF +0/+inf FPCR'
0000D4F0	00000001 F8000001			1784 DC XL16'00000001F800000100000001F8000001'
0000D500	E2C5C2D9 61E2C5C2			1785 DC CL48'SEBR/SEB NF +0/-QNaN FPCR'
0000D530	00000003 F8000003			1786 DC XL16'00000003F800000300000003F8000003'
0000D540	E2C5C2D9 61E2C5C2			1787 DC CL48'SEBR/SEB NF +0/+SNaN FPCR'
0000D570	00800003 F8008003			1788 DC XL16'00800003F800800300800003F8008003'
0000D580	E2C5C2D9 61E2C5C2			1789 DC CL48'SEBR/SEB NF +Dnice/-inf FPCR'
0000D5B0	00000002 F8000002			1790 DC XL16'00000002F800000200000002F8000002'
0000D5C0	E2C5C2D9 61E2C5C2			1791 DC CL48'SEBR/SEB NF +Dnice/-2.0 FPCR'
0000D5F0	00080002 F8000802			1792 DC XL16'00080002F800080200080002F8000802'
0000D600	E2C5C2D9 61E2C5C2			1793 DC CL48'SEBR/SEB NF +Dnice/-Dnice FPCR'
0000D630	00000002 F8001002			1794 DC XL16'00000002F800100200000002F8001002'
0000D640	E2C5C2D9 61E2C5C2			1795 DC CL48'SEBR/SEB NF +Dnice/-0 FPCR'
0000D670	00000002 F8001002			1796 DC XL16'00000002F800100200000002F8001002'
0000D680	E2C5C2D9 61E2C5C2			1797 DC CL48'SEBR/SEB NF +Dnice/+0 FPCR'
0000D6B0	00000002 F8001002			1798 DC XL16'00000002F800100200000002F8001002'
0000D6C0	E2C5C2D9 61E2C5C2			1799 DC CL48'SEBR/SEB NF +Dnice/+Dnice FPCR'
0000D6F0	00000000 F8000000			1800 DC XL16'00000000F800000000000000F8000000'
0000D700	E2C5C2D9 61E2C5C2			1801 DC CL48'SEBR/SEB NF +Dnice/+2.0 FPCR'
0000D730	00080001 F8000C01			1802 DC XL16'00080001F8000C0100080001F8000C01'
0000D740	E2C5C2D9 61E2C5C2			1803 DC CL48'SEBR/SEB NF +Dnice/+inf FPCR'
0000D770	00000001 F8000001			1804 DC XL16'00000001F800000100000001F8000001'
0000D780	E2C5C2D9 61E2C5C2			1805 DC CL48'SEBR/SEB NF +Dnice/-QNaN FPCR'
0000D7B0	00000003 F8000003			1806 DC XL16'00000003F800000300000003F8000003'
0000D7C0	E2C5C2D9 61E2C5C2			1807 DC CL48'SEBR/SEB NF +Dnice/+SNaN FPCR'
0000D7F0	00800003 F8008003			1808 DC XL16'00800003F800800300800003F8008003'
0000D800	E2C5C2D9 61E2C5C2			1809 DC CL48'SEBR/SEB NF +2.0/-inf FPCR'
0000D830	00000002 F8000002			1810 DC XL16'00000002F800000200000002F8000002'
0000D840	E2C5C2D9 61E2C5C2			1811 DC CL48'SEBR/SEB NF +2.0/-2.0 FPCR'
0000D870	00000002 F8000002			1812 DC XL16'00000002F800000200000002F8000002'
0000D880	E2C5C2D9 61E2C5C2			1813 DC CL48'SEBR/SEB NF +2.0/-Dnice FPCR'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
0000D8B0	00080002 F8000802			1814 DC XL16'00080002F800080200080002F8000802'
0000D8C0	E2C5C2D9 61E2C5C2			1815 DC CL48'SEBR/SEB NF +2.0/-0 FPCR'
0000D8F0	00000002 F8000002			1816 DC XL16'00000002F800000200000002F8000002'
0000D900	E2C5C2D9 61E2C5C2			1817 DC CL48'SEBR/SEB NF +2.0/+0 FPCR'
0000D930	00000002 F8000002			1818 DC XL16'00000002F800000200000002F8000002'
0000D940	E2C5C2D9 61E2C5C2			1819 DC CL48'SEBR/SEB NF +2.0/+Dnice FPCR'
0000D970	00080002 F8000C02			1820 DC XL16'00080002F8000C0200080002F8000C02'
0000D980	E2C5C2D9 61E2C5C2			1821 DC CL48'SEBR/SEB NF +2.0/+2.0 FPCR'
0000D9B0	00000000 F8000000			1822 DC XL16'00000000F800000000000000F8000000'
0000D9C0	E2C5C2D9 61E2C5C2			1823 DC CL48'SEBR/SEB NF +2.0/+inf FPCR'
0000D9F0	00000001 F8000001			1824 DC XL16'00000001F800000100000001F8000001'
0000DA00	E2C5C2D9 61E2C5C2			1825 DC CL48'SEBR/SEB NF +2.0/-QNaN FPCR'
0000DA30	00000003 F8000003			1826 DC XL16'00000003F800000300000003F8000003'
0000DA40	E2C5C2D9 61E2C5C2			1827 DC CL48'SEBR/SEB NF +2.0/+SNaN FPCR'
0000DA70	00800003 F8008003			1828 DC XL16'00800003F800800300800003F8008003'
0000DA80	E2C5C2D9 61E2C5C2			1829 DC CL48'SEBR/SEB NF +inf/-inf FPCR'
0000DAB0	00000002 F8000002			1830 DC XL16'00000002F800000200000002F8000002'
0000DAC0	E2C5C2D9 61E2C5C2			1831 DC CL48'SEBR/SEB NF +inf/-2.0 FPCR'
0000DAF0	00000002 F8000002			1832 DC XL16'00000002F800000200000002F8000002'
0000DB00	E2C5C2D9 61E2C5C2			1833 DC CL48'SEBR/SEB NF +inf/-Dnice FPCR'
0000DB30	00000002 F8000002			1834 DC XL16'00000002F800000200000002F8000002'
0000DB40	E2C5C2D9 61E2C5C2			1835 DC CL48'SEBR/SEB NF +inf/-0 FPCR'
0000DB70	00000002 F8000002			1836 DC XL16'00000002F800000200000002F8000002'
0000DB80	E2C5C2D9 61E2C5C2			1837 DC CL48'SEBR/SEB NF +inf/+0 FPCR'
0000DBB0	00000002 F8000002			1838 DC XL16'00000002F800000200000002F8000002'
0000DBC0	E2C5C2D9 61E2C5C2			1839 DC CL48'SEBR/SEB NF +inf/+Dnice FPCR'
0000DBF0	00000002 F8000002			1840 DC XL16'00000002F800000200000002F8000002'
0000DC00	E2C5C2D9 61E2C5C2			1841 DC CL48'SEBR/SEB NF +inf/+2.0 FPCR'
0000DC30	00000002 F8000002			1842 DC XL16'00000002F800000200000002F8000002'
0000DC40	E2C5C2D9 61E2C5C2			1843 DC CL48'SEBR/SEB NF +inf/+inf FPCR'
0000DC70	00800003 F8008003			1844 DC XL16'00800003F800800300800003F8008003'
0000DC80	E2C5C2D9 61E2C5C2			1845 DC CL48'SEBR/SEB NF +inf/-QNaN FPCR'
0000DCB0	00000003 F8000003			1846 DC XL16'00000003F800000300000003F8000003'
0000DCC0	E2C5C2D9 61E2C5C2			1847 DC CL48'SEBR/SEB NF +inf/+SNaN FPCR'
0000DCF0	00800003 F8008003			1848 DC XL16'00800003F800800300800003F8008003'
0000DD00	E2C5C2D9 61E2C5C2			1849 DC CL48'SEBR/SEB NF -QNaN/-inf FPCR'
0000DD30	00000003 F8000003			1850 DC XL16'00000003F800000300000003F8000003'
0000DD40	E2C5C2D9 61E2C5C2			1851 DC CL48'SEBR/SEB NF -QNaN/-2.0 FPCR'
0000DD70	00000003 F8000003			1852 DC XL16'00000003F800000300000003F8000003'
0000DD80	E2C5C2D9 61E2C5C2			1853 DC CL48'SEBR/SEB NF -QNaN/-Dnice FPCR'
0000ddb0	00000003 F8000003			1854 DC XL16'00000003F800000300000003F8000003'
0000DDC0	E2C5C2D9 61E2C5C2			1855 DC CL48'SEBR/SEB NF -QNaN/-0 FPCR'
0000DDF0	00000003 F8000003			1856 DC XL16'00000003F800000300000003F8000003'
0000DE00	E2C5C2D9 61E2C5C2			1857 DC CL48'SEBR/SEB NF -QNaN/+0 FPCR'
0000DE30	00000003 F8000003			1858 DC XL16'00000003F800000300000003F8000003'
0000DE40	E2C5C2D9 61E2C5C2			1859 DC CL48'SEBR/SEB NF -QNaN/+Dnice FPCR'
0000DE70	00000003 F8000003			1860 DC XL16'00000003F800000300000003F8000003'
0000DE80	E2C5C2D9 61E2C5C2			1861 DC CL48'SEBR/SEB NF -QNaN/+2.0 FPCR'
0000DEB0	00000003 F8000003			1862 DC XL16'00000003F800000300000003F8000003'
0000DEC0	E2C5C2D9 61E2C5C2			1863 DC CL48'SEBR/SEB NF -QNaN/+inf FPCR'
0000DEF0	00000003 F8000003			1864 DC XL16'00000003F800000300000003F8000003'
0000DF00	E2C5C2D9 61E2C5C2			1865 DC CL48'SEBR/SEB NF -QNaN/-QNaN FPCR'
0000DF30	00000003 F8000003			1866 DC XL16'00000003F800000300000003F8000003'
0000DF40	E2C5C2D9 61E2C5C2			1867 DC CL48'SEBR/SEB NF -QNaN/+SNaN FPCR'
0000DF70	00800003 F8008003			1868 DC XL16'00800003F800800300800003F8008003'
0000DF80	E2C5C2D9 61E2C5C2			1869 DC CL48'SEBR/SEB NF +SNaN/-inf FPCR'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
0000DFB0	00800003 F8008003			1870 DC XL16'00800003F800800300800003F8008003'
0000DFC0	E2C5C2D9 61E2C5C2			1871 DC CL48'SEBR/SEB NF +SNaN/-2.0 FPCR'
0000DFF0	00800003 F8008003			1872 DC XL16'00800003F800800300800003F8008003'
0000E000	E2C5C2D9 61E2C5C2			1873 DC CL48'SEBR/SEB NF +SNaN/-Dnice FPCR'
0000E030	00800003 F8008003			1874 DC XL16'00800003F800800300800003F8008003'
0000E040	E2C5C2D9 61E2C5C2			1875 DC CL48'SEBR/SEB NF +SNaN/-0 FPCR'
0000E070	00800003 F8008003			1876 DC XL16'00800003F800800300800003F8008003'
0000E080	E2C5C2D9 61E2C5C2			1877 DC CL48'SEBR/SEB NF +SNaN/+0 FPCR'
0000E0B0	00800003 F8008003			1878 DC XL16'00800003F800800300800003F8008003'
0000E0C0	E2C5C2D9 61E2C5C2			1879 DC CL48'SEBR/SEB NF +SNaN/+Dnice FPCR'
0000E0F0	00800003 F8008003			1880 DC XL16'00800003F800800300800003F8008003'
0000E100	E2C5C2D9 61E2C5C2			1881 DC CL48'SEBR/SEB NF +SNaN/+2.0 FPCR'
0000E130	00800003 F8008003			1882 DC XL16'00800003F800800300800003F8008003'
0000E140	E2C5C2D9 61E2C5C2			1883 DC CL48'SEBR/SEB NF +SNaN/+inf FPCR'
0000E170	00800003 F8008003			1884 DC XL16'00800003F800800300800003F8008003'
0000E180	E2C5C2D9 61E2C5C2			1885 DC CL48'SEBR/SEB NF +SNaN/-QNaN FPCR'
0000E1B0	00800003 F8008003			1886 DC XL16'00800003F800800300800003F8008003'
0000E1C0	E2C5C2D9 61E2C5C2			1887 DC CL48'SEBR/SEB NF +SNaN/+SNaN FPCR'
0000E1F0	00800003 F8008003			1888 DC XL16'00800003F800800300800003F8008003'
		00000064	00000001	1889 SBFPNFFL_NUM EQU (*-SBFPNFFL_GOOD)/64
				1890 *
				1891 *
		0000E200	00000001	1892 SBFPOUT_GOOD EQU *
0000E200	E2C5C2D9 61E2C5C2			1893 DC CL48'SEBR/SEB F Ovfl'
0000E230	7F800000 1FFFFFFF			1894 DC XL16'7F8000001FFFFFFF7F8000001FFFFFFF'
0000E240	E2C5C2D9 61E2C5C2			1895 DC CL48'SEBR/SEB F Ufl 1'
0000E270	007FFFFFF 607FFFFE			1896 DC XL16'007FFFFFF607FFFFE007FFFFFF607FFFFE'
0000E280	E2C5C2D9 61E2C5C2			1897 DC CL48'SEBR/SEB F Ufl 2'
0000E2B0	0003F0F1 5DFC3C40			1898 DC XL16'0003F0F15DFC3C400003F0F15DFC3C40'
0000E2C0	E2C5C2D9 61E2C5C2			1899 DC CL48'SEBR/SEB F Nmin'
0000E2F0	00800000 00800000			1900 DC XL16'00800000008000000080000000800000'
0000E300	E2C5C2D9 61E2C5C2			1901 DC CL48'SEBR/SEB F Incr'
0000E330	3F800000 3F800000			1902 DC XL16'3F8000003F8000003F8000003F800000'
0000E340	E2C5C2D9 61E2C5C2			1903 DC CL48'SEBR/SEB F Trun'
0000E370	3F7FFFFFF 3F7FFFFFF			1904 DC XL16'3F7FFFFFF3F7FFFFFF3F7FFFFFF3F7FFFFFF'
		00000006	00000001	1905 SBFPOUT_NUM EQU (*-SBFPOUT_GOOD)/64
				1906 *
				1907 *
		0000E380	00000001	1908 SBFPFLGS_GOOD EQU *
0000E380	E2C5C2D9 61E2C5C2			1909 DC CL48'SEBR/SEB F Ovfl FPCR'
0000E3B0	00280002 F8002002			1910 DC XL16'00280002F800200200280002F8002002'
0000E3C0	E2C5C2D9 61E2C5C2			1911 DC CL48'SEBR/SEB F Ufl 1 FPCR'
0000E3F0	00000002 F8001002			1912 DC XL16'00000002F800100200000002F8001002'
0000E400	E2C5C2D9 61E2C5C2			1913 DC CL48'SEBR/SEB F Ufl 2 FPCR'
0000E430	00000002 F8001002			1914 DC XL16'00000002F800100200000002F8001002'
0000E440	E2C5C2D9 61E2C5C2			1915 DC CL48'SEBR/SEB F Nmin FPCR'
0000E470	00000002 F8000002			1916 DC XL16'00000002F800000200000002F8000002'
0000E480	E2C5C2D9 61E2C5C2			1917 DC CL48'SEBR/SEB F Incr FPCR'
0000E4B0	00080002 F8000C02			1918 DC XL16'00080002F8000C0200080002F8000C02'
0000E4C0	E2C5C2D9 61E2C5C2			1919 DC CL48'SEBR/SEB F Trun FPCR'
0000E4F0	00080002 F8000802			1920 DC XL16'00080002F800080200080002F8000802'
		00000006	00000001	1921 SBFPFLGS_NUM EQU (*-SBFPFLGS_GOOD)/64
				1922 *
				1923 *
		0000E500	00000001	1924 SBFPRMO_GOOD EQU *
0000E500	E2C5C2D9 61E2C5C2			1925 DC CL48'SEBR/SEB RM +NZ RNTE, RZ'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
0000E530	3F7FFFFFFF 3F7FFFFFFF			1926 DC XL16'3F7FFFFFFF3F7FFFFFFF3F7FFFFFFF3F7FFFFFFF'
0000E540	E2C5C2D9 61E2C5C2			1927 DC CL48'SEBR/SEB RM +NZ RP, RM'
0000E570	3F800000 3F800000			1928 DC XL16'3F8000003F8000003F7FFFFFFF3F7FFFFFFF'
0000E580	E2C5C2D9 61E2C5C2			1929 DC CL48'SEBR/SEB RM +NZ RFS'
0000E5B0	3F7FFFFFFF 3F7FFFFFFF			1930 DC XL16'3F7FFFFFFF3F7FFFFFFF0000000000000000'
0000E5C0	E2C5C2D9 61E2C5C2			1931 DC CL48'SEBR/SEB RM -NZ RNTE, RZ'
0000E5F0	BF7FFFFFFF BF7FFFFFFF			1932 DC XL16'BF7FFFFFFFBF7FFFFFFF7FFFFFFF7FFFFFFF'
0000E600	E2C5C2D9 61E2C5C2			1933 DC CL48'SEBR/SEB RM -NZ RP, RM'
0000E630	BF7FFFFFFF BF7FFFFFFF			1934 DC XL16'BF7FFFFFFFBF7FFFFFFF800000BF800000'
0000E640	E2C5C2D9 61E2C5C2			1935 DC CL48'SEBR/SEB RM -NZ RFS'
0000E670	BF7FFFFFFF BF7FFFFFFF			1936 DC XL16'BF7FFFFFFFBF7FFFFFFF0000000000000000'
0000E680	E2C5C2D9 61E2C5C2			1937 DC CL48'SEBR/SEB RM +NA RNTE, RZ'
0000E6B0	3F800000 3F800000			1938 DC XL16'3F8000003F8000003F7FFFFFFF3F7FFFFFFF'
0000E6C0	E2C5C2D9 61E2C5C2			1939 DC CL48'SEBR/SEB RM +NA RP, RM'
0000E6F0	3F800000 3F800000			1940 DC XL16'3F8000003F8000003F7FFFFFFF3F7FFFFFFF'
0000E700	E2C5C2D9 61E2C5C2			1941 DC CL48'SEBR/SEB RM +NA RFS'
0000E730	3F7FFFFFFF 3F7FFFFFFF			1942 DC XL16'3F7FFFFFFF3F7FFFFFFF0000000000000000'
0000E740	E2C5C2D9 61E2C5C2			1943 DC CL48'SEBR/SEB RM -NA RNTE, RZ'
0000E770	BF800000 BF800000			1944 DC XL16'BF800000BF800000BF7FFFFFFF7FFFFFFF'
0000E780	E2C5C2D9 61E2C5C2			1945 DC CL48'SEBR/SEB RM -NA RP, RM'
0000E7B0	BF7FFFFFFF BF7FFFFFFF			1946 DC XL16'BF7FFFFFFFBF7FFFFFFF800000BF800000'
0000E7C0	E2C5C2D9 61E2C5C2			1947 DC CL48'SEBR/SEB RM -NA RFS'
0000E7F0	BF7FFFFFFF BF7FFFFFFF			1948 DC XL16'BF7FFFFFFFBF7FFFFFFF0000000000000000'
0000E800	E2C5C2D9 61E2C5C2			1949 DC CL48'SEBR/SEB RM +TZ RNTE, RZ'
0000E830	3F7FFFFFFE 3F7FFFFFFE			1950 DC XL16'3F7FFFFFFE3F7FFFFFFE3F7FFFFFFE3F7FFFFFFE'
0000E840	E2C5C2D9 61E2C5C2			1951 DC CL48'SEBR/SEB RM +TZ RP, RM'
0000E870	3F7FFFFFFF 3F7FFFFFFF			1952 DC XL16'3F7FFFFFFF3F7FFFFFFF3F7FFFFFFE3F7FFFFFFE'
0000E880	E2C5C2D9 61E2C5C2			1953 DC CL48'SEBR/SEB RM +TZ RFS'
0000E8B0	3F7FFFFFFF 3F7FFFFFFF			1954 DC XL16'3F7FFFFFFF3F7FFFFFFF0000000000000000'
0000E8C0	E2C5C2D9 61E2C5C2			1955 DC CL48'SEBR/SEB RM -TZ RNTE, RZ'
0000E8F0	BF7FFFFFFE BF7FFFFFFE			1956 DC XL16'BF7FFFFFFE3F7FFFFFFE3F7FFFFFFE3F7FFFFFFE'
0000E900	E2C5C2D9 61E2C5C2			1957 DC CL48'SEBR/SEB RM -TZ RP, RM'
0000E930	BF7FFFFFFE BF7FFFFFFE			1958 DC XL16'BF7FFFFFFE3F7FFFFFFE3F7FFFFFFE3F7FFFFFFE'
0000E940	E2C5C2D9 61E2C5C2			1959 DC CL48'SEBR/SEB RM -TZ RFS'
0000E970	BF7FFFFFFF BF7FFFFFFF			1960 DC XL16'BF7FFFFFFFBF7FFFFFFF0000000000000000'
0000E980	E2C5C2D9 61E2C5C2			1961 DC CL48'SEBR/SEB RM +TA RNTE, RZ'
0000E9B0	3F800000 3F800000			1962 DC XL16'3F8000003F8000003F7FFFFFFF3F7FFFFFFF'
0000E9C0	E2C5C2D9 61E2C5C2			1963 DC CL48'SEBR/SEB RM +TA RP, RM'
0000E9F0	3F800000 3F800000			1964 DC XL16'3F8000003F8000003F7FFFFFFF3F7FFFFFFF'
0000EA00	E2C5C2D9 61E2C5C2			1965 DC CL48'SEBR/SEB RM +TA RFS'
0000EA30	3F7FFFFFFF 3F7FFFFFFF			1966 DC XL16'3F7FFFFFFF3F7FFFFFFF0000000000000000'
0000EA40	E2C5C2D9 61E2C5C2			1967 DC CL48'SEBR/SEB RM -TA RNTE, RZ'
0000EA70	BF800000 BF800000			1968 DC XL16'BF800000BF800000BF7FFFFFFF7FFFFFFF'
0000EA80	E2C5C2D9 61E2C5C2			1969 DC CL48'SEBR/SEB RM -TA RP, RM'
0000EAB0	BF7FFFFFFF BF7FFFFFFF			1970 DC XL16'BF7FFFFFFFBF7FFFFFFF800000BF800000'
0000EAC0	E2C5C2D9 61E2C5C2			1971 DC CL48'SEBR/SEB RM -TA RFS'
0000EAF0	BF7FFFFFFF BF7FFFFFFF			1972 DC XL16'BF7FFFFFFFBF7FFFFFFF0000000000000000'
		00000018	00000001	1973 SBFPRMO_NUM EQU (*-SBFPRMO_GOOD)/64
				1974 *
				1975 *
		0000EB00	00000001	1976 SBFPRMOF_GOOD EQU *
0000EB00	E2C5C2D9 61E2C5C2			1977 DC CL48'SEBR/SEB RM +NZ RNTE, RZ FPCR'
0000EB30	00080002 00080002			1978 DC XL16'00080002000800020008000200080002'
0000EB40	E2C5C2D9 61E2C5C2			1979 DC CL48'SEBR/SEB RM +NZ RP, RM FPCR'
0000EB70	00080002 00080002			1980 DC XL16'00080002000800020008000200080002'
0000EB80	E2C5C2D9 61E2C5C2			1981 DC CL48'SEBR/SEB RM +NZ RFS FPCR'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
0000EBB0	00080002 00080002			1982 DC XL16'00080002000800020000000000000000'
0000EBC0	E2C5C2D9 61E2C5C2			1983 DC CL48'SEBR/SEB RM -NZ RNTE, RZ FPCR'
0000EBF0	00080001 00080001			1984 DC XL16'00080001000800010008000100080001'
0000EC00	E2C5C2D9 61E2C5C2			1985 DC CL48'SEBR/SEB RM -NZ RP, RM FPCR'
0000EC30	00080001 00080001			1986 DC XL16'00080001000800010008000100080001'
0000EC40	E2C5C2D9 61E2C5C2			1987 DC CL48'SEBR/SEB RM -NZ RFS FPCR'
0000EC70	00080001 00080001			1988 DC XL16'00080001000800010000000000000000'
0000EC80	E2C5C2D9 61E2C5C2			1989 DC CL48'SEBR/SEB RM +NA RNTE, RZ FPCR'
0000ECB0	00080002 00080002			1990 DC XL16'00080002000800020008000200080002'
0000ECC0	E2C5C2D9 61E2C5C2			1991 DC CL48'SEBR/SEB RM +NA RP, RM FPCR'
0000ECF0	00080002 00080002			1992 DC XL16'00080002000800020008000200080002'
0000ED00	E2C5C2D9 61E2C5C2			1993 DC CL48'SEBR/SEB RM +NA RFS FPCR'
0000ED30	00080002 00080002			1994 DC XL16'00080002000800020000000000000000'
0000ED40	E2C5C2D9 61E2C5C2			1995 DC CL48'SEBR/SEB RM -NA RNTE, RZ FPCR'
0000ED70	00080001 00080001			1996 DC XL16'00080001000800010008000100080001'
0000ED80	E2C5C2D9 61E2C5C2			1997 DC CL48'SEBR/SEB RM -NA RP, RM FPCR'
0000EDB0	00080001 00080001			1998 DC XL16'00080001000800010008000100080001'
0000EDC0	E2C5C2D9 61E2C5C2			1999 DC CL48'SEBR/SEB RM -NA RFS FPCR'
0000EDF0	00080001 00080001			2000 DC XL16'00080001000800010000000000000000'
0000EE00	E2C5C2D9 61E2C5C2			2001 DC CL48'SEBR/SEB RM +TZ RNTE, RZ FPCR'
0000EE30	00080002 00080002			2002 DC XL16'00080002000800020008000200080002'
0000EE40	E2C5C2D9 61E2C5C2			2003 DC CL48'SEBR/SEB RM +TZ RP, RM FPCR'
0000EE70	00080002 00080002			2004 DC XL16'00080002000800020008000200080002'
0000EE80	E2C5C2D9 61E2C5C2			2005 DC CL48'SEBR/SEB RM +TZ RFS FPCR'
0000EEB0	00080002 00080002			2006 DC XL16'00080002000800020000000000000000'
0000EEC0	E2C5C2D9 61E2C5C2			2007 DC CL48'SEBR/SEB RM -TZ RNTE, RZ FPCR'
0000EEF0	00080001 00080001			2008 DC XL16'00080001000800010008000100080001'
0000EF00	E2C5C2D9 61E2C5C2			2009 DC CL48'SEBR/SEB RM -TZ RP, RM FPCR'
0000EF30	00080001 00080001			2010 DC XL16'00080001000800010008000100080001'
0000EF40	E2C5C2D9 61E2C5C2			2011 DC CL48'SEBR/SEB RM -TZ RFS FPCR'
0000EF70	00080001 00080001			2012 DC XL16'00080001000800010000000000000000'
0000EF80	E2C5C2D9 61E2C5C2			2013 DC CL48'SEBR/SEB RM +TA RNTE, RZ FPCR'
0000EFB0	00080002 00080002			2014 DC XL16'00080002000800020008000200080002'
0000EFC0	E2C5C2D9 61E2C5C2			2015 DC CL48'SEBR/SEB RM +TA RP, RM FPCR'
0000EFF0	00080002 00080002			2016 DC XL16'00080002000800020008000200080002'
0000F000	E2C5C2D9 61E2C5C2			2017 DC CL48'SEBR/SEB RM +TA RFS FPCR'
0000F030	00080002 00080002			2018 DC XL16'00080002000800020000000000000000'
0000F040	E2C5C2D9 61E2C5C2			2019 DC CL48'SEBR/SEB RM -TA RNTE, RZ FPCR'
0000F070	00080001 00080001			2020 DC XL16'00080001000800010008000100080001'
0000F080	E2C5C2D9 61E2C5C2			2021 DC CL48'SEBR/SEB RM -TA RP, RM FPCR'
0000F0B0	00080001 00080001			2022 DC XL16'00080001000800010008000100080001'
0000F0C0	E2C5C2D9 61E2C5C2			2023 DC CL48'SEBR/SEB RM -TA RFS FPCR'
0000F0F0	00080001 00080001			2024 DC XL16'00080001000800010000000000000000'
		00000018	00000001	2025 SBFPRMOF_NUM EQU (*-SBFPRMOF_GOOD)/64
				2026 *
				2027 *
		0000F100	00000001	2028 LBFPNFOT_GOOD EQU *
0000F100	E2C4C2D9 40D5C640			2029 DC CL48'SDBR NF -inf/-inf'
0000F130	7FF80000 00000000			2030 DC XL16'7FF8000000000000FFF0000000000000'
0000F140	E2C4C240 D5C64060			2031 DC CL48'SDB NF -inf/-inf'
0000F170	7FF80000 00000000			2032 DC XL16'7FF8000000000000FFF0000000000000'
0000F180	E2C4C2D9 40D5C640			2033 DC CL48'SDBR NF -inf/-2.0'
0000F1B0	FFF00000 00000000			2034 DC XL16'FFF0000000000000FFF0000000000000'
0000F1C0	E2C4C240 D5C64060			2035 DC CL48'SDB NF -inf/-2.0'
0000F1F0	FFF00000 00000000			2036 DC XL16'FFF0000000000000FFF0000000000000'
0000F200	E2C4C2D9 40D5C640			2037 DC CL48'SDBR NF -inf/-Dnice'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
0000F230	FFF00000	00000000		2038 DC XL16'FFF0000000000000FFF0000000000000'
0000F240	E2C4C240	D5C64060		2039 DC CL48'SDB NF -inf/-Dnice'
0000F270	FFF00000	00000000		2040 DC XL16'FFF0000000000000FFF0000000000000'
0000F280	E2C4C2D9	40D5C640		2041 DC CL48'SDBR NF -inf/-0'
0000F2B0	FFF00000	00000000		2042 DC XL16'FFF0000000000000FFF0000000000000'
0000F2C0	E2C4C240	D5C64060		2043 DC CL48'SDB NF -inf/-0'
0000F2F0	FFF00000	00000000		2044 DC XL16'FFF0000000000000FFF0000000000000'
0000F300	E2C4C2D9	40D5C640		2045 DC CL48'SDBR NF -inf/+0'
0000F330	FFF00000	00000000		2046 DC XL16'FFF0000000000000FFF0000000000000'
0000F340	E2C4C240	D5C64060		2047 DC CL48'SDB NF -inf/+0'
0000F370	FFF00000	00000000		2048 DC XL16'FFF0000000000000FFF0000000000000'
0000F380	E2C4C2D9	40D5C640		2049 DC CL48'SDBR NF -inf/+Dnice'
0000F3B0	FFF00000	00000000		2050 DC XL16'FFF0000000000000FFF0000000000000'
0000F3C0	E2C4C240	D5C64060		2051 DC CL48'SDB NF -inf/+Dnice'
0000F3F0	FFF00000	00000000		2052 DC XL16'FFF0000000000000FFF0000000000000'
0000F400	E2C4C2D9	40D5C640		2053 DC CL48'SDBR NF -inf/+2.0'
0000F430	FFF00000	00000000		2054 DC XL16'FFF0000000000000FFF0000000000000'
0000F440	E2C4C240	D5C64060		2055 DC CL48'SDB NF -inf/+2.0'
0000F470	FFF00000	00000000		2056 DC XL16'FFF0000000000000FFF0000000000000'
0000F480	E2C4C2D9	40D5C640		2057 DC CL48'SDBR NF -inf/+inf'
0000F4B0	FFF00000	00000000		2058 DC XL16'FFF0000000000000FFF0000000000000'
0000F4C0	E2C4C240	D5C64060		2059 DC CL48'SDB NF -inf/+inf'
0000F4F0	FFF00000	00000000		2060 DC XL16'FFF0000000000000FFF0000000000000'
0000F500	E2C4C2D9	40D5C640		2061 DC CL48'SDBR NF -inf/-QNaN'
0000F530	FFF8B000	00000000		2062 DC XL16'FFF8B00000000000FFF8B00000000000'
0000F540	E2C4C240	D5C64060		2063 DC CL48'SDB NF -inf/-QNaN'
0000F570	FFF8B000	00000000		2064 DC XL16'FFF8B00000000000FFF8B00000000000'
0000F580	E2C4C2D9	40D5C640		2065 DC CL48'SDBR NF -inf/+SNaN'
0000F5B0	7FF8A000	00000000		2066 DC XL16'7FF8A00000000000FFF0000000000000'
0000F5C0	E2C4C240	D5C64060		2067 DC CL48'SDB NF -inf/+SNaN'
0000F5F0	7FF8A000	00000000		2068 DC XL16'7FF8A00000000000FFF0000000000000'
0000F600	E2C4C2D9	40D5C640		2069 DC CL48'SDBR NF -2.0/-inf'
0000F630	7FF00000	00000000		2070 DC XL16'7FF00000000000007FF0000000000000'
0000F640	E2C4C240	D5C64060		2071 DC CL48'SDB NF -2.0/-inf'
0000F670	7FF00000	00000000		2072 DC XL16'7FF00000000000007FF0000000000000'
0000F680	E2C4C2D9	40D5C640		2073 DC CL48'SDBR NF -2.0/-2.0'
0000F6B0	00000000	00000000		2074 DC XL16'000000000000000000000000000000'
0000F6C0	E2C4C240	D5C64060		2075 DC CL48'SDB NF -2.0/-2.0'
0000F6F0	00000000	00000000		2076 DC XL16'000000000000000000000000000000'
0000F700	E2C4C2D9	40D5C640		2077 DC CL48'SDBR NF -2.0/-Dnice'
0000F730	C0000000	00000000		2078 DC XL16'C000000000000000C000000000000000'
0000F740	E2C4C240	D5C64060		2079 DC CL48'SDB NF -2.0/-Dnice'
0000F770	C0000000	00000000		2080 DC XL16'C000000000000000C000000000000000'
0000F780	E2C4C2D9	40D5C640		2081 DC CL48'SDBR NF -2.0/-0'
0000F7B0	C0000000	00000000		2082 DC XL16'C000000000000000C000000000000000'
0000F7C0	E2C4C240	D5C64060		2083 DC CL48'SDB NF -2.0/-0'
0000F7F0	C0000000	00000000		2084 DC XL16'C000000000000000C000000000000000'
0000F800	E2C4C2D9	40D5C640		2085 DC CL48'SDBR NF -2.0/+0'
0000F830	C0000000	00000000		2086 DC XL16'C000000000000000C000000000000000'
0000F840	E2C4C240	D5C64060		2087 DC CL48'SDB NF -2.0/+0'
0000F870	C0000000	00000000		2088 DC XL16'C000000000000000C000000000000000'
0000F880	E2C4C2D9	40D5C640		2089 DC CL48'SDBR NF -2.0/+Dnice'
0000F8B0	C0000000	00000000		2090 DC XL16'C000000000000000C000000000000000'
0000F8C0	E2C4C240	D5C64060		2091 DC CL48'SDB NF -2.0/+Dnice'
0000F8F0	C0000000	00000000		2092 DC XL16'C000000000000000C000000000000000'
0000F900	E2C4C2D9	40D5C640		2093 DC CL48'SDBR NF -2.0/+2.0'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
0000F930	C0100000 00000000			2094 DC XL16'C010000000000000C01000000000000'
0000F940	E2C4C240 D5C64060			2095 DC CL48'SDB NF -2.0/+2.0'
0000F970	C0100000 00000000			2096 DC XL16'C010000000000000C01000000000000'
0000F980	E2C4C2D9 40D5C640			2097 DC CL48'SDBR NF -2.0/+inf'
0000F9B0	FFF00000 00000000			2098 DC XL16'FFF0000000000000FFF0000000000000'
0000F9C0	E2C4C240 D5C64060			2099 DC CL48'SDB NF -2.0/+inf'
0000F9F0	FFF00000 00000000			2100 DC XL16'FFF0000000000000FFF0000000000000'
0000FA00	E2C4C2D9 40D5C640			2101 DC CL48'SDBR NF -2.0/-QNaN'
0000FA30	FFF8B000 00000000			2102 DC XL16'FFF8B00000000000FFF8B00000000000'
0000FA40	E2C4C240 D5C64060			2103 DC CL48'SDB NF -2.0/-QNaN'
0000FA70	FFF8B000 00000000			2104 DC XL16'FFF8B00000000000FFF8B00000000000'
0000FA80	E2C4C2D9 40D5C640			2105 DC CL48'SDBR NF -2.0/+SNaN'
0000FAB0	7FF8A000 00000000			2106 DC XL16'7FF8A00000000000C0000000000000'
0000FAC0	E2C4C240 D5C64060			2107 DC CL48'SDB NF -2.0/+SNaN'
0000FAF0	7FF8A000 00000000			2108 DC XL16'7FF8A00000000000C0000000000000'
0000FB00	E2C4C2D9 40D5C640			2109 DC CL48'SDBR NF -Dnice/-inf'
0000FB30	7FF00000 00000000			2110 DC XL16'7FF00000000000007FF0000000000000'
0000FB40	E2C4C240 D5C64060			2111 DC CL48'SDB NF -Dnice/-inf'
0000FB70	7FF00000 00000000			2112 DC XL16'7FF00000000000007FF0000000000000'
0000FB80	E2C4C2D9 40D5C640			2113 DC CL48'SDBR NF -Dnice/-2.0'
0000FBB0	40000000 00000000			2114 DC XL16'40000000000000004000000000000000'
0000FBC0	E2C4C240 D5C64060			2115 DC CL48'SDB NF -Dnice/-2.0'
0000FBF0	40000000 00000000			2116 DC XL16'40000000000000004000000000000000'
0000FC00	E2C4C2D9 40D5C640			2117 DC CL48'SDBR NF -Dnice/-Dnice'
0000FC30	00000000 00000000			2118 DC XL16'00000000000000000000000000000000'
0000FC40	E2C4C240 D5C64060			2119 DC CL48'SDB NF -Dnice/-Dnice'
0000FC70	00000000 00000000			2120 DC XL16'00000000000000000000000000000000'
0000FC80	E2C4C2D9 40D5C640			2121 DC CL48'SDBR NF -Dnice/-0'
0000FCB0	80010000 00000000			2122 DC XL16'8001000000000000DFD0000000000000'
0000FCC0	E2C4C240 D5C64060			2123 DC CL48'SDB NF -Dnice/-0'
0000FCF0	80010000 00000000			2124 DC XL16'8001000000000000DFD0000000000000'
0000FD00	E2C4C2D9 40D5C640			2125 DC CL48'SDBR NF -Dnice/+0'
0000FD30	80010000 00000000			2126 DC XL16'8001000000000000DFD0000000000000'
0000FD40	E2C4C240 D5C64060			2127 DC CL48'SDB NF -Dnice/+0'
0000FD70	80010000 00000000			2128 DC XL16'8001000000000000DFD0000000000000'
0000FD80	E2C4C2D9 40D5C640			2129 DC CL48'SDBR NF -Dnice/+Dnice'
0000FDB0	80020000 00000000			2130 DC XL16'8002000000000000DFE0000000000000'
0000FDC0	E2C4C240 D5C64060			2131 DC CL48'SDB NF -Dnice/+Dnice'
0000FDF0	80020000 00000000			2132 DC XL16'8002000000000000DFE0000000000000'
0000FE00	E2C4C2D9 40D5C640			2133 DC CL48'SDBR NF -Dnice/+2.0'
0000FE30	C0000000 00000000			2134 DC XL16'C000000000000000C000000000000000'
0000FE40	E2C4C240 D5C64060			2135 DC CL48'SDB NF -Dnice/+2.0'
0000FE70	C0000000 00000000			2136 DC XL16'C000000000000000C000000000000000'
0000FE80	E2C4C2D9 40D5C640			2137 DC CL48'SDBR NF -Dnice/+inf'
0000FEB0	FFF00000 00000000			2138 DC XL16'FFF0000000000000FFF0000000000000'
0000FEC0	E2C4C240 D5C64060			2139 DC CL48'SDB NF -Dnice/+inf'
0000FEF0	FFF00000 00000000			2140 DC XL16'FFF0000000000000FFF0000000000000'
0000FF00	E2C4C2D9 40D5C640			2141 DC CL48'SDBR NF -Dnice/-QNaN'
0000FF30	FFF8B000 00000000			2142 DC XL16'FFF8B00000000000FFF8B00000000000'
0000FF40	E2C4C240 D5C64060			2143 DC CL48'SDB NF -Dnice/-QNaN'
0000FF70	FFF8B000 00000000			2144 DC XL16'FFF8B00000000000FFF8B00000000000'
0000FF80	E2C4C2D9 40D5C640			2145 DC CL48'SDBR NF -Dnice/+SNaN'
0000FFB0	7FF8A000 00000000			2146 DC XL16'7FF8A000000000008001000000000000'
0000FFC0	E2C4C240 D5C64060			2147 DC CL48'SDB NF -Dnice/+SNaN'
0000FFF0	7FF8A000 00000000			2148 DC XL16'7FF8A000000000008001000000000000'
00010000	E2C4C2D9 40D5C640			2149 DC CL48'SDBR NF -0/-inf'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
00010030	7FF00000 00000000			2150 DC XL16'7FF00000000000007FF0000000000000'
00010040	E2C4C240 D5C64060			2151 DC CL48'SDB NF -0/-inf'
00010070	7FF00000 00000000			2152 DC XL16'7FF00000000000007FF0000000000000'
00010080	E2C4C2D9 40D5C640			2153 DC CL48'SDBR NF -0/-2.0'
000100B0	40000000 00000000			2154 DC XL16'40000000000000004000000000000000'
000100C0	E2C4C240 D5C64060			2155 DC CL48'SDB NF -0/-2.0'
000100F0	40000000 00000000			2156 DC XL16'40000000000000004000000000000000'
00010100	E2C4C2D9 40D5C640			2157 DC CL48'SDBR NF -0/-Dnice'
00010130	00010000 00000000			2158 DC XL16'00010000000000005FD00000000000000'
00010140	E2C4C240 D5C64060			2159 DC CL48'SDB NF -0/-Dnice'
00010170	00010000 00000000			2160 DC XL16'00010000000000005FD00000000000000'
00010180	E2C4C2D9 40D5C640			2161 DC CL48'SDBR NF -0/-0'
000101B0	00000000 00000000			2162 DC XL16'00000000000000000000000000000000'
000101C0	E2C4C240 D5C64060			2163 DC CL48'SDB NF -0/-0'
000101F0	00000000 00000000			2164 DC XL16'00000000000000000000000000000000'
00010200	E2C4C2D9 40D5C640			2165 DC CL48'SDBR NF -0/+0'
00010230	80000000 00000000			2166 DC XL16'80000000000000008000000000000000'
00010240	E2C4C240 D5C64060			2167 DC CL48'SDB NF -0/+0'
00010270	80000000 00000000			2168 DC XL16'80000000000000008000000000000000'
00010280	E2C4C2D9 40D5C640			2169 DC CL48'SDBR NF -0/+Dnice'
000102B0	80010000 00000000			2170 DC XL16'8001000000000000DFD000000000000000'
000102C0	E2C4C240 D5C64060			2171 DC CL48'SDB NF -0/+Dnice'
000102F0	80010000 00000000			2172 DC XL16'8001000000000000DFD000000000000000'
00010300	E2C4C2D9 40D5C640			2173 DC CL48'SDBR NF -0/+2.0'
00010330	C0000000 00000000			2174 DC XL16'C000000000000000C000000000000000'
00010340	E2C4C240 D5C64060			2175 DC CL48'SDB NF -0/+2.0'
00010370	C0000000 00000000			2176 DC XL16'C000000000000000C000000000000000'
00010380	E2C4C2D9 40D5C640			2177 DC CL48'SDBR NF -0/+inf'
000103B0	FFF00000 00000000			2178 DC XL16'FFF0000000000000FFF000000000000000'
000103C0	E2C4C240 D5C64060			2179 DC CL48'SDB NF -0/+inf'
000103F0	FFF00000 00000000			2180 DC XL16'FFF0000000000000FFF000000000000000'
00010400	E2C4C2D9 40D5C640			2181 DC CL48'SDBR NF -0/-QNaN'
00010430	FFF8B000 00000000			2182 DC XL16'FFF8B00000000000FFF8B0000000000000'
00010440	E2C4C240 D5C64060			2183 DC CL48'SDB NF -0/-QNaN'
00010470	FFF8B000 00000000			2184 DC XL16'FFF8B00000000000FFF8B0000000000000'
00010480	E2C4C2D9 40D5C640			2185 DC CL48'SDBR NF -0/+SNaN'
000104B0	7FF8A000 00000000			2186 DC XL16'7FF8A000000000008000000000000000'
000104C0	E2C4C240 D5C64060			2187 DC CL48'SDB NF -0/+SNaN'
000104F0	7FF8A000 00000000			2188 DC XL16'7FF8A000000000008000000000000000'
00010500	E2C4C2D9 40D5C640			2189 DC CL48'SDBR NF +0/-inf'
00010530	7FF00000 00000000			2190 DC XL16'7FF00000000000007FF00000000000000'
00010540	E2C4C240 D5C6404E			2191 DC CL48'SDB NF +0/-inf'
00010570	7FF00000 00000000			2192 DC XL16'7FF00000000000007FF00000000000000'
00010580	E2C4C2D9 40D5C640			2193 DC CL48'SDBR NF +0/-2.0'
000105B0	40000000 00000000			2194 DC XL16'40000000000000004000000000000000'
000105C0	E2C4C240 D5C6404E			2195 DC CL48'SDB NF +0/-2.0'
000105F0	40000000 00000000			2196 DC XL16'40000000000000004000000000000000'
00010600	E2C4C2D9 40D5C640			2197 DC CL48'SDBR NF +0/-Dnice'
00010630	00010000 00000000			2198 DC XL16'00010000000000005FD00000000000000'
00010640	E2C4C240 D5C6404E			2199 DC CL48'SDB NF +0/-Dnice'
00010670	00010000 00000000			2200 DC XL16'00010000000000005FD00000000000000'
00010680	E2C4C2D9 40D5C640			2201 DC CL48'SDBR NF +0/-0'
000106B0	00000000 00000000			2202 DC XL16'00000000000000000000000000000000'
000106C0	E2C4C240 D5C6404E			2203 DC CL48'SDB NF +0/-0'
000106F0	00000000 00000000			2204 DC XL16'00000000000000000000000000000000'
00010700	E2C4C2D9 40D5C640			2205 DC CL48'SDBR NF +0/+0'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
00010730	00000000 00000000			2206 DC XL16'00000000000000000000000000000000'
00010740	E2C4C240 D5C6404E			2207 DC CL48'SDB NF +0/+0'
00010770	00000000 00000000			2208 DC XL16'00000000000000000000000000000000'
00010780	E2C4C2D9 40D5C640			2209 DC CL48'SDBR NF +0/+Dnice'
000107B0	80010000 00000000			2210 DC XL16'8001000000000000DFD0000000000000'
000107C0	E2C4C240 D5C6404E			2211 DC CL48'SDB NF +0/+Dnice'
000107F0	80010000 00000000			2212 DC XL16'8001000000000000DFD0000000000000'
00010800	E2C4C2D9 40D5C640			2213 DC CL48'SDBR NF +0/+2.0'
00010830	C0000000 00000000			2214 DC XL16'C000000000000000C000000000000000'
00010840	E2C4C240 D5C6404E			2215 DC CL48'SDB NF +0/+2.0'
00010870	C0000000 00000000			2216 DC XL16'C000000000000000C000000000000000'
00010880	E2C4C2D9 40D5C640			2217 DC CL48'SDBR NF +0/+inf'
000108B0	FFF00000 00000000			2218 DC XL16'FFF0000000000000FFF0000000000000'
000108C0	E2C4C240 D5C6404E			2219 DC CL48'SDB NF +0/+inf'
000108F0	FFF00000 00000000			2220 DC XL16'FFF0000000000000FFF0000000000000'
00010900	E2C4C2D9 40D5C640			2221 DC CL48'SDBR NF +0/-QNaN'
00010930	FFF8B000 00000000			2222 DC XL16'FFF8B00000000000FFF8B00000000000'
00010940	E2C4C240 D5C6404E			2223 DC CL48'SDB NF +0/-QNaN'
00010970	FFF8B000 00000000			2224 DC XL16'FFF8B00000000000FFF8B00000000000'
00010980	E2C4C2D9 40D5C640			2225 DC CL48'SDBR NF +0/+SNaN'
000109B0	7FF8A000 00000000			2226 DC XL16'7FF8A000000000000000000000000000'
000109C0	E2C4C240 D5C6404E			2227 DC CL48'SDB NF +0/+SNaN'
000109F0	7FF8A000 00000000			2228 DC XL16'7FF8A000000000000000000000000000'
00010A00	E2C4C2D9 40D5C640			2229 DC CL48'SDBR NF +Dnice/-inf'
00010A30	7FF00000 00000000			2230 DC XL16'7FF00000000000007FF0000000000000'
00010A40	E2C4C240 D5C6404E			2231 DC CL48'SDB NF +Dnice/-inf'
00010A70	7FF00000 00000000			2232 DC XL16'7FF00000000000007FF0000000000000'
00010A80	E2C4C2D9 40D5C640			2233 DC CL48'SDBR NF +Dnice/-2.0'
00010AB0	40000000 00000000			2234 DC XL16'40000000000000004000000000000000'
00010AC0	E2C4C240 D5C6404E			2235 DC CL48'SDB NF +Dnice/-2.0'
00010AF0	40000000 00000000			2236 DC XL16'40000000000000004000000000000000'
00010B00	E2C4C2D9 40D5C640			2237 DC CL48'SDBR NF +Dnice/-Dnice'
00010B30	00020000 00000000			2238 DC XL16'000200000000000005FE0000000000000'
00010B40	E2C4C240 D5C6404E			2239 DC CL48'SDB NF +Dnice/-Dnice'
00010B70	00020000 00000000			2240 DC XL16'000200000000000005FE0000000000000'
00010B80	E2C4C2D9 40D5C640			2241 DC CL48'SDBR NF +Dnice/-0'
00010BB0	00010000 00000000			2242 DC XL16'000100000000000005FD0000000000000'
00010BC0	E2C4C240 D5C6404E			2243 DC CL48'SDB NF +Dnice/-0'
00010BF0	00010000 00000000			2244 DC XL16'000100000000000005FD0000000000000'
00010C00	E2C4C2D9 40D5C640			2245 DC CL48'SDBR NF +Dnice/+0'
00010C30	00010000 00000000			2246 DC XL16'000100000000000005FD0000000000000'
00010C40	E2C4C240 D5C6404E			2247 DC CL48'SDB NF +Dnice/+0'
00010C70	00010000 00000000			2248 DC XL16'000100000000000005FD0000000000000'
00010C80	E2C4C2D9 40D5C640			2249 DC CL48'SDBR NF +Dnice/+Dnice'
00010CB0	00000000 00000000			2250 DC XL16'00000000000000000000000000000000'
00010CC0	E2C4C240 D5C6404E			2251 DC CL48'SDB NF +Dnice/+Dnice'
00010CF0	00000000 00000000			2252 DC XL16'00000000000000000000000000000000'
00010D00	E2C4C2D9 40D5C640			2253 DC CL48'SDBR NF +Dnice/+2.0'
00010D30	C0000000 00000000			2254 DC XL16'C000000000000000C000000000000000'
00010D40	E2C4C240 D5C6404E			2255 DC CL48'SDB NF +Dnice/+2.0'
00010D70	C0000000 00000000			2256 DC XL16'C000000000000000C000000000000000'
00010D80	E2C4C2D9 40D5C640			2257 DC CL48'SDBR NF +Dnice/+inf'
00010DB0	FFF00000 00000000			2258 DC XL16'FFF0000000000000FFF0000000000000'
00010DC0	E2C4C240 D5C6404E			2259 DC CL48'SDB NF +Dnice/+inf'
00010DF0	FFF00000 00000000			2260 DC XL16'FFF0000000000000FFF0000000000000'
00010E00	E2C4C2D9 40D5C640			2261 DC CL48'SDBR NF +Dnice/-QNaN'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
00010E30	FFF8B000 00000000			2262	DC XL16'FFF8B0000000000000FFF8B00000000000'
00010E40	E2C4C240 D5C6404E			2263	DC CL48'SDB NF +Dnice/-QNaN'
00010E70	FFF8B000 00000000			2264	DC XL16'FFF8B0000000000000FFF8B00000000000'
00010E80	E2C4C2D9 40D5C640			2265	DC CL48'SDBR NF +Dnice/+SNaN'
00010EB0	7FF8A000 00000000			2266	DC XL16'7FF8A000000000000001000000000000'
00010EC0	E2C4C240 D5C6404E			2267	DC CL48'SDB NF +Dnice/+SNaN'
00010EF0	7FF8A000 00000000			2268	DC XL16'7FF8A000000000000001000000000000'
00010F00	E2C4C2D9 40D5C640			2269	DC CL48'SDBR NF +2.0/-inf'
00010F30	7FF00000 00000000			2270	DC XL16'7FF000000000000007FF0000000000000'
00010F40	E2C4C240 D5C6404E			2271	DC CL48'SDB NF +2.0/-inf'
00010F70	7FF00000 00000000			2272	DC XL16'7FF000000000000007FF0000000000000'
00010F80	E2C4C2D9 40D5C640			2273	DC CL48'SDBR NF +2.0/-2.0'
00010FB0	40100000 00000000			2274	DC XL16'40100000000000000401000000000000'
00010FC0	E2C4C240 D5C6404E			2275	DC CL48'SDB NF +2.0/-2.0'
00010FF0	40100000 00000000			2276	DC XL16'40100000000000000401000000000000'
00011000	E2C4C2D9 40D5C640			2277	DC CL48'SDBR NF +2.0/-Dnice'
00011030	40000000 00000000			2278	DC XL16'40000000000000000400000000000000'
00011040	E2C4C240 D5C6404E			2279	DC CL48'SDB NF +2.0/-Dnice'
00011070	40000000 00000000			2280	DC XL16'40000000000000000400000000000000'
00011080	E2C4C2D9 40D5C640			2281	DC CL48'SDBR NF +2.0/-0'
000110B0	40000000 00000000			2282	DC XL16'40000000000000000400000000000000'
000110C0	E2C4C240 D5C6404E			2283	DC CL48'SDB NF +2.0/-0'
000110F0	40000000 00000000			2284	DC XL16'40000000000000000400000000000000'
00011100	E2C4C2D9 40D5C640			2285	DC CL48'SDBR NF +2.0/+0'
00011130	40000000 00000000			2286	DC XL16'40000000000000000400000000000000'
00011140	E2C4C240 D5C6404E			2287	DC CL48'SDB NF +2.0/+0'
00011170	40000000 00000000			2288	DC XL16'40000000000000000400000000000000'
00011180	E2C4C2D9 40D5C640			2289	DC CL48'SDBR NF +2.0/+Dnice'
000111B0	40000000 00000000			2290	DC XL16'40000000000000000400000000000000'
000111C0	E2C4C240 D5C6404E			2291	DC CL48'SDB NF +2.0/+Dnice'
000111F0	40000000 00000000			2292	DC XL16'40000000000000000400000000000000'
00011200	E2C4C2D9 40D5C640			2293	DC CL48'SDBR NF +2.0/+2.0'
00011230	00000000 00000000			2294	DC XL16'00000000000000000000000000000000'
00011240	E2C4C240 D5C6404E			2295	DC CL48'SDB NF +2.0/+2.0'
00011270	00000000 00000000			2296	DC XL16'00000000000000000000000000000000'
00011280	E2C4C2D9 40D5C640			2297	DC CL48'SDBR NF +2.0/+inf'
000112B0	FFF00000 00000000			2298	DC XL16'FFF0000000000000FFF0000000000000'
000112C0	E2C4C240 D5C6404E			2299	DC CL48'SDB NF +2.0/+inf'
000112F0	FFF00000 00000000			2300	DC XL16'FFF0000000000000FFF0000000000000'
00011300	E2C4C2D9 40D5C640			2301	DC CL48'SDBR NF +2.0/-QNaN'
00011330	FFF8B000 00000000			2302	DC XL16'FFF8B0000000000000FFF8B00000000000'
00011340	E2C4C240 D5C6404E			2303	DC CL48'SDB NF +2.0/-QNaN'
00011370	FFF8B000 00000000			2304	DC XL16'FFF8B0000000000000FFF8B00000000000'
00011380	E2C4C2D9 40D5C640			2305	DC CL48'SDBR NF +2.0/+SNaN'
000113B0	7FF8A000 00000000			2306	DC XL16'7FF8A000000000000400000000000000'
000113C0	E2C4C240 D5C6404E			2307	DC CL48'SDB NF +2.0/+SNaN'
000113F0	7FF8A000 00000000			2308	DC XL16'7FF8A000000000000400000000000000'
00011400	E2C4C2D9 40D5C640			2309	DC CL48'SDBR NF +inf/-inf'
00011430	7FF00000 00000000			2310	DC XL16'7FF000000000000007FF0000000000000'
00011440	E2C4C240 D5C6404E			2311	DC CL48'SDB NF +inf/-inf'
00011470	7FF00000 00000000			2312	DC XL16'7FF000000000000007FF0000000000000'
00011480	E2C4C2D9 40D5C640			2313	DC CL48'SDBR NF +inf/-2.0'
000114B0	7FF00000 00000000			2314	DC XL16'7FF000000000000007FF0000000000000'
000114C0	E2C4C240 D5C6404E			2315	DC CL48'SDB NF +inf/-2.0'
000114F0	7FF00000 00000000			2316	DC XL16'7FF000000000000007FF0000000000000'
00011500	E2C4C2D9 40D5C640			2317	DC CL48'SDBR NF +inf/-Dnice'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
00011530	7FF00000 00000000			2318 DC XL16'7FF00000000000007FF0000000000000'
00011540	E2C4C240 D5C6404E			2319 DC CL48'SDB NF +inf/-Dnice'
00011570	7FF00000 00000000			2320 DC XL16'7FF00000000000007FF0000000000000'
00011580	E2C4C2D9 40D5C640			2321 DC CL48'SDBR NF +inf/-0'
000115B0	7FF00000 00000000			2322 DC XL16'7FF00000000000007FF0000000000000'
000115C0	E2C4C240 D5C6404E			2323 DC CL48'SDB NF +inf/-0'
000115F0	7FF00000 00000000			2324 DC XL16'7FF00000000000007FF0000000000000'
00011600	E2C4C2D9 40D5C640			2325 DC CL48'SDBR NF +inf/+0'
00011630	7FF00000 00000000			2326 DC XL16'7FF00000000000007FF0000000000000'
00011640	E2C4C240 D5C6404E			2327 DC CL48'SDB NF +inf/+0'
00011670	7FF00000 00000000			2328 DC XL16'7FF00000000000007FF0000000000000'
00011680	E2C4C2D9 40D5C640			2329 DC CL48'SDBR NF +inf/+Dnice'
000116B0	7FF00000 00000000			2330 DC XL16'7FF00000000000007FF0000000000000'
000116C0	E2C4C240 D5C6404E			2331 DC CL48'SDB NF +inf/+Dnice'
000116F0	7FF00000 00000000			2332 DC XL16'7FF00000000000007FF0000000000000'
00011700	E2C4C2D9 40D5C640			2333 DC CL48'SDBR NF +inf/+2.0'
00011730	7FF00000 00000000			2334 DC XL16'7FF00000000000007FF0000000000000'
00011740	E2C4C240 D5C6404E			2335 DC CL48'SDB NF +inf/+2.0'
00011770	7FF00000 00000000			2336 DC XL16'7FF00000000000007FF0000000000000'
00011780	E2C4C2D9 40D5C640			2337 DC CL48'SDBR NF +inf/+inf'
000117B0	7FF80000 00000000			2338 DC XL16'7FF80000000000007FF0000000000000'
000117C0	E2C4C240 D5C6404E			2339 DC CL48'SDB NF +inf/+inf'
000117F0	7FF80000 00000000			2340 DC XL16'7FF80000000000007FF0000000000000'
00011800	E2C4C2D9 40D5C640			2341 DC CL48'SDBR NF +inf/-QNaN'
00011830	FFF8B000 00000000			2342 DC XL16'FFF8B00000000000FFF8B00000000000'
00011840	E2C4C240 D5C6404E			2343 DC CL48'SDB NF +inf/-QNaN'
00011870	FFF8B000 00000000			2344 DC XL16'FFF8B00000000000FFF8B00000000000'
00011880	E2C4C2D9 40D5C640			2345 DC CL48'SDBR NF +inf/+SNaN'
000118B0	7FF8A000 00000000			2346 DC XL16'7FF8A000000000007FF0000000000000'
000118C0	E2C4C240 D5C6404E			2347 DC CL48'SDB NF +inf/+SNaN'
000118F0	7FF8A000 00000000			2348 DC XL16'7FF8A000000000007FF0000000000000'
00011900	E2C4C2D9 40D5C640			2349 DC CL48'SDBR NF -QNaN/-inf'
00011930	FFF8B000 00000000			2350 DC XL16'FFF8B00000000000FFF8B00000000000'
00011940	E2C4C240 D5C64060			2351 DC CL48'SDB NF -QNaN/-inf'
00011970	FFF8B000 00000000			2352 DC XL16'FFF8B00000000000FFF8B00000000000'
00011980	E2C4C2D9 40D5C640			2353 DC CL48'SDBR NF -QNaN/-2.0'
000119B0	FFF8B000 00000000			2354 DC XL16'FFF8B00000000000FFF8B00000000000'
000119C0	E2C4C240 D5C64060			2355 DC CL48'SDB NF -QNaN/-2.0'
000119F0	FFF8B000 00000000			2356 DC XL16'FFF8B00000000000FFF8B00000000000'
00011A00	E2C4C2D9 40D5C640			2357 DC CL48'SDBR NF -QNaN/-Dnice'
00011A30	FFF8B000 00000000			2358 DC XL16'FFF8B00000000000FFF8B00000000000'
00011A40	E2C4C240 D5C64060			2359 DC CL48'SDB NF -QNaN/-Dnice'
00011A70	FFF8B000 00000000			2360 DC XL16'FFF8B00000000000FFF8B00000000000'
00011A80	E2C4C2D9 40D5C640			2361 DC CL48'SDBR NF -QNaN/-0'
00011AB0	FFF8B000 00000000			2362 DC XL16'FFF8B00000000000FFF8B00000000000'
00011AC0	E2C4C240 D5C64060			2363 DC CL48'SDB NF -QNaN/-0'
00011AF0	FFF8B000 00000000			2364 DC XL16'FFF8B00000000000FFF8B00000000000'
00011B00	E2C4C2D9 40D5C640			2365 DC CL48'SDBR NF -QNaN/+0'
00011B30	FFF8B000 00000000			2366 DC XL16'FFF8B00000000000FFF8B00000000000'
00011B40	E2C4C240 D5C64060			2367 DC CL48'SDB NF -QNaN/+0'
00011B70	FFF8B000 00000000			2368 DC XL16'FFF8B00000000000FFF8B00000000000'
00011B80	E2C4C2D9 40D5C640			2369 DC CL48'SDBR NF -QNaN/+Dnice'
00011BB0	FFF8B000 00000000			2370 DC XL16'FFF8B00000000000FFF8B00000000000'
00011BC0	E2C4C240 D5C64060			2371 DC CL48'SDB NF -QNaN/+Dnice'
00011BF0	FFF8B000 00000000			2372 DC XL16'FFF8B00000000000FFF8B00000000000'
00011C00	E2C4C2D9 40D5C640			2373 DC CL48'SDBR NF -QNaN/+2.0'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
00011C30	FFF8B000 00000000			2374	DC XL16'FFF8B00000000000FFF8B0000000000'
00011C40	E2C4C240 D5C64060			2375	DC CL48'SDB NF -QNaN/+2.0'
00011C70	FFF8B000 00000000			2376	DC XL16'FFF8B00000000000FFF8B0000000000'
00011C80	E2C4C2D9 40D5C640			2377	DC CL48'SDBR NF -QNaN/+inf'
00011CB0	FFF8B000 00000000			2378	DC XL16'FFF8B00000000000FFF8B0000000000'
00011CC0	E2C4C240 D5C64060			2379	DC CL48'SDB NF -QNaN/+inf'
00011CF0	FFF8B000 00000000			2380	DC XL16'FFF8B00000000000FFF8B0000000000'
00011D00	E2C4C2D9 40D5C640			2381	DC CL48'SDBR NF -QNaN/-QNaN'
00011D30	FFF8B000 00000000			2382	DC XL16'FFF8B00000000000FFF8B0000000000'
00011D40	E2C4C240 D5C64060			2383	DC CL48'SDB NF -QNaN/-QNaN'
00011D70	FFF8B000 00000000			2384	DC XL16'FFF8B00000000000FFF8B0000000000'
00011D80	E2C4C2D9 40D5C640			2385	DC CL48'SDBR NF -QNaN/+SNaN'
00011DB0	7FF8A000 00000000			2386	DC XL16'7FF8A00000000000FFF8B0000000000'
00011DC0	E2C4C240 D5C64060			2387	DC CL48'SDB NF -QNaN/+SNaN'
00011DF0	7FF8A000 00000000			2388	DC XL16'7FF8A00000000000FFF8B0000000000'
00011E00	E2C4C2D9 40D5C640			2389	DC CL48'SDBR NF +SNaN/-inf'
00011E30	7FF8A000 00000000			2390	DC XL16'7FF8A000000000007FF0A0000000000'
00011E40	E2C4C240 D5C6404E			2391	DC CL48'SDB NF +SNaN/-inf'
00011E70	7FF8A000 00000000			2392	DC XL16'7FF8A000000000007FF0A0000000000'
00011E80	E2C4C2D9 40D5C640			2393	DC CL48'SDBR NF +SNaN/-2.0'
00011EB0	7FF8A000 00000000			2394	DC XL16'7FF8A000000000007FF0A0000000000'
00011EC0	E2C4C240 D5C6404E			2395	DC CL48'SDB NF +SNaN/-2.0'
00011EF0	7FF8A000 00000000			2396	DC XL16'7FF8A000000000007FF0A0000000000'
00011F00	E2C4C2D9 40D5C640			2397	DC CL48'SDBR NF +SNaN/-Dnice'
00011F30	7FF8A000 00000000			2398	DC XL16'7FF8A000000000007FF0A0000000000'
00011F40	E2C4C240 D5C6404E			2399	DC CL48'SDB NF +SNaN/-Dnice'
00011F70	7FF8A000 00000000			2400	DC XL16'7FF8A000000000007FF0A0000000000'
00011F80	E2C4C2D9 40D5C640			2401	DC CL48'SDBR NF +SNaN/-0'
00011FB0	7FF8A000 00000000			2402	DC XL16'7FF8A000000000007FF0A0000000000'
00011FC0	E2C4C240 D5C6404E			2403	DC CL48'SDB NF +SNaN/-0'
00011FF0	7FF8A000 00000000			2404	DC XL16'7FF8A000000000007FF0A0000000000'
00012000	E2C4C2D9 40D5C640			2405	DC CL48'SDBR NF +SNaN/+0'
00012030	7FF8A000 00000000			2406	DC XL16'7FF8A000000000007FF0A0000000000'
00012040	E2C4C240 D5C6404E			2407	DC CL48'SDB NF +SNaN/+0'
00012070	7FF8A000 00000000			2408	DC XL16'7FF8A000000000007FF0A0000000000'
00012080	E2C4C2D9 40D5C640			2409	DC CL48'SDBR NF +SNaN/+Dnice'
000120B0	7FF8A000 00000000			2410	DC XL16'7FF8A000000000007FF0A0000000000'
000120C0	E2C4C240 D5C6404E			2411	DC CL48'SDB NF +SNaN/+Dnice'
000120F0	7FF8A000 00000000			2412	DC XL16'7FF8A000000000007FF0A0000000000'
00012100	E2C4C2D9 40D5C640			2413	DC CL48'SDBR NF +SNaN/+2.0'
00012130	7FF8A000 00000000			2414	DC XL16'7FF8A000000000007FF0A0000000000'
00012140	E2C4C240 D5C6404E			2415	DC CL48'SDB NF +SNaN/+2.0'
00012170	7FF8A000 00000000			2416	DC XL16'7FF8A000000000007FF0A0000000000'
00012180	E2C4C2D9 40D5C640			2417	DC CL48'SDBR NF +SNaN/+inf'
000121B0	7FF8A000 00000000			2418	DC XL16'7FF8A000000000007FF0A0000000000'
000121C0	E2C4C240 D5C6404E			2419	DC CL48'SDB NF +SNaN/+inf'
000121F0	7FF8A000 00000000			2420	DC XL16'7FF8A000000000007FF0A0000000000'
00012200	E2C4C2D9 40D5C640			2421	DC CL48'SDBR NF +SNaN/-QNaN'
00012230	7FF8A000 00000000			2422	DC XL16'7FF8A000000000007FF0A0000000000'
00012240	E2C4C240 D5C6404E			2423	DC CL48'SDB NF +SNaN/-QNaN'
00012270	7FF8A000 00000000			2424	DC XL16'7FF8A000000000007FF0A0000000000'
00012280	E2C4C2D9 40D5C640			2425	DC CL48'SDBR NF +SNaN/+SNaN'
000122B0	7FF8A000 00000000			2426	DC XL16'7FF8A000000000007FF0A0000000000'
000122C0	E2C4C240 D5C6404E			2427	DC CL48'SDB NF +SNaN/+SNaN'
000122F0	7FF8A000 00000000			2428	DC XL16'7FF8A000000000007FF0A0000000000'
		000000C8	00000001	2429	LBFPNFOT_NUM EQU (*-LBFPNFOT_GOOD)/64

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				2430 *
				2431 *
		00012300	00000001	2432 LBFPNFFL_GOOD EQU *
00012300	E2C4C2D9	61E2C4C2		2433 DC CL48'SDBR/SDB NF -inf/-inf FPCR'
00012330	00800003	F8008003		2434 DC XL16'00800003F800800300800003F8008003'
00012340	E2C4C2D9	61E2C4C2		2435 DC CL48'SDBR/SDB NF -inf/-2.0 FPCR'
00012370	00000001	F8000001		2436 DC XL16'00000001F800000100000001F8000001'
00012380	E2C4C2D9	61E2C4C2		2437 DC CL48'SDBR/SDB NF -inf/-Dnice FPCR'
000123B0	00000001	F8000001		2438 DC XL16'00000001F800000100000001F8000001'
000123C0	E2C4C2D9	61E2C4C2		2439 DC CL48'SDBR/SDB NF -inf/-0 FPCR'
000123F0	00000001	F8000001		2440 DC XL16'00000001F800000100000001F8000001'
00012400	E2C4C2D9	61E2C4C2		2441 DC CL48'SDBR/SDB NF -inf/+0 FPCR'
00012430	00000001	F8000001		2442 DC XL16'00000001F800000100000001F8000001'
00012440	E2C4C2D9	61E2C4C2		2443 DC CL48'SDBR/SDB NF -inf/+Dnice FPCR'
00012470	00000001	F8000001		2444 DC XL16'00000001F800000100000001F8000001'
00012480	E2C4C2D9	61E2C4C2		2445 DC CL48'SDBR/SDB NF -inf/+2.0 FPCR'
000124B0	00000001	F8000001		2446 DC XL16'00000001F800000100000001F8000001'
000124C0	E2C4C2D9	61E2C4C2		2447 DC CL48'SDBR/SDB NF -inf/+inf FPCR'
000124F0	00000001	F8000001		2448 DC XL16'00000001F800000100000001F8000001'
00012500	E2C4C2D9	61E2C4C2		2449 DC CL48'SDBR/SDB NF -inf/-QNaN FPCR'
00012530	00000003	F8000003		2450 DC XL16'00000003F800000300000003F8000003'
00012540	E2C4C2D9	61E2C4C2		2451 DC CL48'SDBR/SDB NF -inf/+SNaN FPCR'
00012570	00800003	F8008003		2452 DC XL16'00800003F800800300800003F8008003'
00012580	E2C4C2D9	61E2C4C2		2453 DC CL48'SDBR/SDB NF -2.0/-inf FPCR'
000125B0	00000002	F8000002		2454 DC XL16'00000002F800000200000002F8000002'
000125C0	E2C4C2D9	61E2C4C2		2455 DC CL48'SDBR/SDB NF -2.0/-2.0 FPCR'
000125F0	00000000	F8000000		2456 DC XL16'00000000F800000000000000F8000000'
00012600	E2C4C2D9	61E2C4C2		2457 DC CL48'SDBR/SDB NF -2.0/-Dnice FPCR'
00012630	00080001	F8000C01		2458 DC XL16'00080001F8000C0100080001F8000C01'
00012640	E2C4C2D9	61E2C4C2		2459 DC CL48'SDBR/SDB NF -2.0/-0 FPCR'
00012670	00000001	F8000001		2460 DC XL16'00000001F800000100000001F8000001'
00012680	E2C4C2D9	61E2C4C2		2461 DC CL48'SDBR/SDB NF -2.0/+0 FPCR'
000126B0	00000001	F8000001		2462 DC XL16'00000001F800000100000001F8000001'
000126C0	E2C4C2D9	61E2C4C2		2463 DC CL48'SDBR/SDB NF -2.0/+Dnice FPCR'
000126F0	00080001	F8000801		2464 DC XL16'00080001F800080100080001F8000801'
00012700	E2C4C2D9	61E2C4C2		2465 DC CL48'SDBR/SDB NF -2.0/+2.0 FPCR'
00012730	00000001	F8000001		2466 DC XL16'00000001F800000100000001F8000001'
00012740	E2C4C2D9	61E2C4C2		2467 DC CL48'SDBR/SDB NF -2.0/+inf FPCR'
00012770	00000001	F8000001		2468 DC XL16'00000001F800000100000001F8000001'
00012780	E2C4C2D9	61E2C4C2		2469 DC CL48'SDBR/SDB NF -2.0/-QNaN FPCR'
000127B0	00000003	F8000003		2470 DC XL16'00000003F800000300000003F8000003'
000127C0	E2C4C2D9	61E2C4C2		2471 DC CL48'SDBR/SDB NF -2.0/+SNaN FPCR'
000127F0	00800003	F8008003		2472 DC XL16'00800003F800800300800003F8008003'
00012800	E2C4C2D9	61E2C4C2		2473 DC CL48'SDBR/SDB NF -Dnice/-inf FPCR'
00012830	00000002	F8000002		2474 DC XL16'00000002F800000200000002F8000002'
00012840	E2C4C2D9	61E2C4C2		2475 DC CL48'SDBR/SDB NF -Dnice/-2.0 FPCR'
00012870	00080002	F8000C02		2476 DC XL16'00080002F8000C0200080002F8000C02'
00012880	E2C4C2D9	61E2C4C2		2477 DC CL48'SDBR/SDB NF -Dnice/-Dnice FPCR'
000128B0	00000000	F8000000		2478 DC XL16'00000000F800000000000000F8000000'
000128C0	E2C4C2D9	61E2C4C2		2479 DC CL48'SDBR/SDB NF -Dnice/-0 FPCR'
000128F0	00000001	F8001001		2480 DC XL16'00000001F800100100000001F8001001'
00012900	E2C4C2D9	61E2C4C2		2481 DC CL48'SDBR/SDB NF -Dnice/+0 FPCR'
00012930	00000001	F8001001		2482 DC XL16'00000001F800100100000001F8001001'
00012940	E2C4C2D9	61E2C4C2		2483 DC CL48'SDBR/SDB NF -Dnice/+Dnice FPCR'
00012970	00000001	F8001001		2484 DC XL16'00000001F800100100000001F8001001'
00012980	E2C4C2D9	61E2C4C2		2485 DC CL48'SDBR/SDB NF -Dnice/+2.0 FPCR'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
000129B0	00080001 F8000801			2486 DC XL16'00080001F800080100080001F8000801'
000129C0	E2C4C2D9 61E2C4C2			2487 DC CL48'SDBR/SDB NF -Dnice/+inf FPCR'
000129F0	00000001 F8000001			2488 DC XL16'00000001F800000100000001F8000001'
00012A00	E2C4C2D9 61E2C4C2			2489 DC CL48'SDBR/SDB NF -Dnice/-QNaN FPCR'
00012A30	00000003 F8000003			2490 DC XL16'00000003F800000300000003F8000003'
00012A40	E2C4C2D9 61E2C4C2			2491 DC CL48'SDBR/SDB NF -Dnice/+SNaN FPCR'
00012A70	00800003 F8008003			2492 DC XL16'00800003F800800300800003F8008003'
00012A80	E2C4C2D9 61E2C4C2			2493 DC CL48'SDBR/SDB NF -0/-inf FPCR'
00012AB0	00000002 F8000002			2494 DC XL16'00000002F800000200000002F8000002'
00012AC0	E2C4C2D9 61E2C4C2			2495 DC CL48'SDBR/SDB NF -0/-2.0 FPCR'
00012AF0	00000002 F8000002			2496 DC XL16'00000002F800000200000002F8000002'
00012B00	E2C4C2D9 61E2C4C2			2497 DC CL48'SDBR/SDB NF -0/-Dnice FPCR'
00012B30	00000002 F8001002			2498 DC XL16'00000002F800100200000002F8001002'
00012B40	E2C4C2D9 61E2C4C2			2499 DC CL48'SDBR/SDB NF -0/-0 FPCR'
00012B70	00000000 F8000000			2500 DC XL16'00000000F800000000000000F8000000'
00012B80	E2C4C2D9 61E2C4C2			2501 DC CL48'SDBR/SDB NF -0/+0 FPCR'
00012BB0	00000000 F8000000			2502 DC XL16'00000000F800000000000000F8000000'
00012BC0	E2C4C2D9 61E2C4C2			2503 DC CL48'SDBR/SDB NF -0/+Dnice FPCR'
00012BF0	00000001 F8001001			2504 DC XL16'00000001F800100100000001F8001001'
00012C00	E2C4C2D9 61E2C4C2			2505 DC CL48'SDBR/SDB NF -0/+2.0 FPCR'
00012C30	00000001 F8000001			2506 DC XL16'00000001F800000100000001F8000001'
00012C40	E2C4C2D9 61E2C4C2			2507 DC CL48'SDBR/SDB NF -0/+inf FPCR'
00012C70	00000001 F8000001			2508 DC XL16'00000001F800000100000001F8000001'
00012C80	E2C4C2D9 61E2C4C2			2509 DC CL48'SDBR/SDB NF -0/-QNaN FPCR'
00012CB0	00000003 F8000003			2510 DC XL16'00000003F800000300000003F8000003'
00012CC0	E2C4C2D9 61E2C4C2			2511 DC CL48'SDBR/SDB NF -0/+SNaN FPCR'
00012CF0	00800003 F8008003			2512 DC XL16'00800003F800800300800003F8008003'
00012D00	E2C4C2D9 61E2C4C2			2513 DC CL48'SDBR/SDB NF +0/-inf FPCR'
00012D30	00000002 F8000002			2514 DC XL16'00000002F800000200000002F8000002'
00012D40	E2C4C2D9 61E2C4C2			2515 DC CL48'SDBR/SDB NF +0/-2.0 FPCR'
00012D70	00000002 F8000002			2516 DC XL16'00000002F800000200000002F8000002'
00012D80	E2C4C2D9 61E2C4C2			2517 DC CL48'SDBR/SDB NF +0/-Dnice FPCR'
00012DB0	00000002 F8001002			2518 DC XL16'00000002F800100200000002F8001002'
00012DC0	E2C4C2D9 61E2C4C2			2519 DC CL48'SDBR/SDB NF +0/-0 FPCR'
00012DF0	00000000 F8000000			2520 DC XL16'00000000F800000000000000F8000000'
00012E00	E2C4C2D9 61E2C4C2			2521 DC CL48'SDBR/SDB NF +0/+0 FPCR'
00012E30	00000000 F8000000			2522 DC XL16'00000000F800000000000000F8000000'
00012E40	E2C4C2D9 61E2C4C2			2523 DC CL48'SDBR/SDB NF +0/+Dnice FPCR'
00012E70	00000001 F8001001			2524 DC XL16'00000001F800100100000001F8001001'
00012E80	E2C4C2D9 61E2C4C2			2525 DC CL48'SDBR/SDB NF +0/+2.0 FPCR'
00012EB0	00000001 F8000001			2526 DC XL16'00000001F800000100000001F8000001'
00012EC0	E2C4C2D9 61E2C4C2			2527 DC CL48'SDBR/SDB NF +0/+inf FPCR'
00012EF0	00000001 F8000001			2528 DC XL16'00000001F800000100000001F8000001'
00012F00	E2C4C2D9 61E2C4C2			2529 DC CL48'SDBR/SDB NF +0/-QNaN FPCR'
00012F30	00000003 F8000003			2530 DC XL16'00000003F800000300000003F8000003'
00012F40	E2C4C2D9 61E2C4C2			2531 DC CL48'SDBR/SDB NF +0/+SNaN FPCR'
00012F70	00800003 F8008003			2532 DC XL16'00800003F800800300800003F8008003'
00012F80	E2C4C2D9 61E2C4C2			2533 DC CL48'SDBR/SDB NF +Dnice/-inf FPCR'
00012FB0	00000002 F8000002			2534 DC XL16'00000002F800000200000002F8000002'
00012FC0	E2C4C2D9 61E2C4C2			2535 DC CL48'SDBR/SDB NF +Dnice/-2.0 FPCR'
00012FF0	00080002 F8000802			2536 DC XL16'00080002F800080200080002F8000802'
00013000	E2C4C2D9 61E2C4C2			2537 DC CL48'SDBR/SDB NF +Dnice/-Dnice FPCR'
00013030	00000002 F8001002			2538 DC XL16'00000002F800100200000002F8001002'
00013040	E2C4C2D9 61E2C4C2			2539 DC CL48'SDBR/SDB NF +Dnice/-0 FPCR'
00013070	00000002 F8001002			2540 DC XL16'00000002F800100200000002F8001002'
00013080	E2C4C2D9 61E2C4C2			2541 DC CL48'SDBR/SDB NF +Dnice/+0 FPCR'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
000130B0	00000002 F8001002			2542 DC XL16'00000002F800100200000002F8001002'
000130C0	E2C4C2D9 61E2C4C2			2543 DC CL48'SDBR/SDB NF +Dnice/+Dnice FPCR'
000130F0	00000000 F8000000			2544 DC XL16'00000000F800000000000000F8000000'
00013100	E2C4C2D9 61E2C4C2			2545 DC CL48'SDBR/SDB NF +Dnice/+2.0 FPCR'
00013130	00080001 F8000C01			2546 DC XL16'00080001F8000C0100080001F8000C01'
00013140	E2C4C2D9 61E2C4C2			2547 DC CL48'SDBR/SDB NF +Dnice/+inf FPCR'
00013170	00000001 F8000001			2548 DC XL16'00000001F800000100000001F8000001'
00013180	E2C4C2D9 61E2C4C2			2549 DC CL48'SDBR/SDB NF +Dnice/-QNaN FPCR'
000131B0	00000003 F8000003			2550 DC XL16'00000003F800000300000003F8000003'
000131C0	E2C4C2D9 61E2C4C2			2551 DC CL48'SDBR/SDB NF +Dnice/+SNaN FPCR'
000131F0	00800003 F8008003			2552 DC XL16'00800003F800800300800003F8008003'
00013200	E2C4C2D9 61E2C4C2			2553 DC CL48'SDBR/SDB NF +2.0/-inf FPCR'
00013230	00000002 F8000002			2554 DC XL16'00000002F800000200000002F8000002'
00013240	E2C4C2D9 61E2C4C2			2555 DC CL48'SDBR/SDB NF +2.0/-2.0 FPCR'
00013270	00000002 F8000002			2556 DC XL16'00000002F800000200000002F8000002'
00013280	E2C4C2D9 61E2C4C2			2557 DC CL48'SDBR/SDB NF +2.0/-Dnice FPCR'
000132B0	00080002 F8000802			2558 DC XL16'00080002F800080200080002F8000802'
000132C0	E2C4C2D9 61E2C4C2			2559 DC CL48'SDBR/SDB NF +2.0/-0 FPCR'
000132F0	00000002 F8000002			2560 DC XL16'00000002F800000200000002F8000002'
00013300	E2C4C2D9 61E2C4C2			2561 DC CL48'SDBR/SDB NF +2.0/+0 FPCR'
00013330	00000002 F8000002			2562 DC XL16'00000002F800000200000002F8000002'
00013340	E2C4C2D9 61E2C4C2			2563 DC CL48'SDBR/SDB NF +2.0/+Dnice FPCR'
00013370	00080002 F8000C02			2564 DC XL16'00080002F8000C0200080002F8000C02'
00013380	E2C4C2D9 61E2C4C2			2565 DC CL48'SDBR/SDB NF +2.0/+2.0 FPCR'
000133B0	00000000 F8000000			2566 DC XL16'00000000F800000000000000F8000000'
000133C0	E2C4C2D9 61E2C4C2			2567 DC CL48'SDBR/SDB NF +2.0/+inf FPCR'
000133F0	00000001 F8000001			2568 DC XL16'00000001F800000100000001F8000001'
00013400	E2C4C2D9 61E2C4C2			2569 DC CL48'SDBR/SDB NF +2.0/-QNaN FPCR'
00013430	00000003 F8000003			2570 DC XL16'00000003F800000300000003F8000003'
00013440	E2C4C2D9 61E2C4C2			2571 DC CL48'SDBR/SDB NF +2.0/+SNaN FPCR'
00013470	00800003 F8008003			2572 DC XL16'00800003F800800300800003F8008003'
00013480	E2C4C2D9 61E2C4C2			2573 DC CL48'SDBR/SDB NF +inf/-inf FPCR'
000134B0	00000002 F8000002			2574 DC XL16'00000002F800000200000002F8000002'
000134C0	E2C4C2D9 61E2C4C2			2575 DC CL48'SDBR/SDB NF +inf/-2.0 FPCR'
000134F0	00000002 F8000002			2576 DC XL16'00000002F800000200000002F8000002'
00013500	E2C4C2D9 61E2C4C2			2577 DC CL48'SDBR/SDB NF +inf/-Dnice FPCR'
00013530	00000002 F8000002			2578 DC XL16'00000002F800000200000002F8000002'
00013540	E2C4C2D9 61E2C4C2			2579 DC CL48'SDBR/SDB NF +inf/-0 FPCR'
00013570	00000002 F8000002			2580 DC XL16'00000002F800000200000002F8000002'
00013580	E2C4C2D9 61E2C4C2			2581 DC CL48'SDBR/SDB NF +inf/+0 FPCR'
000135B0	00000002 F8000002			2582 DC XL16'00000002F800000200000002F8000002'
000135C0	E2C4C2D9 61E2C4C2			2583 DC CL48'SDBR/SDB NF +inf/+Dnice FPCR'
000135F0	00000002 F8000002			2584 DC XL16'00000002F800000200000002F8000002'
00013600	E2C4C2D9 61E2C4C2			2585 DC CL48'SDBR/SDB NF +inf/+2.0 FPCR'
00013630	00000002 F8000002			2586 DC XL16'00000002F800000200000002F8000002'
00013640	E2C4C2D9 61E2C4C2			2587 DC CL48'SDBR/SDB NF +inf/+inf FPCR'
00013670	00800003 F8008003			2588 DC XL16'00800003F800800300800003F8008003'
00013680	E2C4C2D9 61E2C4C2			2589 DC CL48'SDBR/SDB NF +inf/-QNaN FPCR'
000136B0	00000003 F8000003			2590 DC XL16'00000003F800000300000003F8000003'
000136C0	E2C4C2D9 61E2C4C2			2591 DC CL48'SDBR/SDB NF +inf/+SNaN FPCR'
000136F0	00800003 F8008003			2592 DC XL16'00800003F800800300800003F8008003'
00013700	E2C4C2D9 61E2C4C2			2593 DC CL48'SDBR/SDB NF -QNaN/-inf FPCR'
00013730	00000003 F8000003			2594 DC XL16'00000003F800000300000003F8000003'
00013740	E2C4C2D9 61E2C4C2			2595 DC CL48'SDBR/SDB NF -QNaN/-2.0 FPCR'
00013770	00000003 F8000003			2596 DC XL16'00000003F800000300000003F8000003'
00013780	E2C4C2D9 61E2C4C2			2597 DC CL48'SDBR/SDB NF -QNaN/-Dnice FPCR'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
000137B0	00000003 F8000003			2598 DC XL16'00000003F800000300000003F8000003'
000137C0	E2C4C2D9 61E2C4C2			2599 DC CL48'SDBR/SDB NF -QNaN/-0 FPCR'
000137F0	00000003 F8000003			2600 DC XL16'00000003F800000300000003F8000003'
00013800	E2C4C2D9 61E2C4C2			2601 DC CL48'SDBR/SDB NF -QNaN/+0 FPCR'
00013830	00000003 F8000003			2602 DC XL16'00000003F800000300000003F8000003'
00013840	E2C4C2D9 61E2C4C2			2603 DC CL48'SDBR/SDB NF -QNaN/+Dnice FPCR'
00013870	00000003 F8000003			2604 DC XL16'00000003F800000300000003F8000003'
00013880	E2C4C2D9 61E2C4C2			2605 DC CL48'SDBR/SDB NF -QNaN/+2.0 FPCR'
000138B0	00000003 F8000003			2606 DC XL16'00000003F800000300000003F8000003'
000138C0	E2C4C2D9 61E2C4C2			2607 DC CL48'SDBR/SDB NF -QNaN/+inf FPCR'
000138F0	00000003 F8000003			2608 DC XL16'00000003F800000300000003F8000003'
00013900	E2C4C2D9 61E2C4C2			2609 DC CL48'SDBR/SDB NF -QNaN/-QNaN FPCR'
00013930	00000003 F8000003			2610 DC XL16'00000003F800000300000003F8000003'
00013940	E2C4C2D9 61E2C4C2			2611 DC CL48'SDBR/SDB NF -QNaN/+SNaN FPCR'
00013970	00800003 F8008003			2612 DC XL16'00800003F800800300800003F8008003'
00013980	E2C4C2D9 61E2C4C2			2613 DC CL48'SDBR/SDB NF +SNaN/-inf FPCR'
000139B0	00800003 F8008003			2614 DC XL16'00800003F800800300800003F8008003'
000139C0	E2C4C2D9 61E2C4C2			2615 DC CL48'SDBR/SDB NF +SNaN/-2.0 FPCR'
000139F0	00800003 F8008003			2616 DC XL16'00800003F800800300800003F8008003'
00013A00	E2C4C2D9 61E2C4C2			2617 DC CL48'SDBR/SDB NF +SNaN/-Dnice FPCR'
00013A30	00800003 F8008003			2618 DC XL16'00800003F800800300800003F8008003'
00013A40	E2C4C2D9 61E2C4C2			2619 DC CL48'SDBR/SDB NF +SNaN/-0 FPCR'
00013A70	00800003 F8008003			2620 DC XL16'00800003F800800300800003F8008003'
00013A80	E2C4C2D9 61E2C4C2			2621 DC CL48'SDBR/SDB NF +SNaN/+0 FPCR'
00013AB0	00800003 F8008003			2622 DC XL16'00800003F800800300800003F8008003'
00013AC0	E2C4C2D9 61E2C4C2			2623 DC CL48'SDBR/SDB NF +SNaN/+Dnice FPCR'
00013AF0	00800003 F8008003			2624 DC XL16'00800003F800800300800003F8008003'
00013B00	E2C4C2D9 61E2C4C2			2625 DC CL48'SDBR/SDB NF +SNaN/+2.0 FPCR'
00013B30	00800003 F8008003			2626 DC XL16'00800003F800800300800003F8008003'
00013B40	E2C4C2D9 61E2C4C2			2627 DC CL48'SDBR/SDB NF +SNaN/+inf FPCR'
00013B70	00800003 F8008003			2628 DC XL16'00800003F800800300800003F8008003'
00013B80	E2C4C2D9 61E2C4C2			2629 DC CL48'SDBR/SDB NF +SNaN/-QNaN FPCR'
00013BB0	00800003 F8008003			2630 DC XL16'00800003F800800300800003F8008003'
00013BC0	E2C4C2D9 61E2C4C2			2631 DC CL48'SDBR/SDB NF +SNaN/+SNaN FPCR'
00013BF0	00800003 F8008003			2632 DC XL16'00800003F800800300800003F8008003'
		00000064	00000001	2633 LBFPNFFL_NUM EQU (*-LBFPNFFL_GOOD)/64
				2634 *
				2635 *
		00013C00	00000001	2636 LBFPOUT_GOOD EQU *
00013C00	E2C4C2D9 40C640D6			2637 DC CL48'SDBR F Ovfl'
00013C30	7FFFFFFF FFFFFFFF			2638 DC XL16'7FFFFFFF7FFFFFFF7FFFFFFF7FFFFFFF'
00013C40	E2C4C240 C640D6A5			2639 DC CL48'SDB F Ovfl'
00013C70	7FFFFFFF FFFFFFFF			2640 DC XL16'7FFFFFFF7FFFFFFF7FFFFFFF7FFFFFFF'
00013C80	E2C4C2D9 40C640E4			2641 DC CL48'SDBR F Ufl 1'
00013CB0	000FFFFF FFFFFFFF			2642 DC XL16'000FFFFF600FFFFF600FFFFF600FFFFF'
00013CC0	E2C4C240 C640E486			2643 DC CL48'SDB F Ufl 1'
00013CF0	000FFFFF FFFFFFFF			2644 DC XL16'000FFFFF600FFFFF600FFFFF600FFFFF'
00013D00	E2C4C2D9 40C640E4			2645 DC CL48'SDBR F Ufl 2'
00013D30	00070F10 00000000			2646 DC XL16'00070F10000000005FFC3C4000000000'
00013D40	E2C4C240 C640E486			2647 DC CL48'SDB F Ufl 2'
00013D70	00070F10 00000000			2648 DC XL16'00070F10000000005FFC3C4000000000'
00013D80	E2C4C2D9 40C640D5			2649 DC CL48'SDBR F Nmin'
00013DB0	00100000 00000000			2650 DC XL16'0010000000000000001000000000000'
00013DC0	E2C4C240 C640D594			2651 DC CL48'SDB F Nmin'
00013DF0	00100000 00000000			2652 DC XL16'0010000000000000001000000000000'
00013E00	E2C4C2D9 40C640C9			2653 DC CL48'SDBR F Incr'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
00013E30	3FF00000 00000000			2654 DC XL16'3FF00000000000003FF0000000000000'
00013E40	E2C4C240 C640C995			2655 DC CL48'SDB F Incr'
00013E70	3FF00000 00000000			2656 DC XL16'3FF00000000000003FF0000000000000'
00013E80	E2C4C2D9 40C640E3			2657 DC CL48'SDBR F Trun'
00013EB0	3FEFFFFFF FFFFFFFF			2658 DC XL16'3FEFFFFFFF3FEFFFFFFF'
00013EC0	E2C4C240 C640E399			2659 DC CL48'SDB F Trun'
00013EF0	3FEFFFFFF FFFFFFFF			2660 DC XL16'3FEFFFFFFF3FEFFFFFFF'
		0000000C	00000001	2661 LBFPOUT_NUM EQU (*-LBFPOUT_GOOD)/64
				2662 *
				2663 *
		00013F00	00000001	2664 LBFPFLGS_GOOD EQU *
00013F00	E2C4C2D9 61E2C4C2			2665 DC CL48'SDBR/SDB F Ovfl FPCR'
00013F30	00000003 F8000003			2666 DC XL16'00000003F800000300000003F8000003'
00013F40	E2C4C2D9 61E2C4C2			2667 DC CL48'SDBR/SDB F Ufl 1 FPCR'
00013F70	00000002 F8001002			2668 DC XL16'00000002F800100200000002F8001002'
00013F80	E2C4C2D9 61E2C4C2			2669 DC CL48'SDBR/SDB F Ufl 2 FPCR'
00013FB0	00000002 F8001002			2670 DC XL16'00000002F800100200000002F8001002'
00013FC0	E2C4C2D9 61E2C4C2			2671 DC CL48'SDBR/SDB F Nmin FPCR'
00013FF0	00000002 F8000002			2672 DC XL16'00000002F800000200000002F8000002'
00014000	E2C4C2D9 61E2C4C2			2673 DC CL48'SDBR/SDB F Incr FPCR'
00014030	00080002 F8000C02			2674 DC XL16'00080002F8000C0200080002F8000C02'
00014040	E2C4C2D9 61E2C4C2			2675 DC CL48'SDBR/SDB F Trun FPCR'
00014070	00080002 F8000802			2676 DC XL16'00080002F800080200080002F8000802'
		00000006	00000001	2677 LBFPFLGS_NUM EQU (*-LBFPFLGS_GOOD)/64
				2678 *
				2679 *
		00014080	00000001	2680 LBFPOMO_GOOD EQU *
00014080	E2C4C2D9 61E2C4C2			2681 DC CL48'SDBR/SDB RM +NZ RNTE'
000140B0	3FEFFFFFF FFFFFFFF			2682 DC XL16'3FEFFFFFFF3FEFFFFFFF'
000140C0	E2C4C2D9 61E2C4C2			2683 DC CL48'SDBR/SDB RM +NZ RZ'
000140F0	3FEFFFFFF FFFFFFFF			2684 DC XL16'3FEFFFFFFF3FEFFFFFFF'
00014100	E2C4C2D9 61E2C4C2			2685 DC CL48'SDBR/SDB RM +NZ RP'
00014130	3FF00000 00000000			2686 DC XL16'3FF00000000000003FF0000000000000'
00014140	E2C4C2D9 61E2C4C2			2687 DC CL48'SDBR/SDB RM +NZ RM'
00014170	3FEFFFFFF FFFFFFFF			2688 DC XL16'3FEFFFFFFF3FEFFFFFFF'
00014180	E2C4C2D9 61E2C4C2			2689 DC CL48'SDBR/SDB RM +NZ RFS'
000141B0	3FEFFFFFF FFFFFFFF			2690 DC XL16'3FEFFFFFFF3FEFFFFFFF'
000141C0	E2C4C2D9 61E2C4C2			2691 DC CL48'SDBR/SDB RM -NZ RNTE'
000141F0	BFEFFFFFF FFFFFFFF			2692 DC XL16'BFEFFFFFFFBFEFFFFFFF'
00014200	E2C4C2D9 61E2C4C2			2693 DC CL48'SDBR/SDB RM -NZ RZ'
00014230	BFEFFFFFF FFFFFFFF			2694 DC XL16'BFEFFFFFFFBFEFFFFFFF'
00014240	E2C4C2D9 61E2C4C2			2695 DC CL48'SDBR/SDB RM -NZ RP'
00014270	BFEFFFFFF FFFFFFFF			2696 DC XL16'BFEFFFFFFFBFEFFFFFFF'
00014280	E2C4C2D9 61E2C4C2			2697 DC CL48'SDBR/SDB RM -NZ RM'
000142B0	BFF00000 00000000			2698 DC XL16'BFF0000000000000BFF0000000000000'
000142C0	E2C4C2D9 61E2C4C2			2699 DC CL48'SDBR/SDB RM -NZ RFS'
000142F0	BFEFFFFFF FFFFFFFF			2700 DC XL16'BFEFFFFFFFBFEFFFFFFF'
00014300	E2C4C2D9 61E2C4C2			2701 DC CL48'SDBR/SDB RM +NA RNTE'
00014330	3FF00000 00000000			2702 DC XL16'3FF00000000000003FF0000000000000'
00014340	E2C4C2D9 61E2C4C2			2703 DC CL48'SDBR/SDB RM +NA RZ'
00014370	3FEFFFFFF FFFFFFFF			2704 DC XL16'3FEFFFFFFF3FEFFFFFFF'
00014380	E2C4C2D9 61E2C4C2			2705 DC CL48'SDBR/SDB RM +NA RP'
000143B0	3FF00000 00000000			2706 DC XL16'3FF00000000000003FF0000000000000'
000143C0	E2C4C2D9 61E2C4C2			2707 DC CL48'SDBR/SDB RM +NA RM'
000143F0	3FEFFFFFF FFFFFFFF			2708 DC XL16'3FEFFFFFFF3FEFFFFFFF'
00014400	E2C4C2D9 61E2C4C2			2709 DC CL48'SDBR/SDB RM +NA RFS'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
00014430	3FEFFFFFF FFFFFFFF			2710 DC XL16'3FEFFFFFFF3FEFFFFFFF'
00014440	E2C4C2D9 61E2C4C2			2711 DC CL48'SDBR/SDB RM -NA RNTE'
00014470	BFF00000 00000000			2712 DC XL16'BFF0000000000000BFF0000000000000'
00014480	E2C4C2D9 61E2C4C2			2713 DC CL48'SDBR/SDB RM -NA RZ'
000144B0	BFEFFFFFF FFFFFFFF			2714 DC XL16'BFEFFFFFFF3FEFFFFFFF'
000144C0	E2C4C2D9 61E2C4C2			2715 DC CL48'SDBR/SDB RM -NA RP'
000144F0	BFEFFFFFF FFFFFFFF			2716 DC XL16'BFEFFFFFFF3FEFFFFFFF'
00014500	E2C4C2D9 61E2C4C2			2717 DC CL48'SDBR/SDB RM -NA RM'
00014530	BFF00000 00000000			2718 DC XL16'BFF0000000000000BFF0000000000000'
00014540	E2C4C2D9 61E2C4C2			2719 DC CL48'SDBR/SDB RM -NA RFS'
00014570	BFEFFFFFF FFFFFFFF			2720 DC XL16'BFEFFFFFFF3FEFFFFFFF'
00014580	E2C4C2D9 61E2C4C2			2721 DC CL48'SDBR/SDB RM +TZ RNTE'
000145B0	3FEFFFFFF FFFFFFFE			2722 DC XL16'3FEFFFFFFE3FEFFFFFFE'
000145C0	E2C4C2D9 61E2C4C2			2723 DC CL48'SDBR/SDB RM +TZ RZ'
000145F0	3FEFFFFFF FFFFFFFE			2724 DC XL16'3FEFFFFFFE3FEFFFFFFE'
00014600	E2C4C2D9 61E2C4C2			2725 DC CL48'SDBR/SDB RM +TZ RP'
00014630	3FEFFFFFF FFFFFFFF			2726 DC XL16'3FEFFFFFFF3FEFFFFFFF'
00014640	E2C4C2D9 61E2C4C2			2727 DC CL48'SDBR/SDB RM +TZ RM'
00014670	3FEFFFFFF FFFFFFFE			2728 DC XL16'3FEFFFFFFE3FEFFFFFFE'
00014680	E2C4C2D9 61E2C4C2			2729 DC CL48'SDBR/SDB RM +TZ RFS'
000146B0	3FEFFFFFF FFFFFFFF			2730 DC XL16'3FEFFFFFFF3FEFFFFFFF'
000146C0	E2C4C2D9 61E2C4C2			2731 DC CL48'SDBR/SDB RM -TZ RNTE'
000146F0	BFEFFFFFF FFFFFFFE			2732 DC XL16'BFEFFFFFFE3FEFFFFFFE'
00014700	E2C4C2D9 61E2C4C2			2733 DC CL48'SDBR/SDB RM -TZ RZ'
00014730	BFEFFFFFF FFFFFFFE			2734 DC XL16'BFEFFFFFFE3FEFFFFFFE'
00014740	E2C4C2D9 61E2C4C2			2735 DC CL48'SDBR/SDB RM -TZ RP'
00014770	BFEFFFFFF FFFFFFFE			2736 DC XL16'BFEFFFFFFE3FEFFFFFFE'
00014780	E2C4C2D9 61E2C4C2			2737 DC CL48'SDBR/SDB RM -TZ RM'
000147B0	BFEFFFFFF FFFFFFFF			2738 DC XL16'BFEFFFFFFF3FEFFFFFFF'
000147C0	E2C4C2D9 61E2C4C2			2739 DC CL48'SDBR/SDB RM -TZ RFS'
000147F0	BFEFFFFFF FFFFFFFF			2740 DC XL16'BFEFFFFFFF3FEFFFFFFF'
00014800	E2C4C2D9 61E2C4C2			2741 DC CL48'SDBR/SDB RM +TA RNTE'
00014830	3FF00000 00000000			2742 DC XL16'3FF00000000000003FF0000000000000'
00014840	E2C4C2D9 61E2C4C2			2743 DC CL48'SDBR/SDB RM +TA RZ'
00014870	3FEFFFFFF FFFFFFFF			2744 DC XL16'3FEFFFFFFF3FEFFFFFFF'
00014880	E2C4C2D9 61E2C4C2			2745 DC CL48'SDBR/SDB RM +TA RP'
000148B0	3FF00000 00000000			2746 DC XL16'3FF00000000000003FF0000000000000'
000148C0	E2C4C2D9 61E2C4C2			2747 DC CL48'SDBR/SDB RM +TA RM'
000148F0	3FEFFFFFF FFFFFFFF			2748 DC XL16'3FEFFFFFFF3FEFFFFFFF'
00014900	E2C4C2D9 61E2C4C2			2749 DC CL48'SDBR/SDB RM +TA RFS'
00014930	3FEFFFFFF FFFFFFFF			2750 DC XL16'3FEFFFFFFF3FEFFFFFFF'
00014940	E2C4C2D9 61E2C4C2			2751 DC CL48'SDBR/SDB RM -TA RNTE'
00014970	BFF00000 00000000			2752 DC XL16'BFF0000000000000BFF0000000000000'
00014980	E2C4C2D9 61E2C4C2			2753 DC CL48'SDBR/SDB RM -TA RZ'
000149B0	BFEFFFFFF FFFFFFFF			2754 DC XL16'BFEFFFFFFF3FEFFFFFFF'
000149C0	E2C4C2D9 61E2C4C2			2755 DC CL48'SDBR/SDB RM -TA RP'
000149F0	BFEFFFFFF FFFFFFFF			2756 DC XL16'BFEFFFFFFF3FEFFFFFFF'
00014A00	E2C4C2D9 61E2C4C2			2757 DC CL48'SDBR/SDB RM -TA RM'
00014A30	BFF00000 00000000			2758 DC XL16'BFF0000000000000BFF0000000000000'
00014A40	E2C4C2D9 61E2C4C2			2759 DC CL48'SDBR/SDB RM -TA RFS'
00014A70	BFEFFFFFF FFFFFFFF			2760 DC XL16'BFEFFFFFFF3FEFFFFFFF'
		00000028	00000001	2761 LBFPRMO_NUM EQU (*-LBFPRMO_GOOD)/64
				2762 *
				2763 *
		00014A80	00000001	2764 LBFPRMOF_GOOD EQU *
00014A80	E2C4C2D9 61E2C4C2			2765 DC CL48'SDBR/SDB RM +NZ RNTE, RZ FPCR'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
00014AB0	00080002 00080002			2766 DC XL16'00080002000800020008000200080002'
00014AC0	E2C4C2D9 61E2C4C2			2767 DC CL48'SDBR/SDB RM +NZ RP, RM FPCR'
00014AF0	00080002 00080002			2768 DC XL16'00080002000800020008000200080002'
00014B00	E2C4C2D9 61E2C4C2			2769 DC CL48'SDBR/SDB RM +NZ RFS FPCR'
00014B30	00080002 00080002			2770 DC XL16'00080002000800020000000000000000'
00014B40	E2C4C2D9 61E2C4C2			2771 DC CL48'SDBR/SDB RM +NZ RNTE, RZ FPCR'
00014B70	00080001 00080001			2772 DC XL16'00080001000800010008000100080001'
00014B80	E2C4C2D9 61E2C4C2			2773 DC CL48'SDBR/SDB RM +NZ RP, RM FPCR'
00014BB0	00080001 00080001			2774 DC XL16'00080001000800010008000100080001'
00014BC0	E2C4C2D9 61E2C4C2			2775 DC CL48'SDBR/SDB RM -NZ RFS FPCR'
00014BF0	00080001 00080001			2776 DC XL16'00080001000800010000000000000000'
00014C00	E2C4C2D9 61E2C4C2			2777 DC CL48'SDBR/SDB RM -NZ RNTE, RZ FPCR'
00014C30	00080002 00080002			2778 DC XL16'00080002000800020008000200080002'
00014C40	E2C4C2D9 61E2C4C2			2779 DC CL48'SDBR/SDB RM -NZ RP, RM FPCR'
00014C70	00080002 00080002			2780 DC XL16'00080002000800020008000200080002'
00014C80	E2C4C2D9 61E2C4C2			2781 DC CL48'SDBR/SDB RM -NZ RFS FPCR'
00014CB0	00080002 00080002			2782 DC XL16'00080002000800020000000000000000'
00014CC0	E2C4C2D9 61E2C4C2			2783 DC CL48'SDBR/SDB RM -NZ RNTE, RZ FPCR'
00014CF0	00080001 00080001			2784 DC XL16'00080001000800010008000100080001'
00014D00	E2C4C2D9 61E2C4C2			2785 DC CL48'SDBR/SDB RM -NA RP, RM FPCR'
00014D30	00080001 00080001			2786 DC XL16'00080001000800010008000100080001'
00014D40	E2C4C2D9 61E2C4C2			2787 DC CL48'SDBR/SDB RM -NA RFS FPCR'
00014D70	00080001 00080001			2788 DC XL16'00080001000800010000000000000000'
00014D80	E2C4C2D9 61E2C4C2			2789 DC CL48'SDBR/SDB RM +TZ RNTE, RZ FPCR'
00014DB0	00080002 00080002			2790 DC XL16'00080002000800020008000200080002'
00014DC0	E2C4C2D9 61E2C4C2			2791 DC CL48'SDBR/SDB RM +TZ RP, RM FPCR'
00014DF0	00080002 00080002			2792 DC XL16'00080002000800020008000200080002'
00014E00	E2C4C2D9 61E2C4C2			2793 DC CL48'SDBR/SDB RM +TZ RFS FPCR'
00014E30	00080002 00080002			2794 DC XL16'00080002000800020000000000000000'
00014E40	E2C4C2D9 61E2C4C2			2795 DC CL48'SDBR/SDB RM -TZ RNTE, RZ FPCR'
00014E70	00080001 00080001			2796 DC XL16'00080001000800010008000100080001'
00014E80	E2C4C2D9 61E2C4C2			2797 DC CL48'SDBR/SDB RM -TZ RP, RM FPCR'
00014EB0	00080001 00080001			2798 DC XL16'00080001000800010008000100080001'
00014EC0	E2C4C2D9 61E2C4C2			2799 DC CL48'SDBR/SDB RM -TZ RFS FPCR'
00014EF0	00080001 00080001			2800 DC XL16'00080001000800010000000000000000'
00014F00	E2C4C2D9 61E2C4C2			2801 DC CL48'SDBR/SDB RM +TA RNTE, RZ FPCR'
00014F30	00080002 00080002			2802 DC XL16'00080002000800020008000200080002'
00014F40	E2C4C2D9 61E2C4C2			2803 DC CL48'SDBR/SDB RM +TA RP, RM FPCR'
00014F70	00080002 00080002			2804 DC XL16'00080002000800020008000200080002'
00014F80	E2C4C2D9 61E2C4C2			2805 DC CL48'SDBR/SDB RM +TA RFS FPCR'
00014FB0	00080002 00080002			2806 DC XL16'00080002000800020000000000000000'
00014FC0	E2C4C2D9 61E2C4C2			2807 DC CL48'SDBR/SDB RM -TA RNTE, RZ FPCR'
00014FF0	00080001 00080001			2808 DC XL16'00080001000800010008000100080001'
00015000	E2C4C2D9 61E2C4C2			2809 DC CL48'SDBR/SDB RM -TA RP, RM FPCR'
00015030	00080001 00080001			2810 DC XL16'00080001000800010008000100080001'
00015040	E2C4C2D9 61E2C4C2			2811 DC CL48'SDBR/SDB RM -TA RFS FPCR'
00015070	00080001 00080001			2812 DC XL16'00080001000800010000000000000000'
		00000018	00000001	2813 LBFPRMOF_NUM EQU (*-LBFPRMOF_GOOD)/64
				2814 *
				2815 *
		00015080	00000001	2816 XBFPNFOT_GOOD EQU *
00015080	E2E7C2D9 40D5C640			2817 DC CL48'SXBR NF -inf/-inf NT'
000150B0	7FFF8000 00000000			2818 DC XL16'7FFF8000000000000000000000000000'
000150C0	E2E7C2D9 40D5C640			2819 DC CL48'SXBR NF -inf/-inf Tr'
000150F0	FFFF0000 00000000			2820 DC XL16'FFFF0000000000000000000000000000'
00015100	E2E7C2D9 40D5C640			2821 DC CL48'SXBR NF -inf/-2.0 NT'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
00015130	FFFF0000 00000000			2822 DC XL16'FFFF0000000000000000000000000000'
00015140	E2E7C2D9 40D5C640			2823 DC CL48'SXBR NF -inf/-2.0 Tr'
00015170	FFFF0000 00000000			2824 DC XL16'FFFF0000000000000000000000000000'
00015180	E2E7C2D9 40D5C640			2825 DC CL48'SXBR NF -inf/-Dnice NT'
000151B0	FFFF0000 00000000			2826 DC XL16'FFFF0000000000000000000000000000'
000151C0	E2E7C2D9 40D5C640			2827 DC CL48'SXBR NF -inf/-Dnice Tr'
000151F0	FFFF0000 00000000			2828 DC XL16'FFFF0000000000000000000000000000'
00015200	E2E7C2D9 40D5C640			2829 DC CL48'SXBR NF -inf/-0 NT'
00015230	FFFF0000 00000000			2830 DC XL16'FFFF0000000000000000000000000000'
00015240	E2E7C2D9 40D5C640			2831 DC CL48'SXBR NF -inf/-0 Tr'
00015270	FFFF0000 00000000			2832 DC XL16'FFFF0000000000000000000000000000'
00015280	E2E7C2D9 40D5C640			2833 DC CL48'SXBR NF -inf/+0 NT'
000152B0	FFFF0000 00000000			2834 DC XL16'FFFF0000000000000000000000000000'
000152C0	E2E7C2D9 40D5C640			2835 DC CL48'SXBR NF -inf/+0 Tr'
000152F0	FFFF0000 00000000			2836 DC XL16'FFFF0000000000000000000000000000'
00015300	E2E7C2D9 40D5C640			2837 DC CL48'SXBR NF -inf/+Dnice NT'
00015330	FFFF0000 00000000			2838 DC XL16'FFFF0000000000000000000000000000'
00015340	E2E7C2D9 40D5C640			2839 DC CL48'SXBR NF -inf/+Dnice Tr'
00015370	FFFF0000 00000000			2840 DC XL16'FFFF0000000000000000000000000000'
00015380	E2E7C2D9 40D5C640			2841 DC CL48'SXBR NF -inf/+2.0 NT'
000153B0	FFFF0000 00000000			2842 DC XL16'FFFF0000000000000000000000000000'
000153C0	E2E7C2D9 40D5C640			2843 DC CL48'SXBR NF -inf/+2.0 Tr'
000153F0	FFFF0000 00000000			2844 DC XL16'FFFF0000000000000000000000000000'
00015400	E2E7C2D9 40D5C640			2845 DC CL48'SXBR NF -inf/+inf NT'
00015430	FFFF0000 00000000			2846 DC XL16'FFFF0000000000000000000000000000'
00015440	E2E7C2D9 40D5C640			2847 DC CL48'SXBR NF -inf/+inf Tr'
00015470	FFFF0000 00000000			2848 DC XL16'FFFF0000000000000000000000000000'
00015480	E2E7C2D9 40D5C640			2849 DC CL48'SXBR NF -inf/-QNaN NT'
000154B0	FFFF8B00 00000000			2850 DC XL16'FFFF8B00000000000000000000000000'
000154C0	E2E7C2D9 40D5C640			2851 DC CL48'SXBR NF -inf/-QNaN Tr'
000154F0	FFFF8B00 00000000			2852 DC XL16'FFFF8B00000000000000000000000000'
00015500	E2E7C2D9 40D5C640			2853 DC CL48'SXBR NF -inf/+SNaN NT'
00015530	7FFF8A00 00000000			2854 DC XL16'7FFF8A00000000000000000000000000'
00015540	E2E7C2D9 40D5C640			2855 DC CL48'SXBR NF -inf/+SNaN Tr'
00015570	FFFF0000 00000000			2856 DC XL16'FFFF0000000000000000000000000000'
00015580	E2E7C2D9 40D5C640			2857 DC CL48'SXBR NF -2.0/-inf NT'
000155B0	7FFF0000 00000000			2858 DC XL16'7FFF0000000000000000000000000000'
000155C0	E2E7C2D9 40D5C640			2859 DC CL48'SXBR NF -2.0/-inf Tr'
000155F0	7FFF0000 00000000			2860 DC XL16'7FFF0000000000000000000000000000'
00015600	E2E7C2D9 40D5C640			2861 DC CL48'SXBR NF -2.0/-2.0 NT'
00015630	00000000 00000000			2862 DC XL16'00000000000000000000000000000000'
00015640	E2E7C2D9 40D5C640			2863 DC CL48'SXBR NF -2.0/-2.0 Tr'
00015670	00000000 00000000			2864 DC XL16'00000000000000000000000000000000'
00015680	E2E7C2D9 40D5C640			2865 DC CL48'SXBR NF -2.0/-Dnice NT'
000156B0	C0000000 00000000			2866 DC XL16'C0000000000000000000000000000000'
000156C0	E2E7C2D9 40D5C640			2867 DC CL48'SXBR NF -2.0/-Dnice Tr'
000156F0	C0000000 00000000			2868 DC XL16'C0000000000000000000000000000000'
00015700	E2E7C2D9 40D5C640			2869 DC CL48'SXBR NF -2.0/-0 NT'
00015730	C0000000 00000000			2870 DC XL16'C0000000000000000000000000000000'
00015740	E2E7C2D9 40D5C640			2871 DC CL48'SXBR NF -2.0/-0 Tr'
00015770	C0000000 00000000			2872 DC XL16'C0000000000000000000000000000000'
00015780	E2E7C2D9 40D5C640			2873 DC CL48'SXBR NF -2.0/+0 NT'
000157B0	C0000000 00000000			2874 DC XL16'C0000000000000000000000000000000'
000157C0	E2E7C2D9 40D5C640			2875 DC CL48'SXBR NF -2.0/+0 Tr'
000157F0	C0000000 00000000			2876 DC XL16'C0000000000000000000000000000000'
00015800	E2E7C2D9 40D5C640			2877 DC CL48'SXBR NF -2.0/+Dnice NT'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
00015830	C0000000 00000000			2878	DC XL16'C000000000000000000000000000000000'
00015840	E2E7C2D9 40D5C640			2879	DC CL48'SXBR NF -2.0/+Dnice Tr'
00015870	C0000000 00000000			2880	DC XL16'C00000000000000000000000000000000'
00015880	E2E7C2D9 40D5C640			2881	DC CL48'SXBR NF -2.0/+2.0 NT'
000158B0	C0010000 00000000			2882	DC XL16'C00100000000000000000000000000000'
000158C0	E2E7C2D9 40D5C640			2883	DC CL48'SXBR NF -2.0/+2.0 Tr'
000158F0	C0010000 00000000			2884	DC XL16'C00100000000000000000000000000000'
00015900	E2E7C2D9 40D5C640			2885	DC CL48'SXBR NF -2.0/+inf NT'
00015930	FFFF0000 00000000			2886	DC XL16'FFFF00000000000000000000000000000'
00015940	E2E7C2D9 40D5C640			2887	DC CL48'SXBR NF -2.0/+inf Tr'
00015970	FFFF0000 00000000			2888	DC XL16'FFFF00000000000000000000000000000'
00015980	E2E7C2D9 40D5C640			2889	DC CL48'SXBR NF -2.0/-QNaN NT'
000159B0	FFFF8B00 00000000			2890	DC XL16'FFFF8B000000000000000000000000000'
000159C0	E2E7C2D9 40D5C640			2891	DC CL48'SXBR NF -2.0/-QNaN Tr'
000159F0	FFFF8B00 00000000			2892	DC XL16'FFFF8B000000000000000000000000000'
00015A00	E2E7C2D9 40D5C640			2893	DC CL48'SXBR NF -2.0/+SNaN NT'
00015A30	7FFF8A00 00000000			2894	DC XL16'7FFF8A000000000000000000000000000'
00015A40	E2E7C2D9 40D5C640			2895	DC CL48'SXBR NF -2.0/+SNaN Tr'
00015A70	C0000000 00000000			2896	DC XL16'C00000000000000000000000000000000'
00015A80	E2E7C2D9 40D5C640			2897	DC CL48'SXBR NF -Dnice/-inf NT'
00015AB0	7FFF0000 00000000			2898	DC XL16'7FFF00000000000000000000000000000'
00015AC0	E2E7C2D9 40D5C640			2899	DC CL48'SXBR NF -Dnice/-inf Tr'
00015AF0	7FFF0000 00000000			2900	DC XL16'7FFF00000000000000000000000000000'
00015B00	E2E7C2D9 40D5C640			2901	DC CL48'SXBR NF -Dnice/-2.0 NT'
00015B30	40000000 00000000			2902	DC XL16'400000000000000000000000000000000'
00015B40	E2E7C2D9 40D5C640			2903	DC CL48'SXBR NF -Dnice/-2.0 Tr'
00015B70	40000000 00000000			2904	DC XL16'400000000000000000000000000000000'
00015B80	E2E7C2D9 40D5C640			2905	DC CL48'SXBR NF -Dnice/-Dnice NT'
00015BB0	00000000 00000000			2906	DC XL16'000000000000000000000000000000000'
00015BC0	E2E7C2D9 40D5C640			2907	DC CL48'SXBR NF -Dnice/-Dnice Tr'
00015BF0	00000000 00000000			2908	DC XL16'000000000000000000000000000000000'
00015C00	E2E7C2D9 40D5C640			2909	DC CL48'SXBR NF -Dnice/-0 NT'
00015C30	80001000 00000000			2910	DC XL16'800010000000000000000000000000000'
00015C40	E2E7C2D9 40D5C640			2911	DC CL48'SXBR NF -Dnice/-0 Tr'
00015C70	DFFD0000 00000000			2912	DC XL16'DFFD00000000000000000000000000000'
00015C80	E2E7C2D9 40D5C640			2913	DC CL48'SXBR NF -Dnice/+0 NT'
00015CB0	80001000 00000000			2914	DC XL16'800010000000000000000000000000000'
00015CC0	E2E7C2D9 40D5C640			2915	DC CL48'SXBR NF -Dnice/+0 Tr'
00015CF0	DFFD0000 00000000			2916	DC XL16'DFFD00000000000000000000000000000'
00015D00	E2E7C2D9 40D5C640			2917	DC CL48'SXBR NF -Dnice/+Dnice NT'
00015D30	80002000 00000000			2918	DC XL16'800020000000000000000000000000000'
00015D40	E2E7C2D9 40D5C640			2919	DC CL48'SXBR NF -Dnice/+Dnice Tr'
00015D70	DFFE0000 00000000			2920	DC XL16'DFFE00000000000000000000000000000'
00015D80	E2E7C2D9 40D5C640			2921	DC CL48'SXBR NF -Dnice/+2.0 NT'
00015DB0	C0000000 00000000			2922	DC XL16'C00000000000000000000000000000000'
00015DC0	E2E7C2D9 40D5C640			2923	DC CL48'SXBR NF -Dnice/+2.0 Tr'
00015DF0	C0000000 00000000			2924	DC XL16'C00000000000000000000000000000000'
00015E00	E2E7C2D9 40D5C640			2925	DC CL48'SXBR NF -Dnice/+inf NT'
00015E30	FFFF0000 00000000			2926	DC XL16'FFFF00000000000000000000000000000'
00015E40	E2E7C2D9 40D5C640			2927	DC CL48'SXBR NF -Dnice/+inf Tr'
00015E70	FFFF0000 00000000			2928	DC XL16'FFFF00000000000000000000000000000'
00015E80	E2E7C2D9 40D5C640			2929	DC CL48'SXBR NF -Dnice/-QNaN NT'
00015EB0	FFFF8B00 00000000			2930	DC XL16'FFFF8B000000000000000000000000000'
00015EC0	E2E7C2D9 40D5C640			2931	DC CL48'SXBR NF -Dnice/-QNaN Tr'
00015EF0	FFFF8B00 00000000			2932	DC XL16'FFFF8B000000000000000000000000000'
00015F00	E2E7C2D9 40D5C640			2933	DC CL48'SXBR NF -Dnice/+SNaN NT'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
00015F30	7FFF8A00	00000000		2934	DC XL16'7FFF8A00000000000000000000000000'
00015F40	E2E7C2D9	40D5C640		2935	DC CL48'SXBR NF -Dnice/+SNaN Tr'
00015F70	80001000	00000000		2936	DC XL16'800010000000000000000000000000'
00015F80	E2E7C2D9	40D5C640		2937	DC CL48'SXBR NF -0/-inf NT'
00015FB0	7FFF0000	00000000		2938	DC XL16'7FFF00000000000000000000000000'
00015FC0	E2E7C2D9	40D5C640		2939	DC CL48'SXBR NF -0/-inf Tr'
00015FF0	7FFF0000	00000000		2940	DC XL16'7FFF00000000000000000000000000'
00016000	E2E7C2D9	40D5C640		2941	DC CL48'SXBR NF -0/-2.0 NT'
00016030	40000000	00000000		2942	DC XL16'400000000000000000000000000000'
00016040	E2E7C2D9	40D5C640		2943	DC CL48'SXBR NF -0/-2.0 Tr'
00016070	40000000	00000000		2944	DC XL16'400000000000000000000000000000'
00016080	E2E7C2D9	40D5C640		2945	DC CL48'SXBR NF -0/-Dnice NT'
000160B0	00001000	00000000		2946	DC XL16'000010000000000000000000000000'
000160C0	E2E7C2D9	40D5C640		2947	DC CL48'SXBR NF -0/-Dnice Tr'
000160F0	5FFD0000	00000000		2948	DC XL16'5FFD00000000000000000000000000'
00016100	E2E7C2D9	40D5C640		2949	DC CL48'SXBR NF -0/-0 NT'
00016130	00000000	00000000		2950	DC XL16'000000000000000000000000000000'
00016140	E2E7C2D9	40D5C640		2951	DC CL48'SXBR NF -0/-0 Tr'
00016170	00000000	00000000		2952	DC XL16'000000000000000000000000000000'
00016180	E2E7C2D9	40D5C640		2953	DC CL48'SXBR NF -0/+0 NT'
000161B0	80000000	00000000		2954	DC XL16'800000000000000000000000000000'
000161C0	E2E7C2D9	40D5C640		2955	DC CL48'SXBR NF -0/+0 Tr'
000161F0	80000000	00000000		2956	DC XL16'800000000000000000000000000000'
00016200	E2E7C2D9	40D5C640		2957	DC CL48'SXBR NF -0/+Dnice NT'
00016230	80001000	00000000		2958	DC XL16'800010000000000000000000000000'
00016240	E2E7C2D9	40D5C640		2959	DC CL48'SXBR NF -0/+Dnice Tr'
00016270	DFFD0000	00000000		2960	DC XL16'DFFD00000000000000000000000000'
00016280	E2E7C2D9	40D5C640		2961	DC CL48'SXBR NF -0/+2.0 NT'
000162B0	C0000000	00000000		2962	DC XL16'C00000000000000000000000000000'
000162C0	E2E7C2D9	40D5C640		2963	DC CL48'SXBR NF -0/+2.0 Tr'
000162F0	C0000000	00000000		2964	DC XL16'C00000000000000000000000000000'
00016300	E2E7C2D9	40D5C640		2965	DC CL48'SXBR NF -0/+inf NT'
00016330	FFFF0000	00000000		2966	DC XL16'FFFF00000000000000000000000000'
00016340	E2E7C2D9	40D5C640		2967	DC CL48'SXBR NF -0/+inf Tr'
00016370	FFFF0000	00000000		2968	DC XL16'FFFF00000000000000000000000000'
00016380	E2E7C2D9	40D5C640		2969	DC CL48'SXBR NF -0/-QNaN NT'
000163B0	FFFF8B00	00000000		2970	DC XL16'FFFF8B000000000000000000000000'
000163C0	E2E7C2D9	40D5C640		2971	DC CL48'SXBR NF -0/-QNaN Tr'
000163F0	FFFF8B00	00000000		2972	DC XL16'FFFF8B000000000000000000000000'
00016400	E2E7C2D9	40D5C640		2973	DC CL48'SXBR NF -0/+SNaN NT'
00016430	7FFF8A00	00000000		2974	DC XL16'7FFF8A000000000000000000000000'
00016440	E2E7C2D9	40D5C640		2975	DC CL48'SXBR NF -0/+SNaN Tr'
00016470	80000000	00000000		2976	DC XL16'800000000000000000000000000000'
00016480	E2E7C2D9	40D5C640		2977	DC CL48'SXBR NF +0/-inf NT'
000164B0	7FFF0000	00000000		2978	DC XL16'7FFF00000000000000000000000000'
000164C0	E2E7C2D9	40D5C640		2979	DC CL48'SXBR NF +0/-inf Tr'
000164F0	7FFF0000	00000000		2980	DC XL16'7FFF00000000000000000000000000'
00016500	E2E7C2D9	40D5C640		2981	DC CL48'SXBR NF +0/-2.0 NT'
00016530	40000000	00000000		2982	DC XL16'400000000000000000000000000000'
00016540	E2E7C2D9	40D5C640		2983	DC CL48'SXBR NF +0/-2.0 Tr'
00016570	40000000	00000000		2984	DC XL16'400000000000000000000000000000'
00016580	E2E7C2D9	40D5C640		2985	DC CL48'SXBR NF +0/-Dnice NT'
000165B0	00001000	00000000		2986	DC XL16'000010000000000000000000000000'
000165C0	E2E7C2D9	40D5C640		2987	DC CL48'SXBR NF +0/-Dnice Tr'
000165F0	5FFD0000	00000000		2988	DC XL16'5FFD00000000000000000000000000'
00016600	E2E7C2D9	40D5C640		2989	DC CL48'SXBR NF +0/-0 NT'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
00016630	00000000 00000000			2990 DC XL16'00000000000000000000000000000000'
00016640	E2E7C2D9 40D5C640			2991 DC CL48'SXBR NF +0/-0 Tr'
00016670	00000000 00000000			2992 DC XL16'00000000000000000000000000000000'
00016680	E2E7C2D9 40D5C640			2993 DC CL48'SXBR NF +0/+0 NT'
000166B0	00000000 00000000			2994 DC XL16'00000000000000000000000000000000'
000166C0	E2E7C2D9 40D5C640			2995 DC CL48'SXBR NF +0/+0 Tr'
000166F0	00000000 00000000			2996 DC XL16'00000000000000000000000000000000'
00016700	E2E7C2D9 40D5C640			2997 DC CL48'SXBR NF +0/+Dnice NT'
00016730	80001000 00000000			2998 DC XL16'80001000000000000000000000000000'
00016740	E2E7C2D9 40D5C640			2999 DC CL48'SXBR NF +0/+Dnice Tr'
00016770	DFFD0000 00000000			3000 DC XL16'DFFD0000000000000000000000000000'
00016780	E2E7C2D9 40D5C640			3001 DC CL48'SXBR NF +0/+2.0 NT'
000167B0	C0000000 00000000			3002 DC XL16'C0000000000000000000000000000000'
000167C0	E2E7C2D9 40D5C640			3003 DC CL48'SXBR NF +0/+2.0 Tr'
000167F0	C0000000 00000000			3004 DC XL16'C0000000000000000000000000000000'
00016800	E2E7C2D9 40D5C640			3005 DC CL48'SXBR NF +0/+inf NT'
00016830	FFFF0000 00000000			3006 DC XL16'FFFF0000000000000000000000000000'
00016840	E2E7C2D9 40D5C640			3007 DC CL48'SXBR NF +0/+inf Tr'
00016870	FFFF0000 00000000			3008 DC XL16'FFFF0000000000000000000000000000'
00016880	E2E7C2D9 40D5C640			3009 DC CL48'SXBR NF +0/-QNaN NT'
000168B0	FFFF8B00 00000000			3010 DC XL16'FFFF8B00000000000000000000000000'
000168C0	E2E7C2D9 40D5C640			3011 DC CL48'SXBR NF +0/-QNaN Tr'
000168F0	FFFF8B00 00000000			3012 DC XL16'FFFF8B00000000000000000000000000'
00016900	E2E7C2D9 40D5C640			3013 DC CL48'SXBR NF +0/+SNaN NT'
00016930	7FFF8A00 00000000			3014 DC XL16'7FFF8A00000000000000000000000000'
00016940	E2E7C2D9 40D5C640			3015 DC CL48'SXBR NF +0/+SNaN Tr'
00016970	00000000 00000000			3016 DC XL16'00000000000000000000000000000000'
00016980	E2E7C2D9 40D5C640			3017 DC CL48'SXBR NF +Dnice/-inf NT'
000169B0	7FFF0000 00000000			3018 DC XL16'7FFF0000000000000000000000000000'
000169C0	E2E7C2D9 40D5C640			3019 DC CL48'SXBR NF +Dnice/-inf Tr'
000169F0	7FFF0000 00000000			3020 DC XL16'7FFF0000000000000000000000000000'
00016A00	E2E7C2D9 40D5C640			3021 DC CL48'SXBR NF +Dnice/-2.0 NT'
00016A30	40000000 00000000			3022 DC XL16'40000000000000000000000000000000'
00016A40	E2E7C2D9 40D5C640			3023 DC CL48'SXBR NF +Dnice/-2.0 Tr'
00016A70	40000000 00000000			3024 DC XL16'40000000000000000000000000000000'
00016A80	E2E7C2D9 40D5C640			3025 DC CL48'SXBR NF +Dnice/-Dnice NT'
00016AB0	00002000 00000000			3026 DC XL16'00002000000000000000000000000000'
00016AC0	E2E7C2D9 40D5C640			3027 DC CL48'SXBR NF +Dnice/-Dnice Tr'
00016AF0	5FFE0000 00000000			3028 DC XL16'5FFE0000000000000000000000000000'
00016B00	E2E7C2D9 40D5C640			3029 DC CL48'SXBR NF +Dnice/-0 NT'
00016B30	00001000 00000000			3030 DC XL16'00001000000000000000000000000000'
00016B40	E2E7C2D9 40D5C640			3031 DC CL48'SXBR NF +Dnice/-0 Tr'
00016B70	5FFD0000 00000000			3032 DC XL16'5FFD0000000000000000000000000000'
00016B80	E2E7C2D9 40D5C640			3033 DC CL48'SXBR NF +Dnice/+0 NT'
00016BB0	00001000 00000000			3034 DC XL16'00001000000000000000000000000000'
00016BC0	E2E7C2D9 40D5C640			3035 DC CL48'SXBR NF +Dnice/+0 Tr'
00016BF0	5FFD0000 00000000			3036 DC XL16'5FFD0000000000000000000000000000'
00016C00	E2E7C2D9 40D5C640			3037 DC CL48'SXBR NF +Dnice/+Dnice NT'
00016C30	00000000 00000000			3038 DC XL16'00000000000000000000000000000000'
00016C40	E2E7C2D9 40D5C640			3039 DC CL48'SXBR NF +Dnice/+Dnice Tr'
00016C70	00000000 00000000			3040 DC XL16'00000000000000000000000000000000'
00016C80	E2E7C2D9 40D5C640			3041 DC CL48'SXBR NF +Dnice/+2.0 NT'
00016CB0	C0000000 00000000			3042 DC XL16'C0000000000000000000000000000000'
00016CC0	E2E7C2D9 40D5C640			3043 DC CL48'SXBR NF +Dnice/+2.0 Tr'
00016CF0	C0000000 00000000			3044 DC XL16'C0000000000000000000000000000000'
00016D00	E2E7C2D9 40D5C640			3045 DC CL48'SXBR NF +Dnice/+inf NT'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
00016D30	FFFF0000 00000000			3046	DC XL16'FFFF0000000000000000000000000000'
00016D40	E2E7C2D9 40D5C640			3047	DC CL48'SXBR NF +Dnice/+inf Tr'
00016D70	FFFF0000 00000000			3048	DC XL16'FFFF0000000000000000000000000000'
00016D80	E2E7C2D9 40D5C640			3049	DC CL48'SXBR NF +Dnice/-QNaN NT'
00016DB0	FFFF8B00 00000000			3050	DC XL16'FFFF8B00000000000000000000000000'
00016DC0	E2E7C2D9 40D5C640			3051	DC CL48'SXBR NF +Dnice/-QNaN Tr'
00016DF0	FFFF8B00 00000000			3052	DC XL16'FFFF8B00000000000000000000000000'
00016E00	E2E7C2D9 40D5C640			3053	DC CL48'SXBR NF +Dnice/+SNaN NT'
00016E30	7FFF8A00 00000000			3054	DC XL16'7FFF8A00000000000000000000000000'
00016E40	E2E7C2D9 40D5C640			3055	DC CL48'SXBR NF +Dnice/+SNaN Tr'
00016E70	00001000 00000000			3056	DC XL16'00001000000000000000000000000000'
00016E80	E2E7C2D9 40D5C640			3057	DC CL48'SXBR NF +2.0/-inf NT'
00016EB0	7FFF0000 00000000			3058	DC XL16'7FFF0000000000000000000000000000'
00016EC0	E2E7C2D9 40D5C640			3059	DC CL48'SXBR NF +2.0/-inf Tr'
00016EF0	7FFF0000 00000000			3060	DC XL16'7FFF0000000000000000000000000000'
00016F00	E2E7C2D9 40D5C640			3061	DC CL48'SXBR NF +2.0/-2.0 NT'
00016F30	40010000 00000000			3062	DC XL16'40010000000000000000000000000000'
00016F40	E2E7C2D9 40D5C640			3063	DC CL48'SXBR NF +2.0/-2.0 Tr'
00016F70	40010000 00000000			3064	DC XL16'40010000000000000000000000000000'
00016F80	E2E7C2D9 40D5C640			3065	DC CL48'SXBR NF +2.0/-Dnice NT'
00016FB0	40000000 00000000			3066	DC XL16'40000000000000000000000000000000'
00016FC0	E2E7C2D9 40D5C640			3067	DC CL48'SXBR NF +2.0/-Dnice Tr'
00016FF0	40000000 00000000			3068	DC XL16'40000000000000000000000000000000'
00017000	E2E7C2D9 40D5C640			3069	DC CL48'SXBR NF +2.0/-0 NT'
00017030	40000000 00000000			3070	DC XL16'40000000000000000000000000000000'
00017040	E2E7C2D9 40D5C640			3071	DC CL48'SXBR NF +2.0/-0 Tr'
00017070	40000000 00000000			3072	DC XL16'40000000000000000000000000000000'
00017080	E2E7C2D9 40D5C640			3073	DC CL48'SXBR NF +2.0/+0 NT'
000170B0	40000000 00000000			3074	DC XL16'40000000000000000000000000000000'
000170C0	E2E7C2D9 40D5C640			3075	DC CL48'SXBR NF +2.0/+0 Tr'
000170F0	40000000 00000000			3076	DC XL16'40000000000000000000000000000000'
00017100	E2E7C2D9 40D5C640			3077	DC CL48'SXBR NF +2.0/+Dnice NT'
00017130	40000000 00000000			3078	DC XL16'40000000000000000000000000000000'
00017140	E2E7C2D9 40D5C640			3079	DC CL48'SXBR NF +2.0/+Dnice Tr'
00017170	40000000 00000000			3080	DC XL16'40000000000000000000000000000000'
00017180	E2E7C2D9 40D5C640			3081	DC CL48'SXBR NF +2.0/+2.0 NT'
000171B0	00000000 00000000			3082	DC XL16'00000000000000000000000000000000'
000171C0	E2E7C2D9 40D5C640			3083	DC CL48'SXBR NF +2.0/+2.0 Tr'
000171F0	00000000 00000000			3084	DC XL16'00000000000000000000000000000000'
00017200	E2E7C2D9 40D5C640			3085	DC CL48'SXBR NF +2.0/+inf NT'
00017230	FFFF0000 00000000			3086	DC XL16'FFFF0000000000000000000000000000'
00017240	E2E7C2D9 40D5C640			3087	DC CL48'SXBR NF +2.0/+inf Tr'
00017270	FFFF0000 00000000			3088	DC XL16'FFFF0000000000000000000000000000'
00017280	E2E7C2D9 40D5C640			3089	DC CL48'SXBR NF +2.0/-QNaN NT'
000172B0	FFFF8B00 00000000			3090	DC XL16'FFFF8B00000000000000000000000000'
000172C0	E2E7C2D9 40D5C640			3091	DC CL48'SXBR NF +2.0/-QNaN Tr'
000172F0	FFFF8B00 00000000			3092	DC XL16'FFFF8B00000000000000000000000000'
00017300	E2E7C2D9 40D5C640			3093	DC CL48'SXBR NF +2.0/+SNaN NT'
00017330	7FFF8A00 00000000			3094	DC XL16'7FFF8A00000000000000000000000000'
00017340	E2E7C2D9 40D5C640			3095	DC CL48'SXBR NF +2.0/+SNaN Tr'
00017370	40000000 00000000			3096	DC XL16'40000000000000000000000000000000'
00017380	E2E7C2D9 40D5C640			3097	DC CL48'SXBR NF +inf/-inf NT'
000173B0	7FFF0000 00000000			3098	DC XL16'7FFF0000000000000000000000000000'
000173C0	E2E7C2D9 40D5C640			3099	DC CL48'SXBR NF +inf/-inf Tr'
000173F0	7FFF0000 00000000			3100	DC XL16'7FFF0000000000000000000000000000'
00017400	E2E7C2D9 40D5C640			3101	DC CL48'SXBR NF +inf/-2.0 NT'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
00017430	7FFF0000 00000000			3102 DC XL16'7FFF0000000000000000000000000000'
00017440	E2E7C2D9 40D5C640			3103 DC CL48'SXBR NF +inf/-2.0 Tr'
00017470	7FFF0000 00000000			3104 DC XL16'7FFF0000000000000000000000000000'
00017480	E2E7C2D9 40D5C640			3105 DC CL48'SXBR NF +inf/-Dnice NT'
000174B0	7FFF0000 00000000			3106 DC XL16'7FFF0000000000000000000000000000'
000174C0	E2E7C2D9 40D5C640			3107 DC CL48'SXBR NF +inf/-Dnice Tr'
000174F0	7FFF0000 00000000			3108 DC XL16'7FFF0000000000000000000000000000'
00017500	E2E7C2D9 40D5C640			3109 DC CL48'SXBR NF +inf/-0 NT'
00017530	7FFF0000 00000000			3110 DC XL16'7FFF0000000000000000000000000000'
00017540	E2E7C2D9 40D5C640			3111 DC CL48'SXBR NF +inf/-0 Tr'
00017570	7FFF0000 00000000			3112 DC XL16'7FFF0000000000000000000000000000'
00017580	E2E7C2D9 40D5C640			3113 DC CL48'SXBR NF +inf/+0 NT'
000175B0	7FFF0000 00000000			3114 DC XL16'7FFF0000000000000000000000000000'
000175C0	E2E7C2D9 40D5C640			3115 DC CL48'SXBR NF +inf/+0 Tr'
000175F0	7FFF0000 00000000			3116 DC XL16'7FFF0000000000000000000000000000'
00017600	E2E7C2D9 40D5C640			3117 DC CL48'SXBR NF +inf/+Dnice NT'
00017630	7FFF0000 00000000			3118 DC XL16'7FFF0000000000000000000000000000'
00017640	E2E7C2D9 40D5C640			3119 DC CL48'SXBR NF +inf/+Dnice Tr'
00017670	7FFF0000 00000000			3120 DC XL16'7FFF0000000000000000000000000000'
00017680	E2E7C2D9 40D5C640			3121 DC CL48'SXBR NF +inf/+2.0 NT'
000176B0	7FFF0000 00000000			3122 DC XL16'7FFF0000000000000000000000000000'
000176C0	E2E7C2D9 40D5C640			3123 DC CL48'SXBR NF +inf/+2.0 Tr'
000176F0	7FFF0000 00000000			3124 DC XL16'7FFF0000000000000000000000000000'
00017700	E2E7C2D9 40D5C640			3125 DC CL48'SXBR NF +inf/+inf NT'
00017730	7FFF8000 00000000			3126 DC XL16'7FFF8000000000000000000000000000'
00017740	E2E7C2D9 40D5C640			3127 DC CL48'SXBR NF +inf/+inf Tr'
00017770	7FFF0000 00000000			3128 DC XL16'7FFF0000000000000000000000000000'
00017780	E2E7C2D9 40D5C640			3129 DC CL48'SXBR NF +inf/-QNaN NT'
000177B0	FFFF8B00 00000000			3130 DC XL16'FFFF8B00000000000000000000000000'
000177C0	E2E7C2D9 40D5C640			3131 DC CL48'SXBR NF +inf/-QNaN Tr'
000177F0	FFFF8B00 00000000			3132 DC XL16'FFFF8B00000000000000000000000000'
00017800	E2E7C2D9 40D5C640			3133 DC CL48'SXBR NF +inf/+SNaN NT'
00017830	7FFF8A00 00000000			3134 DC XL16'7FFF8A00000000000000000000000000'
00017840	E2E7C2D9 40D5C640			3135 DC CL48'SXBR NF +inf/+SNaN Tr'
00017870	7FFF0000 00000000			3136 DC XL16'7FFF0000000000000000000000000000'
00017880	E2E7C2D9 40D5C640			3137 DC CL48'SXBR NF -QNaN/-inf NT'
000178B0	FFFF8B00 00000000			3138 DC XL16'FFFF8B00000000000000000000000000'
000178C0	E2E7C2D9 40D5C640			3139 DC CL48'SXBR NF -QNaN/-inf Tr'
000178F0	FFFF8B00 00000000			3140 DC XL16'FFFF8B00000000000000000000000000'
00017900	E2E7C2D9 40D5C640			3141 DC CL48'SXBR NF -QNaN/-2.0 NT'
00017930	FFFF8B00 00000000			3142 DC XL16'FFFF8B00000000000000000000000000'
00017940	E2E7C2D9 40D5C640			3143 DC CL48'SXBR NF -QNaN/-2.0 Tr'
00017970	FFFF8B00 00000000			3144 DC XL16'FFFF8B00000000000000000000000000'
00017980	E2E7C2D9 40D5C640			3145 DC CL48'SXBR NF -QNaN/-Dnice NT'
000179B0	FFFF8B00 00000000			3146 DC XL16'FFFF8B00000000000000000000000000'
000179C0	E2E7C2D9 40D5C640			3147 DC CL48'SXBR NF -QNaN/-Dnice Tr'
000179F0	FFFF8B00 00000000			3148 DC XL16'FFFF8B00000000000000000000000000'
00017A00	E2E7C2D9 40D5C640			3149 DC CL48'SXBR NF -QNaN/-0 NT'
00017A30	FFFF8B00 00000000			3150 DC XL16'FFFF8B00000000000000000000000000'
00017A40	E2E7C2D9 40D5C640			3151 DC CL48'SXBR NF -QNaN/-0 Tr'
00017A70	FFFF8B00 00000000			3152 DC XL16'FFFF8B00000000000000000000000000'
00017A80	E2E7C2D9 40D5C640			3153 DC CL48'SXBR NF -QNaN/+0 NT'
00017AB0	FFFF8B00 00000000			3154 DC XL16'FFFF8B00000000000000000000000000'
00017AC0	E2E7C2D9 40D5C640			3155 DC CL48'SXBR NF -QNaN/+0 Tr'
00017AF0	FFFF8B00 00000000			3156 DC XL16'FFFF8B00000000000000000000000000'
00017B00	E2E7C2D9 40D5C640			3157 DC CL48'SXBR NF -QNaN/+Dnice NT'

LOC	OBJECT	CODE	ADDR1	ADDR2	STMT
00018230	7FFF8A00	00000000			3214 DC XL16'7FFF8A000000000000000000000000'
00018240	E2E7C2D9	40D5C640			3215 DC CL48'SXBR NF +SNaN/+SNaN Tr'
00018270	7FFF0A00	00000000			3216 DC XL16'7FFF0A000000000000000000000000'
			000000C8	00000001	3217 XBFPNFOT_NUM EQU (*-XBFPNFOT_GOOD)/64
					3218 *
					3219 *
			00018280	00000001	3220 XBFPNFFL_GOOD EQU *
00018280	E2E7C2D9	40D5C640			3221 DC CL48'SXBR NF -inf/-inf FPCR'
000182B0	00800003	F8008003			3222 DC XL16'00800003F80080030000000000000000'
000182C0	E2E7C2D9	40D5C640			3223 DC CL48'SXBR NF -inf/-2.0 FPCR'
000182F0	00000001	F8000001			3224 DC XL16'00000001F80000010000000000000000'
00018300	E2E7C2D9	40D5C640			3225 DC CL48'SXBR NF -inf/-Dnice FPCR'
00018330	00000001	F8000001			3226 DC XL16'00000001F80000010000000000000000'
00018340	E2E7C2D9	40D5C640			3227 DC CL48'SXBR NF -inf/-0 FPCR'
00018370	00000001	F8000001			3228 DC XL16'00000001F80000010000000000000000'
00018380	E2E7C2D9	40D5C640			3229 DC CL48'SXBR NF -inf/+0 FPCR'
000183B0	00000001	F8000001			3230 DC XL16'00000001F80000010000000000000000'
000183C0	E2E7C2D9	40D5C640			3231 DC CL48'SXBR NF -inf/+Dnice FPCR'
000183F0	00000001	F8000001			3232 DC XL16'00000001F80000010000000000000000'
00018400	E2E7C2D9	40D5C640			3233 DC CL48'SXBR NF -inf/+2.0 FPCR'
00018430	00000001	F8000001			3234 DC XL16'00000001F80000010000000000000000'
00018440	E2E7C2D9	40D5C640			3235 DC CL48'SXBR NF -inf/+inf FPCR'
00018470	00000001	F8000001			3236 DC XL16'00000001F80000010000000000000000'
00018480	E2E7C2D9	40D5C640			3237 DC CL48'SXBR NF -inf/-QNaN FPCR'
000184B0	00000003	F8000003			3238 DC XL16'00000003F80000030000000000000000'
000184C0	E2E7C2D9	40D5C640			3239 DC CL48'SXBR NF -inf/+SNaN FPCR'
000184F0	00800003	F8008003			3240 DC XL16'00800003F80080030000000000000000'
00018500	E2E7C2D9	40D5C640			3241 DC CL48'SXBR NF -2.0/-inf FPCR'
00018530	00000002	F8000002			3242 DC XL16'00000002F80000020000000000000000'
00018540	E2E7C2D9	40D5C640			3243 DC CL48'SXBR NF -2.0/-2.0 FPCR'
00018570	00000000	F8000000			3244 DC XL16'00000000F80000000000000000000000'
00018580	E2E7C2D9	40D5C640			3245 DC CL48'SXBR NF -2.0/-Dnice FPCR'
000185B0	00080001	F8000C01			3246 DC XL16'00080001F8000C010000000000000000'
000185C0	E2E7C2D9	40D5C640			3247 DC CL48'SXBR NF -2.0/-0 FPCR'
000185F0	00000001	F8000001			3248 DC XL16'00000001F80000010000000000000000'
00018600	E2E7C2D9	40D5C640			3249 DC CL48'SXBR NF -2.0/+0 FPCR'
00018630	00000001	F8000001			3250 DC XL16'00000001F80000010000000000000000'
00018640	E2E7C2D9	40D5C640			3251 DC CL48'SXBR NF -2.0/+Dnice FPCR'
00018670	00080001	F8000801			3252 DC XL16'00080001F80008010000000000000000'
00018680	E2E7C2D9	40D5C640			3253 DC CL48'SXBR NF -2.0/+2.0 FPCR'
000186B0	00000001	F8000001			3254 DC XL16'00000001F80000010000000000000000'
000186C0	E2E7C2D9	40D5C640			3255 DC CL48'SXBR NF -2.0/+inf FPCR'
000186F0	00000001	F8000001			3256 DC XL16'00000001F80000010000000000000000'
00018700	E2E7C2D9	40D5C640			3257 DC CL48'SXBR NF -2.0/-QNaN FPCR'
00018730	00000003	F8000003			3258 DC XL16'00000003F80000030000000000000000'
00018740	E2E7C2D9	40D5C640			3259 DC CL48'SXBR NF -2.0/+SNaN FPCR'
00018770	00800003	F8008003			3260 DC XL16'00800003F80080030000000000000000'
00018780	E2E7C2D9	40D5C640			3261 DC CL48'SXBR NF -Dnice/-inf FPCR'
000187B0	00000002	F8000002			3262 DC XL16'00000002F80000020000000000000000'
000187C0	E2E7C2D9	40D5C640			3263 DC CL48'SXBR NF -Dnice/-2.0 FPCR'
000187F0	00080002	F8000C02			3264 DC XL16'00080002F8000C020000000000000000'
00018800	E2E7C2D9	40D5C640			3265 DC CL48'SXBR NF -Dnice/-Dnice FPCR'
00018830	00000000	F8000000			3266 DC XL16'00000000F80000000000000000000000'
00018840	E2E7C2D9	40D5C640			3267 DC CL48'SXBR NF -Dnice/-0 FPCR'
00018870	00000001	F8001001			3268 DC XL16'00000001F800100100000000000000000'
00018880	E2E7C2D9	40D5C640			3269 DC CL48'SXBR NF -Dnice/+0 FPCR'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
000188B0	00000001 F8001001			3270 DC XL16'00000001F80010010000000000000000'
000188C0	E2E7C2D9 40D5C640			3271 DC CL48'SXBR NF -Dnice/+Dnice FPCR'
000188F0	00000001 F8001001			3272 DC XL16'00000001F80010010000000000000000'
00018900	E2E7C2D9 40D5C640			3273 DC CL48'SXBR NF -Dnice/+2.0 FPCR'
00018930	00080001 F8000801			3274 DC XL16'00080001F80008010000000000000000'
00018940	E2E7C2D9 40D5C640			3275 DC CL48'SXBR NF -Dnice/+inf FPCR'
00018970	00000001 F8000001			3276 DC XL16'00000001F80000010000000000000000'
00018980	E2E7C2D9 40D5C640			3277 DC CL48'SXBR NF -Dnice/-QNaN FPCR'
000189B0	00000003 F8000003			3278 DC XL16'00000003F80000030000000000000000'
000189C0	E2E7C2D9 40D5C640			3279 DC CL48'SXBR NF -Dnice/+SNaN FPCR'
000189F0	00800003 F8008003			3280 DC XL16'00800003F80080030000000000000000'
00018A00	E2E7C2D9 40D5C640			3281 DC CL48'SXBR NF -0/-inf FPCR'
00018A30	00000002 F8000002			3282 DC XL16'00000002F80000020000000000000000'
00018A40	E2E7C2D9 40D5C640			3283 DC CL48'SXBR NF -0/-2.0 FPCR'
00018A70	00000002 F8000002			3284 DC XL16'00000002F80000020000000000000000'
00018A80	E2E7C2D9 40D5C640			3285 DC CL48'SXBR NF -0/-Dnice FPCR'
00018AB0	00000002 F8001002			3286 DC XL16'00000002F80010020000000000000000'
00018AC0	E2E7C2D9 40D5C640			3287 DC CL48'SXBR NF -0/-0 FPCR'
00018AF0	00000000 F8000000			3288 DC XL16'00000000F80000000000000000000000'
00018B00	E2E7C2D9 40D5C640			3289 DC CL48'SXBR NF -0/+0 FPCR'
00018B30	00000000 F8000000			3290 DC XL16'00000000F80000000000000000000000'
00018B40	E2E7C2D9 40D5C640			3291 DC CL48'SXBR NF -0/+Dnice FPCR'
00018B70	00000001 F8001001			3292 DC XL16'00000001F80010010000000000000000'
00018B80	E2E7C2D9 40D5C640			3293 DC CL48'SXBR NF -0/+2.0 FPCR'
00018BB0	00000001 F8000001			3294 DC XL16'00000001F80000010000000000000000'
00018BC0	E2E7C2D9 40D5C640			3295 DC CL48'SXBR NF -0/+inf FPCR'
00018BF0	00000001 F8000001			3296 DC XL16'00000001F80000010000000000000000'
00018C00	E2E7C2D9 40D5C640			3297 DC CL48'SXBR NF -0/-QNaN FPCR'
00018C30	00000003 F8000003			3298 DC XL16'00000003F80000030000000000000000'
00018C40	E2E7C2D9 40D5C640			3299 DC CL48'SXBR NF -0/+SNaN FPCR'
00018C70	00800003 F8008003			3300 DC XL16'00800003F80080030000000000000000'
00018C80	E2E7C2D9 40D5C640			3301 DC CL48'SXBR NF +0/-inf FPCR'
00018CB0	00000002 F8000002			3302 DC XL16'00000002F80000020000000000000000'
00018CC0	E2E7C2D9 40D5C640			3303 DC CL48'SXBR NF +0/-2.0 FPCR'
00018CF0	00000002 F8000002			3304 DC XL16'00000002F80000020000000000000000'
00018D00	E2E7C2D9 40D5C640			3305 DC CL48'SXBR NF +0/-Dnice FPCR'
00018D30	00000002 F8001002			3306 DC XL16'00000002F80010020000000000000000'
00018D40	E2E7C2D9 40D5C640			3307 DC CL48'SXBR NF +0/-0 FPCR'
00018D70	00000000 F8000000			3308 DC XL16'00000000F80000000000000000000000'
00018D80	E2E7C2D9 40D5C640			3309 DC CL48'SXBR NF +0/+0 FPCR'
00018DB0	00000000 F8000000			3310 DC XL16'00000000F80000000000000000000000'
00018DC0	E2E7C2D9 40D5C640			3311 DC CL48'SXBR NF +0/+Dnice FPCR'
00018DF0	00000001 F8001001			3312 DC XL16'00000001F80010010000000000000000'
00018E00	E2E7C2D9 40D5C640			3313 DC CL48'SXBR NF +0/+2.0 FPCR'
00018E30	00000001 F8000001			3314 DC XL16'00000001F80000010000000000000000'
00018E40	E2E7C2D9 40D5C640			3315 DC CL48'SXBR NF +0/+inf FPCR'
00018E70	00000001 F8000001			3316 DC XL16'00000001F80000010000000000000000'
00018E80	E2E7C2D9 40D5C640			3317 DC CL48'SXBR NF +0/-QNaN FPCR'
00018EB0	00000003 F8000003			3318 DC XL16'00000003F80000030000000000000000'
00018EC0	E2E7C2D9 40D5C640			3319 DC CL48'SXBR NF +0/+SNaN FPCR'
00018EF0	00800003 F8008003			3320 DC XL16'00800003F80080030000000000000000'
00018F00	E2E7C2D9 40D5C640			3321 DC CL48'SXBR NF +Dnice/-inf FPCR'
00018F30	00000002 F8000002			3322 DC XL16'00000002F80000020000000000000000'
00018F40	E2E7C2D9 40D5C640			3323 DC CL48'SXBR NF +Dnice/-2.0 FPCR'
00018F70	00080002 F8000802			3324 DC XL16'00080002F80008020000000000000000'
00018F80	E2E7C2D9 40D5C640			3325 DC CL48'SXBR NF +Dnice/-Dnice FPCR'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
00018FB0	00000002 F8001002			3326 DC XL16'00000002F80010020000000000000000'
00018FC0	E2E7C2D9 40D5C640			3327 DC CL48'SXBR NF +Dnice/-0 FPCR'
00018FF0	00000002 F8001002			3328 DC XL16'00000002F80010020000000000000000'
00019000	E2E7C2D9 40D5C640			3329 DC CL48'SXBR NF +Dnice/+0 FPCR'
00019030	00000002 F8001002			3330 DC XL16'00000002F80010020000000000000000'
00019040	E2E7C2D9 40D5C640			3331 DC CL48'SXBR NF +Dnice/+Dnice FPCR'
00019070	00000000 F8000000			3332 DC XL16'00000000F80000000000000000000000'
00019080	E2E7C2D9 40D5C640			3333 DC CL48'SXBR NF +Dnice/+2.0 FPCR'
000190B0	00080001 F8000C01			3334 DC XL16'00080001F8000C010000000000000000'
000190C0	E2E7C2D9 40D5C640			3335 DC CL48'SXBR NF +Dnice/+inf FPCR'
000190F0	00000001 F8000001			3336 DC XL16'00000001F80000010000000000000000'
00019100	E2E7C2D9 40D5C640			3337 DC CL48'SXBR NF +Dnice/-QNaN FPCR'
00019130	00000003 F8000003			3338 DC XL16'00000003F80000030000000000000000'
00019140	E2E7C2D9 40D5C640			3339 DC CL48'SXBR NF +Dnice/+SNaN FPCR'
00019170	00800003 F8008003			3340 DC XL16'00800003F80080030000000000000000'
00019180	E2E7C2D9 40D5C640			3341 DC CL48'SXBR NF +2.0/-inf FPCR'
000191B0	00000002 F8000002			3342 DC XL16'00000002F80000020000000000000000'
000191C0	E2E7C2D9 40D5C640			3343 DC CL48'SXBR NF +2.0/-2.0 FPCR'
000191F0	00000002 F8000002			3344 DC XL16'00000002F80000020000000000000000'
00019200	E2E7C2D9 40D5C640			3345 DC CL48'SXBR NF +2.0/-Dnice FPCR'
00019230	00080002 F8000802			3346 DC XL16'00080002F80008020000000000000000'
00019240	E2E7C2D9 40D5C640			3347 DC CL48'SXBR NF +2.0/-0 FPCR'
00019270	00000002 F8000002			3348 DC XL16'00000002F80000020000000000000000'
00019280	E2E7C2D9 40D5C640			3349 DC CL48'SXBR NF +2.0/+0 FPCR'
000192B0	00000002 F8000002			3350 DC XL16'00000002F80000020000000000000000'
000192C0	E2E7C2D9 40D5C640			3351 DC CL48'SXBR NF +2.0/+Dnice FPCR'
000192F0	00080002 F8000C02			3352 DC XL16'00080002F8000C020000000000000000'
00019300	E2E7C2D9 40D5C640			3353 DC CL48'SXBR NF +2.0/+2.0 FPCR'
00019330	00000000 F8000000			3354 DC XL16'00000000F80000000000000000000000'
00019340	E2E7C2D9 40D5C640			3355 DC CL48'SXBR NF +2.0/+inf FPCR'
00019370	00000001 F8000001			3356 DC XL16'00000001F80000010000000000000000'
00019380	E2E7C2D9 40D5C640			3357 DC CL48'SXBR NF +2.0/-QNaN FPCR'
000193B0	00000003 F8000003			3358 DC XL16'00000003F80000030000000000000000'
000193C0	E2E7C2D9 40D5C640			3359 DC CL48'SXBR NF +2.0/+SNaN FPCR'
000193F0	00800003 F8008003			3360 DC XL16'00800003F80080030000000000000000'
00019400	E2E7C2D9 40D5C640			3361 DC CL48'SXBR NF +inf/-inf FPCR'
00019430	00000002 F8000002			3362 DC XL16'00000002F80000020000000000000000'
00019440	E2E7C2D9 40D5C640			3363 DC CL48'SXBR NF +inf/-2.0 FPCR'
00019470	00000002 F8000002			3364 DC XL16'00000002F80000020000000000000000'
00019480	E2E7C2D9 40D5C640			3365 DC CL48'SXBR NF +inf/-Dnice FPCR'
000194B0	00000002 F8000002			3366 DC XL16'00000002F80000020000000000000000'
000194C0	E2E7C2D9 40D5C640			3367 DC CL48'SXBR NF +inf/-0 FPCR'
000194F0	00000002 F8000002			3368 DC XL16'00000002F80000020000000000000000'
00019500	E2E7C2D9 40D5C640			3369 DC CL48'SXBR NF +inf/+0 FPCR'
00019530	00000002 F8000002			3370 DC XL16'00000002F80000020000000000000000'
00019540	E2E7C2D9 40D5C640			3371 DC CL48'SXBR NF +inf/+Dnice FPCR'
00019570	00000002 F8000002			3372 DC XL16'00000002F80000020000000000000000'
00019580	E2E7C2D9 40D5C640			3373 DC CL48'SXBR NF +inf/+2.0 FPCR'
000195B0	00000002 F8000002			3374 DC XL16'00000002F80000020000000000000000'
000195C0	E2E7C2D9 40D5C640			3375 DC CL48'SXBR NF +inf/+inf FPCR'
000195F0	00800003 F8008003			3376 DC XL16'00800003F80080030000000000000000'
00019600	E2E7C2D9 40D5C640			3377 DC CL48'SXBR NF +inf/-QNaN FPCR'
00019630	00000003 F8000003			3378 DC XL16'00000003F80000030000000000000000'
00019640	E2E7C2D9 40D5C640			3379 DC CL48'SXBR NF +inf/+SNaN FPCR'
00019670	00800003 F8008003			3380 DC XL16'00800003F80080030000000000000000'
00019680	E2E7C2D9 40D5C640			3381 DC CL48'SXBR NF -QNaN/-inf FPCR'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
000196B0	00000003 F8000003			3382 DC XL16'00000003F80000030000000000000000'
000196C0	E2E7C2D9 40D5C640			3383 DC CL48'SXBR NF -QNaN/-2.0 FPCR'
000196F0	00000003 F8000003			3384 DC XL16'00000003F80000030000000000000000'
00019700	E2E7C2D9 40D5C640			3385 DC CL48'SXBR NF -QNaN/-Dnice FPCR'
00019730	00000003 F8000003			3386 DC XL16'00000003F80000030000000000000000'
00019740	E2E7C2D9 40D5C640			3387 DC CL48'SXBR NF -QNaN/-0 FPCR'
00019770	00000003 F8000003			3388 DC XL16'00000003F80000030000000000000000'
00019780	E2E7C2D9 40D5C640			3389 DC CL48'SXBR NF -QNaN/+0 FPCR'
000197B0	00000003 F8000003			3390 DC XL16'00000003F80000030000000000000000'
000197C0	E2E7C2D9 40D5C640			3391 DC CL48'SXBR NF -QNaN/+Dnice FPCR'
000197F0	00000003 F8000003			3392 DC XL16'00000003F80000030000000000000000'
00019800	E2E7C2D9 40D5C640			3393 DC CL48'SXBR NF -QNaN/+2.0 FPCR'
00019830	00000003 F8000003			3394 DC XL16'00000003F80000030000000000000000'
00019840	E2E7C2D9 40D5C640			3395 DC CL48'SXBR NF -QNaN/+inf FPCR'
00019870	00000003 F8000003			3396 DC XL16'00000003F80000030000000000000000'
00019880	E2E7C2D9 40D5C640			3397 DC CL48'SXBR NF -QNaN/-QNaN FPCR'
000198B0	00000003 F8000003			3398 DC XL16'00000003F80000030000000000000000'
000198C0	E2E7C2D9 40D5C640			3399 DC CL48'SXBR NF -QNaN/+SNaN FPCR'
000198F0	00800003 F8008003			3400 DC XL16'00800003F80080030000000000000000'
00019900	E2E7C2D9 40D5C640			3401 DC CL48'SXBR NF +SNaN/-inf FPCR'
00019930	00800003 F8008003			3402 DC XL16'00800003F80080030000000000000000'
00019940	E2E7C2D9 40D5C640			3403 DC CL48'SXBR NF +SNaN/-2.0 FPCR'
00019970	00800003 F8008003			3404 DC XL16'00800003F80080030000000000000000'
00019980	E2E7C2D9 40D5C640			3405 DC CL48'SXBR NF +SNaN/-Dnice FPCR'
000199B0	00800003 F8008003			3406 DC XL16'00800003F80080030000000000000000'
000199C0	E2E7C2D9 40D5C640			3407 DC CL48'SXBR NF +SNaN/-0 FPCR'
000199F0	00800003 F8008003			3408 DC XL16'00800003F80080030000000000000000'
00019A00	E2E7C2D9 40D5C640			3409 DC CL48'SXBR NF +SNaN/+0 FPCR'
00019A30	00800003 F8008003			3410 DC XL16'00800003F80080030000000000000000'
00019A40	E2E7C2D9 40D5C640			3411 DC CL48'SXBR NF +SNaN/+Dnice FPCR'
00019A70	00800003 F8008003			3412 DC XL16'00800003F80080030000000000000000'
00019A80	E2E7C2D9 40D5C640			3413 DC CL48'SXBR NF +SNaN/+2.0 FPCR'
00019AB0	00800003 F8008003			3414 DC XL16'00800003F80080030000000000000000'
00019AC0	E2E7C2D9 40D5C640			3415 DC CL48'SXBR NF +SNaN/+inf FPCR'
00019AF0	00800003 F8008003			3416 DC XL16'00800003F80080030000000000000000'
00019B00	E2E7C2D9 40D5C640			3417 DC CL48'SXBR NF +SNaN/-QNaN FPCR'
00019B30	00800003 F8008003			3418 DC XL16'00800003F80080030000000000000000'
00019B40	E2E7C2D9 40D5C640			3419 DC CL48'SXBR NF +SNaN/+SNaN FPCR'
00019B70	00800003 F8008003			3420 DC XL16'00800003F80080030000000000000000'
		00000064	00000001	3421 XBFPNFFL_NUM EQU (*-XBFPNFFL_GOOD)/64
				3422 *
				3423 *
		00019B80	00000001	3424 XBFPOUT_GOOD EQU *
00019B80	E2E7C2D9 40C640D6			3425 DC CL48'SXBR F Ovfl NT'
00019BB0	7FFFFFFF FFFFFFFF			3426 DC XL16'7FFFFFFF7FFFFFFF7FFFFFFF7FFFFFFF'
00019BC0	E2E7C2D9 40C640D6			3427 DC CL48'SXBR F Ovfl Tr'
00019BF0	7FFFFFFF FFFFFFFF			3428 DC XL16'7FFFFFFF7FFFFFFF7FFFFFFF7FFFFFFF'
00019C00	E2E7C2D9 40C640E4			3429 DC CL48'SXBR F Ufl 1 NT'
00019C30	0000FFFF FFFFFFFF			3430 DC XL16'0000FFFF00000000000000000000000'
00019C40	E2E7C2D9 40C640E4			3431 DC CL48'SXBR F Ufl 1 Tr'
00019C70	6000FFFF FFFFFFFF			3432 DC XL16'6000FFFF6000000000000000000000'
00019C80	E2E7C2D9 40C640E4			3433 DC CL48'SXBR F Ufl 2 NT'
00019CB0	000070F1 00000000			3434 DC XL16'000070F1000000000000000000000000'
00019CC0	E2E7C2D9 40C640E4			3435 DC CL48'SXBR F Ufl 2 Tr'
00019CF0	5FFFC3C4 00000000			3436 DC XL16'5FFFC3C4000000000000000000000000'
00019D00	E2E7C2D9 40C640D5			3437 DC CL48'SXBR F Nmin NT'

LOC	OBJECT	CODE	ADDR1	ADDR2	STMT
00019D30	00010000	00000000			3438 DC XL16'00010000000000000000000000000000'
00019D40	E2E7C2D9	40C640D5			3439 DC CL48'SXBR F Nmin Tr'
00019D70	00010000	00000000			3440 DC XL16'00010000000000000000000000000000'
00019D80	E2E7C2D9	40C640C9			3441 DC CL48'SXBR F Incr NT'
00019DB0	3FFF0000	00000000			3442 DC XL16'3FFF0000000000000000000000000000'
00019DC0	E2E7C2D9	40C640C9			3443 DC CL48'SXBR F Incr Tr'
00019DF0	3FFF0000	00000000			3444 DC XL16'3FFF0000000000000000000000000000'
00019E00	E2E7C2D9	40C640E3			3445 DC CL48'SXBR F Trun NT'
00019E30	3FFEFFFF	FFFFFFFF			3446 DC XL16'3FFEFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFF'
00019E40	E2E7C2D9	40C640E3			3447 DC CL48'SXBR F Trun Tr'
00019E70	3FFEFFFF	FFFFFFFF			3448 DC XL16'3FFEFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFF'
			0000000C	00000001	3449 XBFPOUT_NUM EQU (*-XBFPOUT_GOOD)/64
					3450 *
					3451 *
			00019E80	00000001	3452 XBFPFLGS_GOOD EQU *
00019E80	E2E7C2D9	40C640D6			3453 DC CL48'SXBR F Ovfl FPCR'
00019EB0	00000003	F8000003			3454 DC XL16'00000003F80000030000000000000000'
00019EC0	E2E7C2D9	40C640E4			3455 DC CL48'SXBR F Ufl 1 FPCR'
00019EF0	00000002	F8001002			3456 DC XL16'00000002F80010020000000000000000'
00019F00	E2E7C2D9	40C640E4			3457 DC CL48'SXBR F Ufl 2 FPCR'
00019F30	00000002	F8001002			3458 DC XL16'00000002F80010020000000000000000'
00019F40	E2E7C2D9	40C640D5			3459 DC CL48'SXBR F Nmin FPCR'
00019F70	00000002	F8000002			3460 DC XL16'00000002F80000020000000000000000'
00019F80	E2E7C2D9	40C640C9			3461 DC CL48'SXBR F Incr FPCR'
00019FB0	00080002	F8000C02			3462 DC XL16'00080002F8000C020000000000000000'
00019FC0	E2E7C2D9	40C640E3			3463 DC CL48'SXBR F Trun FPCR'
00019FF0	00080002	F8000802			3464 DC XL16'00080002F80008020000000000000000'
			00000006	00000001	3465 XBFPFLGS_NUM EQU (*-XBFPFLGS_GOOD)/64
					3466 *
					3467 *
			0001A000	00000001	3468 XBFPRMO_GOOD EQU *
0001A000	E2E7C2D9	40D9D440			3469 DC CL48'SXBR RM +NZ RNTE'
0001A030	3FFEFFFF	FFFFFFFF			3470 DC XL16'3FFEFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFF'
0001A040	E2E7C2D9	40D9D440			3471 DC CL48'SXBR RM +NZ RZ'
0001A070	3FFEFFFF	FFFFFFFF			3472 DC XL16'3FFEFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFF'
0001A080	E2E7C2D9	40D9D440			3473 DC CL48'SXBR RM +NZ RP'
0001A0B0	3FFF0000	00000000			3474 DC XL16'3FFF0000000000000000000000000000'
0001A0C0	E2E7C2D9	40D9D440			3475 DC CL48'SXBR RM +NZ RM'
0001A0F0	3FFEFFFF	FFFFFFFF			3476 DC XL16'3FFEFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFF'
0001A100	E2E7C2D9	40D9D440			3477 DC CL48'SXBR RM +NZ RFS'
0001A130	3FFEFFFF	FFFFFFFF			3478 DC XL16'3FFEFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFF'
0001A140	E2E7C2D9	40D9D440			3479 DC CL48'SXBR RM -NZ RNTE'
0001A170	BFFEFFFF	FFFFFFFF			3480 DC XL16'BFFEFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFF'
0001A180	E2E7C2D9	40D9D440			3481 DC CL48'SXBR RM -NZ RZ'
0001A1B0	BFFEFFFF	FFFFFFFF			3482 DC XL16'BFFEFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFF'
0001A1C0	E2E7C2D9	40D9D440			3483 DC CL48'SXBR RM -NZ RP'
0001A1F0	BFFEFFFF	FFFFFFFF			3484 DC XL16'BFFEFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFF'
0001A200	E2E7C2D9	40D9D440			3485 DC CL48'SXBR RM -NZ RM'
0001A230	BFFF0000	00000000			3486 DC XL16'BFFF0000000000000000000000000000'
0001A240	E2E7C2D9	40D9D440			3487 DC CL48'SXBR RM -NZ RFS'
0001A270	BFFEFFFF	FFFFFFFF			3488 DC XL16'BFFEFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFF'
0001A280	E2E7C2D9	40D9D440			3489 DC CL48'SXBR RM +NA RNTE'
0001A2B0	3FFF0000	00000000			3490 DC XL16'3FFF0000000000000000000000000000'
0001A2C0	E2E7C2D9	40D9D440			3491 DC CL48'SXBR RM +NA RZ'
0001A2F0	3FFEFFFF	FFFFFFFF			3492 DC XL16'3FFEFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFF'
0001A300	E2E7C2D9	40D9D440			3493 DC CL48'SXBR RM +NA RP'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
0001A330	3FFF0000 00000000			3494 DC XL16'3FFF0000000000000000000000000000'
0001A340	E2E7C2D9 40D9D440			3495 DC CL48'SXBR RM +NA RM'
0001A370	3FFEFFFF FFFFFFFF			3496 DC XL16'3FFEFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFF'
0001A380	E2E7C2D9 40D9D440			3497 DC CL48'SXBR RM +NA RFS'
0001A3B0	3FFEFFFF FFFFFFFF			3498 DC XL16'3FFEFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFF'
0001A3C0	E2E7C2D9 40D9D440			3499 DC CL48'SXBR RM -NA RNTE'
0001A3F0	BFFF0000 00000000			3500 DC XL16'BFFF0000000000000000000000000000'
0001A400	E2E7C2D9 40D9D440			3501 DC CL48'SXBR RM -NA RZ'
0001A430	BFFEFFFF FFFFFFFF			3502 DC XL16'BFFEFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFF'
0001A440	E2E7C2D9 40D9D440			3503 DC CL48'SXBR RM -NA RP'
0001A470	BFFEFFFF FFFFFFFF			3504 DC XL16'BFFEFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFF'
0001A480	E2E7C2D9 40D9D440			3505 DC CL48'SXBR RM -NA RM'
0001A4B0	BFFF0000 00000000			3506 DC XL16'BFFF0000000000000000000000000000'
0001A4C0	E2E7C2D9 40D9D440			3507 DC CL48'SXBR RM -NA RFS'
0001A4F0	BFFEFFFF FFFFFFFF			3508 DC XL16'BFFEFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFF'
0001A500	E2E7C2D9 40D9D440			3509 DC CL48'SXBR RM +TZ RNTE'
0001A530	3FFEFFFF FFFFFFFF			3510 DC XL16'3FFEFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFE'
0001A540	E2E7C2D9 40D9D440			3511 DC CL48'SXBR RM +TZ RZ'
0001A570	3FFEFFFF FFFFFFFF			3512 DC XL16'3FFEFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFE'
0001A580	E2E7C2D9 40D9D440			3513 DC CL48'SXBR RM +TZ RP'
0001A5B0	3FFEFFFF FFFFFFFF			3514 DC XL16'3FFEFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFF'
0001A5C0	E2E7C2D9 40D9D440			3515 DC CL48'SXBR RM +TZ RM'
0001A5F0	3FFEFFFF FFFFFFFF			3516 DC XL16'3FFEFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFE'
0001A600	E2E7C2D9 40D9D440			3517 DC CL48'SXBR RM +TZ RFS'
0001A630	3FFEFFFF FFFFFFFF			3518 DC XL16'3FFEFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFF'
0001A640	E2E7C2D9 40D9D440			3519 DC CL48'SXBR RM -TZ RNTE'
0001A670	BFFEFFFF FFFFFFFF			3520 DC XL16'BFFEFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFE'
0001A680	E2E7C2D9 40D9D440			3521 DC CL48'SXBR RM -TZ RZ'
0001A6B0	BFFEFFFF FFFFFFFF			3522 DC XL16'BFFEFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFE'
0001A6C0	E2E7C2D9 40D9D440			3523 DC CL48'SXBR RM -TZ RP'
0001A6F0	BFFEFFFF FFFFFFFF			3524 DC XL16'BFFEFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFE'
0001A700	E2E7C2D9 40D9D440			3525 DC CL48'SXBR RM -TZ RM'
0001A730	BFFEFFFF FFFFFFFF			3526 DC XL16'BFFEFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFF'
0001A740	E2E7C2D9 40D9D440			3527 DC CL48'SXBR RM -TZ RFS'
0001A770	BFFEFFFF FFFFFFFF			3528 DC XL16'BFFEFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFF'
0001A780	E2E7C2D9 40D9D440			3529 DC CL48'SXBR RM +TA RNTE'
0001A7B0	3FFF0000 00000000			3530 DC XL16'3FFF0000000000000000000000000000'
0001A7C0	E2E7C2D9 40D9D440			3531 DC CL48'SXBR RM +TA RZ'
0001A7F0	3FFEFFFF FFFFFFFF			3532 DC XL16'3FFEFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFF'
0001A800	E2E7C2D9 40D9D440			3533 DC CL48'SXBR RM +TA RP'
0001A830	3FFF0000 00000000			3534 DC XL16'3FFF0000000000000000000000000000'
0001A840	E2E7C2D9 40D9D440			3535 DC CL48'SXBR RM +TA RM'
0001A870	3FFEFFFF FFFFFFFF			3536 DC XL16'3FFEFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFF'
0001A880	E2E7C2D9 40D9D440			3537 DC CL48'SXBR RM +TA RFS'
0001A8B0	3FFEFFFF FFFFFFFF			3538 DC XL16'3FFEFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFF'
0001A8C0	E2E7C2D9 40D9D440			3539 DC CL48'SXBR RM -TA RNTE'
0001A8F0	BFFF0000 00000000			3540 DC XL16'BFFF0000000000000000000000000000'
0001A900	E2E7C2D9 40D9D440			3541 DC CL48'SXBR RM -TA RZ'
0001A930	BFFEFFFF FFFFFFFF			3542 DC XL16'BFFEFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFF'
0001A940	E2E7C2D9 40D9D440			3543 DC CL48'SXBR RM -TA RP'
0001A970	BFFEFFFF FFFFFFFF			3544 DC XL16'BFFEFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFF'
0001A980	E2E7C2D9 40D9D440			3545 DC CL48'SXBR RM -TA RM'
0001A9B0	BFFF0000 00000000			3546 DC XL16'BFFF0000000000000000000000000000'
0001A9C0	E2E7C2D9 40D9D440			3547 DC CL48'SXBR RM -TA RFS'
0001A9F0	BFFEFFFF FFFFFFFF			3548 DC XL16'BFFEFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFF'

00000028 00000001

3549 XBFPRMO_NUM EQU (*-XBFPRMO_GOOD)/64

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				3550 *
				3551 *
		0001AA00	00000001	3552 XBFPRMOF_GOOD EQU *
0001AA00	E2E7C2D9	40D9D440		3553 DC CL48'SXBR RM +NZ RNTE, RZ,RP,RM FPCR'
0001AA30	00080002	00080002		3554 DC XL16'00080002000800020008000200080002'
0001AA40	E2E7C2D9	40D9D440		3555 DC CL48'SXBR RM +NZ RFS FPCR'
0001AA70	00080002	00000000		3556 DC XL16'000800020000000000000000000000'
0001AA80	E2E7C2D9	40D9D440		3557 DC CL48'SXBR RM -NZ RNTE, RZ,RP,RM FPCR'
0001AAB0	00080001	00080001		3558 DC XL16'00080001000800010008000100080001'
0001AAC0	E2E7C2D9	40D9D440		3559 DC CL48'SXBR RM -NZ RFS FPCR'
0001AAF0	00080001	00000000		3560 DC XL16'000800010000000000000000000000'
0001AB00	E2E7C2D9	40D9D440		3561 DC CL48'SXBR RM +NA RNTE, RZ,RP,RM FPCR'
0001AB30	00080002	00080002		3562 DC XL16'00080002000800020008000200080002'
0001AB40	E2E7C2D9	40D9D440		3563 DC CL48'SXBR RM +NA RFS FPCR'
0001AB70	00080002	00000000		3564 DC XL16'000800020000000000000000000000'
0001AB80	E2E7C2D9	40D9D440		3565 DC CL48'SXBR RM -NA RNTE, RZ,RP,RM FPCR'
0001ABB0	00080001	00080001		3566 DC XL16'00080001000800010008000100080001'
0001ABC0	E2E7C2D9	40D9D440		3567 DC CL48'SXBR RM -NA RFS FPCR'
0001ABF0	00080001	00000000		3568 DC XL16'000800010000000000000000000000'
0001AC00	E2E7C2D9	40D9D440		3569 DC CL48'SXBR RM +TZ RNTE, RZ,RP,RM FPCR'
0001AC30	00080002	00080002		3570 DC XL16'00080002000800020008000200080002'
0001AC40	E2E7C2D9	40D9D440		3571 DC CL48'SXBR RM +TZ RFS FPCR'
0001AC70	00080002	00000000		3572 DC XL16'000800020000000000000000000000'
0001AC80	E2E7C2D9	40D9D440		3573 DC CL48'SXBR RM -TZ RNTE, RZ,RP,RM FPCR'
0001ACB0	00080001	00080001		3574 DC XL16'00080001000800010008000100080001'
0001ACC0	E2E7C2D9	40D9D440		3575 DC CL48'SXBR RM -TZ RFS FPCR'
0001ACF0	00080001	00000000		3576 DC XL16'000800010000000000000000000000'
0001AD00	E2E7C2D9	40D9D440		3577 DC CL48'SXBR RM +TA RNTE, RZ,RP,RM FPCR'
0001AD30	00080002	00080002		3578 DC XL16'00080002000800020008000200080002'
0001AD40	E2E7C2D9	40D9D440		3579 DC CL48'SXBR RM +TA RFS FPCR'
0001AD70	00080002	00000000		3580 DC XL16'000800020000000000000000000000'
0001AD80	E2E7C2D9	40D9D440		3581 DC CL48'SXBR RM -TA RNTE, RZ,RP,RM FPCR'
0001ADB0	00080001	00080001		3582 DC XL16'00080001000800010008000100080001'
0001ADC0	E2E7C2D9	40D9D440		3583 DC CL48'SXBR RM -TA RFS FPCR'
0001ADF0	00080001	00000000		3584 DC XL16'000800010000000000000000000000'
		00000010	00000001	3585 XBFPRMOF_NUM EQU (*-XBFPRMOF_GOOD)/64

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				3627 *****
				3628 * VERIFICATION ROUTINE
				3629 *****
0001AEA0				3631 VERISUB DS 0H
				3632 *
				3633 ** Loop through the VERIFY TABLE...
				3634 *
0001AEA0	4110 C32C		0001B12C	3636 LA R1,VERIFTAB R1 --> Verify table
0001AEA4	4120 0012		00000012	3637 LA R2,VERIFLEN R2 <= Number of entries
0001AEA8	0D30			3638 BASR R3,0 Set top of loop
0001AEAA	9846 1000		00000000	3640 LM R4,R6,0(R1) Load verify table values
0001AEAE	4D70 C0C2		0001AEC2	3641 BAS R7,VERIFY Verify results
0001AEB2	4110 100C		0000000C	3642 LA R1,12(,R1) Next verify table entry
0001AEB6	0623			3643 BCTR R2,R3 Loop through verify table
0001AEB8	9500 C278		0001B078	3645 CLI FAILFLAG,X'00' Did all tests verify okay?
0001AEB0	078D			3646 BER R13 Yes, return to caller
0001AEBE	47F0 F238		00000238	3647 B FAIL No, load FAILURE disabled wait PSW
				3649 *
				3650 ** Loop through the ACTUAL / EXPECTED results...
				3651 *
0001AEC2	0D80			3653 VERIFY BASR R8,0 Set top of loop
0001AEC4	D50F 4000 5030	00000000	00000030	3655 CLC 0(16,R4),48(R5) Actual results == Expected results?
0001AEC6	4770 C0DA		0001AEDA	3656 BNE VERIFAIL No, show failure
0001AEC8	4140 4010		00000010	3657 VERINEXT LA R4,16(,R4) Next actual result
0001AED0	4150 5040		00000040	3658 LA R5,64(,R5) Next expected result
0001AED2	0668			3659 BCTR R6,R8 Loop through results
0001AED8	07F7			3661 BR R7 Return to caller

LOC	OBJECT	CODE	ADDR1	ADDR2	STMT
					3663 *****
					3664 * Report the failure...
					3665 *****
0001AEDA	9005	C250		0001B050	3667 VERIFAIL STM R0,R5,SAVER0R5 Save registers
0001AEDE	92FF	C278		0001B078	3668 MVI FAILFLAG,X'FF' Remember verification failure
					3669 *
					3670 ** First, show them the description...
					3671 *
0001AEE2	D22F	C1E0	5000	0001AFE0	00000000 3672 MVC FAILDESC,0(R5) Save results/test description
0001AEE8	4100	0044		00000044	3673 LA R0,L'FAILMSG1 R0 <= length of message
0001AEEC	4110	C1CC		0001AFCC	3674 LA R1,FAILMSG1 R1 --> the message text itself
0001AEF0	4520	C27A		0001B07A	3675 BAL R2,MSG Go display this message
					3676 *
					3677 ** Save address of actual and expected results
					3678 *
0001AEF4	5040	C24C		0001B04C	3679 ST R4,AACTUAL Save A(actual results)
0001AEF8	4150	5030		00000030	3680 LA R5,48(,R5) R5 ==> expected results
0001AEFC	5050	C248		0001B048	3681 ST R5,AEXPECT Save A(expected results)
					3682 *
					3683 ** Format and show them the EXPECTED ("Want") results...
					3684 *
0001AF00	D205	C210	C408	0001B010	0001B208 3685 MVC WANTGOT,=CL6'Want: '
0001AF06	F384	C216	C248	0001B016	0001B048 3686 UNPK FAILADR(L'FAILADR+1),AEXPECT(L'AEXPECT+1)
0001AF0C	9240	C21E		0001B01E	3687 MVI BLANKEQ,C' '
0001AF10	DC07	C216	C178	0001B016	0001AF78 3688 TR FAILADR,HEXTRTAB
0001AF16	F384	C221	5000	0001B021	00000000 3690 UNPK FAILVALS+(0*9)(9),(0*4)(5,R5)
0001AF1C	9240	C229		0001B029	3691 MVI FAILVALS+(0*9)+8,C' '
0001AF20	DC07	C221	C178	0001B021	0001AF78 3692 TR FAILVALS+(0*9)(8),HEXTRTAB
0001AF26	F384	C22A	5004	0001B02A	00000004 3694 UNPK FAILVALS+(1*9)(9),(1*4)(5,R5)
0001AF2C	9240	C232		0001B032	3695 MVI FAILVALS+(1*9)+8,C' '
0001AF30	DC07	C22A	C178	0001B02A	0001AF78 3696 TR FAILVALS+(1*9)(8),HEXTRTAB
0001AF36	F384	C233	5008	0001B033	00000008 3698 UNPK FAILVALS+(2*9)(9),(2*4)(5,R5)
0001AF3C	9240	C23B		0001B03B	3699 MVI FAILVALS+(2*9)+8,C' '
0001AF40	DC07	C233	C178	0001B033	0001AF78 3700 TR FAILVALS+(2*9)(8),HEXTRTAB
0001AF46	F384	C23C	500C	0001B03C	0000000C 3702 UNPK FAILVALS+(3*9)(9),(3*4)(5,R5)
0001AF4C	9240	C244		0001B044	3703 MVI FAILVALS+(3*9)+8,C' '
0001AF50	DC07	C23C	C178	0001B03C	0001AF78 3704 TR FAILVALS+(3*9)(8),HEXTRTAB
0001AF56	4100	0035		00000035	3706 LA R0,L'FAILMSG2 R0 <= length of message
0001AF5A	4110	C210		0001B010	3707 LA R1,FAILMSG2 R1 --> the message text itself
0001AF5E	4520	C27A		0001B07A	3708 BAL R2,MSG Go display this message

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				3710 *
				3711 **
				3712 *
0001AF62	D205 C210 C40E	0001B010	0001B20E	3713 MVC WANTGOT,=CL6'Got: '
0001AF68	F384 C216 C24C	0001B016	0001B04C	3714 UNPK FAILADR(L'FAILADR+1),AACTUAL(L'AACTUAL+1)
0001AF6E	9240 C21E		0001B01E	3715 MVI BLANKEQ,C' '
0001AF72	DC07 C216 C178	0001B016	0001AF78	3716 TR FAILADR,HEXTRTAB
0001AF78	F384 C221 4000	0001B021	00000000	3718 UNPK FAILVALS+(0*9)(9),(0*4)(5,R4)
0001AF7E	9240 C229		0001B029	3719 MVI FAILVALS+(0*9)+8,C' '
0001AF82	DC07 C221 C178	0001B021	0001AF78	3720 TR FAILVALS+(0*9)(8),HEXTRTAB
0001AF88	F384 C22A 4004	0001B02A	00000004	3722 UNPK FAILVALS+(1*9)(9),(1*4)(5,R4)
0001AF8E	9240 C232		0001B032	3723 MVI FAILVALS+(1*9)+8,C' '
0001AF92	DC07 C22A C178	0001B02A	0001AF78	3724 TR FAILVALS+(1*9)(8),HEXTRTAB
0001AF98	F384 C233 4008	0001B033	00000008	3726 UNPK FAILVALS+(2*9)(9),(2*4)(5,R4)
0001AF9E	9240 C23B		0001B03B	3727 MVI FAILVALS+(2*9)+8,C' '
0001AFA2	DC07 C233 C178	0001B033	0001AF78	3728 TR FAILVALS+(2*9)(8),HEXTRTAB
0001AFA8	F384 C23C 400C	0001B03C	0000000C	3730 UNPK FAILVALS+(3*9)(9),(3*4)(5,R4)
0001AFAE	9240 C244		0001B044	3731 MVI FAILVALS+(3*9)+8,C' '
0001AFB2	DC07 C23C C178	0001B03C	0001AF78	3732 TR FAILVALS+(3*9)(8),HEXTRTAB
0001AFB8	4100 0035		00000035	3734 LA R0,L'FAILMSG2 R0 <= length of message
0001AFBC	4110 C210		0001B010	3735 LA R1,FAILMSG2 R1 --> the message text itself
0001AFC0	4520 C27A		0001B07A	3736 BAL R2,MSG Go display this message
0001AFC4	9805 C250		0001B050	3738 LM R0,R5,SAVER0R5 Restore registers
0001AFC8	47F0 C0CE		0001AECE	3739 B VERINEXT Continue with verification...
0001AFCC				3741 FAILMSG1 DS 0CL68
0001AFCC	C3D6D4D7 C1D9C9E2			3742 DC CL20'COMPARISON FAILURE! '
0001AFE0	4D8485A2 83998997			3743 FAILDESC DC CL48'(description)'
0001B010				3745 FAILMSG2 DS 0CL53
0001B010	40404040 4040			3746 WANTGOT DC CL6' ' 'Want: ' -or- 'Got: '
0001B016	C1C1C1C1 C1C1C1C1			3747 FAILADR DC CL8'AAAAAAA'
0001B01E	407E40			3748 BLANKEQ DC CL3' = '
0001B021	88888888 88888888			3749 FAILVALS DC CL36'hhhhhhhh hhhhhhhh hhhhhhhh hhhhhhhh '
0001B048	00000000			3751 AEXPECT DC F'0' ==> Expected ("Want") results
0001B04C	00000000			3752 AACTUAL DC F'0' ==> Actual ("Got") results
0001B050	00000000 00000000			3753 SAVER0R5 DC 6F'0' Registers R0 - R5 save area
0001B068	F0F1F2F3 F4F5F6F7			3754 CHARHEX DC CL16'0123456789ABCDEF'
		0001AF78	00000010	3755 HEXTRTAB EQU CHARHEX-X'F0' Hexadecimal translation table
0001B078	00			3756 FAILFLAG DC X'00' FF = Fail, 00 = Success

LOC	OBJECT CODE	ADDR1	ADDR2	STMT				
				3758	*****			
				3759	* Issue HERCULES MESSAGE pointed to by R1, length in R0			
				3760	*****			
0001B07A	4900 C404		0001B204	3762	MSG	CH	R0,=H'0'	Do we even HAVE a message?
0001B07E	07D2			3763		BNHR	R2	No, ignore
0001B080	9002 C2B0		0001B0B0	3765		STM	R0,R2,MSGSAVE	Save registers
0001B084	4900 C406		0001B206	3767		CH	R0,=AL2(L'MSGMSG)	Message length within limits?
0001B088	47D0 C290		0001B090	3768		BNH	MSGOK	Yes, continue
0001B08C	4100 005F		0000005F	3769		LA	R0,L'MSGMSG	No, set to maximum
0001B090	1820			3771	MSGOK	LR	R2,R0	Copy length to work register
0001B092	0620			3772		BCTR	R2,0	Minus-1 for execute
0001B094	4420 C2BC		0001B0BC	3773		EX	R2,MSGMVC	Copy message to O/P buffer
0001B098	4120 200A		0000000A	3775		LA	R2,1+L'MSGCMD(,R2)	Calculate true command length
0001B09C	4110 C2C2		0001B0C2	3776		LA	R1,MSGCMD	Point to true command
0001B0A0	83120008			3778		DC	X'83',X'12',X'0008'	Issue Hercules Diagnose X'008'
0001B0A4	4780 C2AA		0001B0AA	3779		BZ	MSGRET	Return if successful
0001B0A8	0000			3780		DC	H'0'	CRASH for debugging purposes
0001B0AA	9802 C2B0		0001B0B0	3782	MSGRET	LM	R0,R2,MSGSAVE	Restore registers
0001B0AE	07F2			3783		BR	R2	Return to caller
0001B0B0	00000000 00000000			3785	MSGSAVE	DC	3F'0'	Registers save area
0001B0BC	D200 C2CB 1000	0001B0CB	00000000	3786	MSGMVC	MVC	MSGMSG(0),0(R1)	Executed instruction
0001B0C2	D4E2C7D5 D6C8405C			3788	MSGCMD	DC	C'MSGNOH * '	*** HERCULES MESSAGE COMMAND ***
0001B0CB	40404040 40404040			3789	MSGMSG	DC	CL95' '	The message text to be displayed

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				3791 *****
				3792 * VERIFY TABLE
				3793 *****
				3794 *
				3795 * A(actual results), A(expected results), A(#of results)
				3796 *
				3797 *****
0001B12C				3799 VERIFTAB DC 0F'0'
0001B12C	00001000			3800 DC A(SBFPNFOT)
0001B130	0000B000			3801 DC A(SBFPNFOT_GOOD)
0001B134	00000064			3802 DC A(SBFPNFOT_NUM)
				3803 *
0001B138	00001700			3804 DC A(SBFPNFFL)
0001B13C	0000C900			3805 DC A(SBFPNFFL_GOOD)
0001B140	00000064			3806 DC A(SBFPNFFL_NUM)
				3807 *
0001B144	00001E00			3808 DC A(SBFPOUT)
0001B148	0000E200			3809 DC A(SBFPOUT_GOOD)
0001B14C	00000006			3810 DC A(SBFPOUT_NUM)
				3811 *
0001B150	00001F00			3812 DC A(SBFPFLGS)
0001B154	0000E380			3813 DC A(SBFPFLGS_GOOD)
0001B158	00000006			3814 DC A(SBFPFLGS_NUM)
				3815 *
0001B15C	00002000			3816 DC A(SBFPRMO)
0001B160	0000E500			3817 DC A(SBFPRMO_GOOD)
0001B164	00000018			3818 DC A(SBFPRMO_NUM)
				3819 *
0001B168	00002300			3820 DC A(SBFPRMOF)
0001B16C	0000EB00			3821 DC A(SBFPRMOF_GOOD)
0001B170	00000018			3822 DC A(SBFPRMOF_NUM)
				3823 *
0001B174	00004000			3824 DC A(LBFPNFOT)
0001B178	0000F100			3825 DC A(LBFPNFOT_GOOD)
0001B17C	000000C8			3826 DC A(LBFPNFOT_NUM)
				3827 *
0001B180	00004D00			3828 DC A(LBFPNFFL)
0001B184	00012300			3829 DC A(LBFPNFFL_GOOD)
0001B188	00000064			3830 DC A(LBFPNFFL_NUM)
				3831 *
0001B18C	00005400			3832 DC A(LBFPOUT)
0001B190	00013C00			3833 DC A(LBFPOUT_GOOD)
0001B194	0000000C			3834 DC A(LBFPOUT_NUM)
				3835 *
0001B198	00005600			3836 DC A(LBFPFLGS)
0001B19C	00013F00			3837 DC A(LBFPFLGS_GOOD)
0001B1A0	00000006			3838 DC A(LBFPFLGS_NUM)
				3839 *
0001B1A4	00005700			3840 DC A(LBFPRMO)
0001B1A8	00014080			3841 DC A(LBFPRMO_GOOD)
0001B1AC	00000028			3842 DC A(LBFPRMO_NUM)
				3843 *
0001B1B0	00005C00			3844 DC A(LBFPRMOF)
0001B1B4	00014A80			3845 DC A(LBFPRMOF_GOOD)
0001B1B8	00000018			3846 DC A(LBFPRMOF_NUM)

SYMBOL	TYPE	VALUE	LENGTH	DEFN	REFERENCES
SBFPRMO_NUM	U	000018	1	1973	3818
SHORTF	F	000324	4	255	206
SHORTNF	F	000314	4	249	204
START	H	000280	2	199	164
STRTLABL	U	000000	1	108	158 161 163 166 174 1429 1431 1434 1436 1439 1441 1445 1447 1450
					1452 1455 1457 1461 1463 1466 1468 1471 1473 1482
VERIFAIL	I	01AEDA	4	3667	3656
VERIFLEN	U	000012	1	3872	3637
VERIFTAB	F	01B12C	4	3799	3872 3636
VERIFY	I	01AEC2	2	3653	3641
VERINEXT	I	01AECE	4	3657	3739
VERISUB	H	01AEA0	2	3631	230
WANTGOT	C	01B010	6	3746	3685 3713
XBFPCT	U	000006	1	1356	292
XBFPF	I	0007EA	4	809	221
XBFPFLGS	U	009600	1	1468	295 3860
XBFPFLGS_GOOD	U	019E80	1	3452	3465 3861
XBFPFLGS_NUM	U	000006	1	3465	3862
XBFPIN	F	000B38	4	1314	1356 293
XBFPINRM	D	000BF8	8	1382	1421 299
XBFPNF	H	000760	2	747	219
XBFPNFCT	U	00000A	1	1293	286
XBFPNFFL	U	008D00	1	1463	289 3852
XBFPNFFL_GOOD	U	018280	1	3220	3421 3853
XBFPNFFL_NUM	U	000064	1	3421	3854
XBFPNFIN	F	000A98	4	1282	1293 287
XBFPNFOT	U	008000	1	1461	288 3848
XBFPNFOT_GOOD	U	015080	1	2816	3217 3849
XBFPNFOT_NUM	U	0000C8	1	3217	3850
XBFPOUT	U	009400	1	1466	294 3856
XBFPOUT_GOOD	U	019B80	1	3424	3449 3857
XBFPOUT_NUM	U	00000C	1	3449	3858
XBFPRM	I	000860	4	864	223
XBFPRMCT	U	000008	1	1421	298
XBFPRMO	U	009700	1	1471	300 3864
XBFPRMOF	U	009C00	1	1473	301 3868
XBFPRMOF_GOOD	U	01AA00	1	3552	3585 3869
XBFPRMOF_NUM	U	000010	1	3585	3870
XBFPRMO_GOOD	U	01A000	1	3468	3549 3865
XBFPRMO_NUM	U	000028	1	3549	3866
XTNDF	F	000384	4	291	220
XTNDNF	F	000374	4	285	218
=AL2(L'MSGMSG)	R	01B206	2	3876	3767
=CL6'Got: '	C	01B20E	6	3878	3713
=CL6'Want: '	C	01B208	6	3877	3685
=H'0'	H	01B204	2	3875	3762

MACRO DEFN REFERENCES

No defined macros

DESC	SYMBOL	SIZE	POS	ADDR
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Entry: 0

Image	IMAGE	111124	00000-1B213	00000-1B213
Region		111124	00000-1B213	00000-1B213
CSECT	BFPSUBTR	111124	00000-1B213	00000-1B213

STMT

FILE NAME

```
1 c:\Users\Fish\Documents\Visual Studio 2008\Projects\MyProjects\ASMA-0\bfp-018-subtract\bfp-018-subtract.asm
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** NO ERRORS FOUND **
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