

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				2 *****
				3 *
				4 * CU12 instruction tests
				5 *
				6 * NOTE: This test is based the CLCL-et-al Test
				7 * modified to only test the Performance
				8 * of the CU12 instruction.
				9 *
				10 * The MSG routine is from the Hercules Binary
				11 * Floating Point Validation Package by Stephen R. Orso
				12 *
				13 * *****
				14 * ** IMPORTANT! **
				15 * *****
				16 *
				17 * This test uses the Hercules Diagnose X'008' interface
				18 * to display messages and thus your .tst runtest script
				19 * MUST contain a "DIAG8CMD ENABLE" statement within it!
				20 *
				21 * James Wekel February 2024
				22 *****
				24 *****
				25 *
				26 * CU12 Performance instruction tests
				27 *
				28 *****
				29 *
				30 * This program ONLY tests the performance of the CU12
				31 * instructions.
				32 *
				33 * Tests:
				34 *
				35 * All tests are 'CU12 R0,R2'
				36 *
				37 * 1. CU12 with CC=0 - no crossed pages
				38 * source: 61 bytes (28 UTF8 Chars)
				39 *
				40 * 2. CU12 with CC=0 - source cross page
				41 * source: 61 bytes (28 UTF8 Chars)
				42 *
				43 * 3. CU12 with CC=0 - target cross page
				44 * source: 61 bytes (28 UTF8 Chars)
				45 *
				46 * 4. CU12 with CC=0 - both arguments crossed pages
				47 * source: 61 bytes (28 UTF8 Chars)
				48 *
				49 * 5. CU12 with CC=3 - both arguments crossed pages
				50 * source: 13,738 bytes only 4095+
				51 * processed
				52 *
				53 *****

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
				55	*****
				56	*
				57	* Example Hercules Testcase:
				58	*
				59	*
				60	* *Testcase CU12-02-performance (Test CU12 instructions)
				61	* main size 16
				62	* numcpu 1
				63	* sysclear
				64	* archlvl z/Arch
				65	*
				66	* loadcore "\$(testpath)/CU12-02-performance.core" 0x0
				67	*
				68	* diag8cmd enable # (needed for messages to Hercules console)
				69	* #r 408=ff # (enable timing tests)
				70	* runtest 300 # (test duration, depends on host)
				71	* diag8cmd disable # (reset back to default)
				72	*
				73	* *Done
				74	*
				75	*
				76	*****
				78	*****
				79	*
				80	*****
				81	*
00000000		00000000	00000D17	82	CU122TST START 0
		00000000		83	USING CU122TST, R0 Low core addressability
00000000		00000000	000001A0	85	ORG CU122TST+X' 1A0' z/Architecture RESTART PSW
000001A0	00000001 80000000			86	DC X' 00000000180000000'
000001A8	00000000 00000200			87	DC AD(BEGIN)
000001B0		000001B0	000001D0	89	ORG CU122TST+X' 1D0' z/Architecture PROGRAM CHECK PSW
000001D0	00020001 80000000			90	DC X' 0002000180000000'
000001D8	00000000 0000DEAD			91	DC AD(X' DEAD')
000001E0		000001E0	00000200	93	ORG CU122TST+X' 200' Start of actual test program..

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
				835	*****
				836	* CALCDUR Calculate DURATION
				837	*****
00000B08	50F0 894C		00000B4C	839	CALCDUR ST R15, CALCRET Save return address
00000B0C	9057 8950		00000B50	840	STM R5, R7, CALCWORK Save work registers
				841	*
00000B10	9867 8A90		00000C90	842	LM R6, R7, BEGCLOCK Remove CPU number from clock value
00000B14	8C60 0006		00000006	843	SRDL R6, 6 "
00000B18	8D60 0006		00000006	844	SLDL R6, 6 "
00000B1C	9067 8A90		00000C90	845	STM R6, R7, BEGCLOCK "
				846	*
00000B20	9867 8A98		00000C98	847	LM R6, R7, ENDCLOCK Remove CPU number from clock value
00000B24	8C60 0006		00000006	848	SRDL R6, 6 "
00000B28	8D60 0006		00000006	849	SLDL R6, 6 "
00000B2C	9067 8A98		00000C98	850	STM R6, R7, ENDCLOCK "
				851	*
00000B30	4150 8A90		00000C90	852	LA R5, BEGCLOCK Starting time
00000B34	4160 8A98		00000C98	853	LA R6, ENDCLOCK Ending time
00000B38	4170 8AA0		00000CA0	854	LA R7, DURATION Difference
00000B3C	45F0 895C		00000B5C	855	BAL R15, SUBDWORD Calculate duration
				856	*
00000B40	9857 8950		00000B50	857	LM R5, R7, CALCWORK Restore work registers
00000B44	58F0 894C		00000B4C	858	L R15, CALCRET Restore return address
00000B48	07FF			859	BR R15 Return to caller
00000B4C	00000000			861	CALCRET DC F' 0' R15 save area
00000B50	00000000 00000000			862	CALCWORK DC 3F' 0' R5-R7 save area
				864	*****
				865	* SUBDWORD Subtract two doublewords
				866	* R5 --> subtrahend, R6 --> minuend, R7 --> result
				867	*****
00000B5C	9014 8980		00000B80	869	SUBDWORD STM R1, R4, SUBDWSAV Save registers
				870	*
00000B60	9812 5000		00000000	871	LM R1, R2, 0(R5) Subtrahend (value to subtract)
00000B64	9834 6000		00000000	872	LM R3, R4, 0(R6) Minuend (what to subtract FROM)
00000B68	1F42			873	SLR R4, R2 Subtract LOW part
00000B6A	47B0 8972		00000B72	874	BNM *+4+4 (branch if no borrow)
00000B6E	5F30 8A78		00000C78	875	SL R3, =F' 1' (otherwise do borrow)
00000B72	1F31			876	SLR R3, R1 Subtract HIGH part
00000B74	9034 7000		00000000	877	STM R3, R4, 0(R7) Store results
				878	*
00000B78	9814 8980		00000B80	879	LM R1, R4, SUBDWSAV Restore registers
00000B7C	07FF			880	BR R15 Return to caller
00000B80	00000000 00000000			882	SUBDWSAV DC 2D' 0' R1-R4 save area

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
		00000D18	00004425	997 CU12TST CSECT ,	
				999 *****	
				1000 * CU12 Testing Control tables (ref: CU12TEST DSECT)	
				1001 *****	
00000D18				1002 PRINT DATA	
				1003 CU12CTL DC 0A(0) start of table	
				1004 *****	
				1005 * tests with CC=0 MB=0	
				1006 *****	
00000D18				1008 CC0T1 DS 0F	
00000D18	01			1009 DC X' 01'	Test Num
00000D19	0000			1010 DC X' 00' , X' 00'	
00000D1B	00			1011 DC X' 00'	MB
				1012 *	
00000D1C	00000E38	00000044		1013 DC A(UTF16A) , A(UTF16AED- UTF16A)	target - 0p1 & length
00000D24	00000DF0	0000003D		1014 DC A(UTF8A) , A(UTF8AEND- UTF8A)	Source - 0p2 & length
				1015	
00000D2C	00400000			1016 DC A(4*MB+(0*K16))	target
00000D30	00200000			1017 DC A(2*MB+(0*K16))	source
				1018 *	
00000D34	00000007			1019 DC A(7)	FailCC - not CC0
00000D38	00000000			1020 DC A(0)	Result - target len
00000D3C	00000000			1021 DC A(0)	Result - source len
00000D40				1023 CC0T2 DS 0F	
00000D40	02			1024 DC X' 02'	Test Num
00000D41	0000			1025 DC X' 00' , X' 00'	
00000D43	00			1026 DC X' 00'	MB
				1027 *	
00000D44	00000E38	00000044		1028 DC A(UTF16A) , A(UTF16AED- UTF16A)	target - 0p1 & length
00000D4C	00000DF0	0000003D		1029 DC A(UTF8A) , A(UTF8AEND- UTF8A)	Source - 0p2 & length
				1030	
00000D54	0010C000			1031 DC A(1*MB+(3*K16))	target
00000D58	00213FE9			1032 DC A(2*MB+(5*K16) - 23)	source
				1033 *	
00000D5C	00000007			1034 DC A(7)	FailCC - not CC0
00000D60	00000000			1035 DC A(0)	Result - target len
00000D64	00000000			1036 DC A(0)	Result - source len
00000D68				1038 CC0T3 DS 0F	
00000D68	03			1039 DC X' 03'	Test Num
00000D69	0000			1040 DC X' 00' , X' 00'	
00000D6B	00			1041 DC X' 00'	MB
				1042 *	
00000D6C	00000E38	00000044		1043 DC A(UTF16A) , A(UTF16AED- UTF16A)	target - 0p1 & length
00000D74	00000DF0	0000003D		1044 DC A(UTF8A) , A(UTF8AEND- UTF8A)	Source - 0p2 & length
				1045	
00000D7C	0011BFE9			1046 DC A(1*MB+(7*K16) - 23)	target
00000D80	00224000			1047 DC A(2*MB+(9*K16))	source
				1048 *	

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				1092 *****
				1093 * CU12 UTF-8 tests
				1094 *****
00000DEC	0000003D			1096 UTF8ALN DC A(UTF8AEND- UTF8A)
00000DF0				1097 UTF8A DS 0H
00000DF0	00			1098 DC XL1' 00' first UTF-8 1 Byte character
00000DF1	31			1099 DC XL1' 31' 1
00000DF2	39			1100 DC XL1' 39' 9
00000DF3	40			1101 DC XL1' 40' @
00000DF4	41			1102 DC XL1' 41' A
00000DF5	42			1103 DC XL1' 42' B
00000DF6	7F			1104 DC XL1' 7F' last UTF-8 1 Byte character
00000DF7	C280			1106 DC XL2' C280' first UTF-8 2 Byte character
00000DF9	C380			1107 DC XL2' C380' c3 80 LATIN CAPITAL LETTER A WITH GRAVE
00000DFB	C3B8			1108 DC XL2' C3B8' c3 b8 LATIN SMALL LETTER O WITH STROKE
00000DFD	D09C			1109 DC XL2' D09C' D0 9C Dœ Cyrillic Capital Letter Em
00000DFE	DFBF			1110 DC XL2' DFBF' last UTF-8 2 Byte character DF BF ß
00000E01	43			1112 DC XL1' 43' C
00000E02	E0A080			1114 DC XL3' E0A080' first UTF-8 3 Byte character
				1115 * E0 A0 80 à € Samaritan Letter Alaf
00000E05	E0A18D			1116 DC XL3' E0A18D' E0 A1 8D à• Mandaic Letter An
00000E08	EA9FBD			1117 DC XL3' EA9FBD' EA 9F BD ê½ Latin Epigraphic Inverted M
00000E0B	EFBF87			1118 DC XL3' EFBF87' EF BF 87 ï½ Halfwidth Hangul Letter E
00000E0E	EFBFBF			1119 DC XL3' EFBFBF' last UTF-8 3 Byte character EF BF BF
00000E11	44			1121 DC XL1' 44' D
00000E12	F0908080			1123 DC XL4' F0908080' first UTF-8 4 Byte character
				1124 * F0 90 80 80 ð•€€ Linear B Syllable B008 A
00000E16	F0908487			1125 DC XL4' F0908487' F0 90 84 87 ð•,½ Aegean Number One
00000E1A	F09294B5			1126 DC XL4' F09294B5' F0 92 94 B5 Cuneiform Sign She Plus Sar
00000E1E	F09082B8			1127 DC XL4' F09082B8' F0 90 82 B8 ð•, Linear B Ideogram B177
00000E22	F096AB83			1128 DC XL4' F096AB83' F0 96 A8 83 ð-½ Bamum Letter Phase-f Ka
00000E26	F0989A9F			1129 DC XL4' F0989A9F' last UTF-8 4 Byte character
00000E2A	45			1131 DC XL1' 45' E
00000E2B	4E			1132 DC XL1' 4E' N
00000E2C	44			1133 DC XL1' 44' D
00000E2D				1134 UTF8AEND DS 0X
				1135
				1137 *****
				1138 * CU12 UTF-12 Result
				1139 *****
00000E2D	E4C6E3F3 F27A			1140 DC C' UFT32: '
00000E34	00000044			1141 UTF16ALN DC A(UTF16AED- UTF16A)
00000E38				1142 UTF16A DC 0X
00000E38	0000			1143 DC X' 0000'
00000E3A	0031			1144 DC X' 0031'
00000E3C	0039			1145 DC X' 0039'
00000E3E	0040			1146 DC X' 0040'
00000E40	0041			1147 DC X' 0041'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				1181 *****
				1182 * UTF-8 LONG LONG LONG String (CC=3 result)
				1183 *****
00000E7C				1185 UTF8B DS 0F
00000E7C	0A			1186 DC x' 0a'
00000E7D	3C			1187 DC x' 3c'
00000E7E	21			1188 DC x' 21'
00000E7F	44			1189 DC x' 44'
00000E80	4F			1190 DC x' 4f'
00000E81	43			1191 DC x' 43'
00000E82	54			1192 DC x' 54'
00000E83	59			1193 DC x' 59'
00000E84	50			1194 DC x' 50'
				1195 * ETC.....
				1197 PRINT OFF
				14917 PRINT ON
				14918 * ETC.....
				14919
0000441B	79			14920 DC x' 79'
0000441C	3E			14921 DC x' 3e'
0000441D	0A			14922 DC x' 0a'
0000441E	3C			14923 DC x' 3c'
0000441F	2F			14924 DC x' 2f'
00004420	68			14925 DC x' 68'
00004421	74			14926 DC x' 74'
00004422	6D			14927 DC x' 6d'
00004423	6C			14928 DC x' 6c'
00004424	3E			14929 DC x' 3e'
00004425	0A			14930 DC x' 0a'
00004426				14931 UTF8BEND DS 0C
				14933 *****
				14934 * Register equates
				14935 *****
	00000000	00000001	14937 R0	EQU 0
	00000001	00000001	14938 R1	EQU 1
	00000002	00000001	14939 R2	EQU 2
	00000003	00000001	14940 R3	EQU 3
	00000004	00000001	14941 R4	EQU 4
	00000005	00000001	14942 R5	EQU 5
	00000006	00000001	14943 R6	EQU 6
	00000007	00000001	14944 R7	EQU 7
	00000008	00000001	14945 R8	EQU 8
	00000009	00000001	14946 R9	EQU 9
	0000000A	00000001	14947 R10	EQU 10
	0000000B	00000001	14948 R11	EQU 11
	0000000C	00000001	14949 R12	EQU 12
	0000000D	00000001	14950 R13	EQU 13

LOC

OBJECT CODE**ADDR1****ADDR2****STMT**

0000000E	00000001	14951 R14	EQU	14
0000000F	00000001	14952 R15	EQU	15

14954 **END**

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SYMBOL	TYPE	VALUE	LENGTH	DEFN	REFERENCES																
BEGCLOCK	D	000C90	8	951	236	460	842	845	852												
BEGIN	I	000200	2	120	151	87	117	118													
CALCDUR	I	000B08	4	839	452	797															
CALCRET	F	000B4C	4	861	839	858															
CALCWORK	F	000B50	4	862	840	857															
CCOT1	F	000D18	4	1008																	
CCOT2	F	000D40	4	1023																	
CCOT3	F	000D68	4	1038																	
CCOT4	F	000D90	4	1053																	
CCOT5	F	000DB8	4	1073																	
CU122TST	J	000000	3352	82	85	89	93	83													
CU12CTL	A	000D18	4	1003	205																
CU12NEXT	U	000028	1	994	783																
CU12PERF	4	000000	40	971	206																
CU12TST	J	000D18	14094	997																	
DURATION	D	000CA0	8	953	453	800	801	804	854												
EDIT	X	000D0C	12	963	814	815															
ENDCLOCK	D	000C98	8	952	451	774	847	850	853												
ENDLN1	A	000020	4	990																	
ENDLN2	A	000024	4	991																	
EOJ	I	000C58	4	924	137	145															
EOJPSW	D	000C48	8	922	924																
FAILMASK	A	00001C	4	987																	
FAILPSW	D	000C60	8	926	928																
FAILTEST	I	000C70	4	928	140	143															
IMAGE	1	000000	17446	0																	
K	U	000400	1	942	943	944	945	946	947	1078											
K16	U	004000	1	944	1016	1017	1031	1032	1046	1047	1061	1062	1081	1082							
K32	U	008000	1	945																	
K64	U	010000	1	946																	
MB	X	000003	1	975																	
MB	U	100000	1	947	1016	1017	1031	1032	1046	1047	1061	1062	1078	1081	1082						
MSG	I	000B90	4	889	823																
MSGCMD	C	000BDA	9	915	902	903															
MSGMSG	C	000BE3	95	916	896	913	894														
MSGMVC	I	000BD4	6	913	900																
MSGOK	I	000BA6	2	898	895																
MSGRET	I	000BC0	4	909	906																
MSGSAVE	F	000BC8	4	912	892	909															
NUMLOOPS	F	000C8C	4	949	235	459															
OP1DATA	A	000004	4	978																	
OP1LEN	F	000008	4	979	225																
OP1WHERE	A	000014	4	984	224																
OP2DATA	A	00000C	4	980	219																
OP2LEN	F	000010	4	981	218	220	227														
OP2WHERE	A	000018	4	985	217	226															
OPSPERF	D	000A68	8	788	228	242	244	251	253	255	257	259	261	263	265						
					267	269	271	273	275	277	279	281	283	285	287						
					289	291	293	295	297	299	301	303	305	307	309						
					311	313	315	317	319	321	323	325	327	329	331						
					333	335	337	339	341	343	345	347	349	351	353						
					355	357	359	361	363	365	367	369	371	373	375						
					377	379	381	383	385	387	389	391	393	395	397						
					399	401	403	405	407	409	411	413	415	417	419						
					421	423	425	427	429	431	433	435	437	439	441						
					446	448	465	468	476	479	482	485	488	491	494						

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					497	500	503	506	509	512	515	518	521	524	527							
					530	533	536	539	542	545	548	551	554	557	560							
					563	566	569	572	575	578	581	584	587	590	593							
					596	599	602	605	608	611	614	617	620	623	626							
					629	632	635	638	641	644	647	650	653	656	659							
					662	665	668	671	674	677	680	683	686	689	692							
					695	698	701	704	707	710	713	716	719	722	725							
					728	731	734	737	740	743	746	749	752	755	758							
					761	767	770															
					OPSWHERE	U	000014	1	983													
					OVERHEAD	D	000CA8	8	954	453	799											
					PAGE	U	001000	1	943													
					PRTLIN	C	000CC8	38	960	962	777	814	815	822								
					PRTLNG	U	000044	1	962	821												
					R0	U	000000	1	14937	83	217	221	224	228	242	244	251	253	255	257		
					259	261	263	265	267	269	271	273	275	277	279							
					281	283	285	287	289	291	293	295	297	299	301							
					303	305	307	309	311	313	315	317	319	321	323							
					325	327	329	331	333	335	337	339	341	343	345							
					347	349	351	353	355	357	359	361	363	365	367							
					369	371	373	375	377	379	381	383	385	387	389							
					391	393	395	397	399	401	403	405	407	409	411							
					413	415	417	419	421	423	425	427	429	431	433							
					435	437	439	441	446	448	465	466	468	469	476							
					477	479	480	482	483	485	486	488	489	491	492							
					494	495	497	498	500	501	503	504	506	507	509							
					510	512	513	515	516	518	519	521	522	524	525							
					527	528	530	531	533	534	536	537	539	540	542							
					543	545	546	548	549	551	552	554	555	557	558							
					560	561	563	564	566	567	569	570	572	573	575							
					576	578	579	581	582	584	585	587	588	590	591							
					593	594	596	597	599	600	602	603	605	606	608							
					609	611	612	614	615	617	618	620	621	623	624							
					626	627	629	630	632	633	635	636	638	639	641							
					642	644	645	647	648	650	651	653	654	656	657							
					659	660	662	663	665	666	668	669	671	672	674							
					675	677	678	680	681	683	684	686	687	689	690							
					692	693	695	696	698	699	701	702	704	705	707							
					708	710	711	713	714	716	717	719	720	722	723							
					725	726	728	729	731	732	734	735	737	738	740							
					741	743	744	746	747	749	750	752	753	755	756							
					758	759	761	762	767	768	770	771	820	821	824							
										889	892	894	896	898	909							
218	225	822	869	871						876	879	903	913									
R10	U	00000A	1	14947						804	805	807										
R11	U	00000B	1	14948						804	808											
R12	U	00000C	1	14949																		
R13	U	00000D	1	14950						205	206	209	782	783	784							
R14	U	00000E	1	14951						130	203	786										
R15	U	00000F	1	14952						452	778	794	797	802	827	828	839	855	858	859		
										880												
										R2	U	000002	1	14939	219	221	226	466	469	477	480	483
					495	498	501	504	507	510	513	516	519	522	525							
					528	531	534	537	540	543	546	549	552	555	558							
					561	564	567	570	573	576	579	582	585	588	591							
					594	597	600	603	606	609	612	615	618	621	624							

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R3	U	000003	1	14940	627	630	633	636	639	642	645	648	651	654	657	
					660	663	666	669	672	675	678	681	684	687	690	
					693	696	699	702	705	708	711	714	717	720	723	
					726	729	732	735	738	741	744	747	750	753	756	
					759	762	768	771	820	823	824	871	873	890	892	
					898	899	900	902	909	910						
					220	227	228	237	242	244	251	253	255	257	259	
					261	263	265	267	269	271	273	275	277	279	281	
					283	285	287	289	291	293	295	297	299	301	303	
					305	307	309	311	313	315	317	319	321	323	325	
					327	329	331	333	335	337	339	341	343	345	347	
					349	351	353	355	357	359	361	363	365	367	369	
					371	373	375	377	379	381	383	385	387	389	391	
					393	395	397	399	401	403	405	407	409	411	413	
					415	417	419	421	423	425	427	429	431	433	435	
R4	U	000004	1	14941	437	439	441	446	448	465	468	476	479	482	485	
					488	491	494	497	500	503	506	509	512	515	518	
					521	524	527	530	533	536	539	542	545	548	551	
					554	557	560	563	566	569	572	575	578	581	584	
					587	590	593	596	599	602	605	608	611	614	617	
					620	623	626	629	632	635	638	641	644	647	650	
					653	656	659	662	665	668	671	674	677	680	683	
					686	689	692	695	698	701	704	707	710	713	716	
					719	722	725	728	731	734	737	740	743	746	749	
					752	755	758	761	767	770	776	872	875	876	877	
					869	872	873	877	879							
					237	776	795	799	826	840	852	857	871			
					211	212	238	450	461	773	800	842	843	844	845	
					847	848	849	850	853	872						
					R5	U	000005	1	14942	237	776	795	799	826	840	852
R6	U	000006	1	14943	211	212	238	450	461	773	800	842	843	844	845	
R7	U	000007	1	14944	847	848	849	850	853	872						
R8	U	000008	1	14945	235	450	459	773	801	840	842	845	847	850	854	
R9	U	000009	1	14946	857	877										
RPTDWSAV	D	000AF8	8	833	117	120	121	122	124							
RPTSAVE	F	000AF0	4	830	118	124	125									
RPTSPEED	I	000A88	4	794	820	824										
RPTSVR5	F	000AF4	4	831	794	827										
SAVE3T5	F	000410	4	161	778											
SAVER13	F	000424	4	163	795	826										
SAVER2	F	000420	4	162	237	776										
SUBDWORD	I	000B5C	4	869	209	782										
SUBDWSAV	D	000B80	8	882	802	855										
SUBTEST	X	000401	1	155	869	879										
TEST91	I	000528	4	202												
TESTADDR	D	000400	8	153	142											
TESTNUM	X	000400	1	154	130											
TICKSAAA	P	000CB0	8	956	139	212										
TICKSBBB	P	000CB8	8	957	807	810										
TICKSTOT	P	000CC0	8	958	808	812	815									
TIMEOPT	X	000408	1	158	810	811										
TNUM	X	000000	1	972	136	202										
TST91LOP	U	000532	1	208	211											
UTF16A	X	000E38	1	1142	785											
UTF16AED	X	000E7C	1	1179	1013	1028	1043	1058	1078	1141						
UTF16ALN	A	000E34	4	1141	1013	1028	1043	1058	1141							
UTF8A	H	000DF0	2	1097	1014	1029	1044	1059	1096							

SYMBOL	TYPE	VALUE	LENGTH	DEFN	REFERENCES				
UTF8AEND	X	000E2D	1	1134	1014	1029	1044	1059	1096
UTF8ALN	A	000DEC	4	1096					
UTF8B	F	000E7C	4	1185	1079				
UTF8BEND	C	004426	1	14931	1079				
=AL2(L' MSGMSG)	R	000C7E	2	938	894				
=CL5' CU12'	C	000C80	5	939	777				
=F' 0'	F	000C74	4	935	784				
=F' 1'	F	000C78	4	936	875				
=H' 0'	H	000C7C	2	937	889				
=P' 4294967296'	P	000C85	6	940	811				

DESC	SYMBOL	SIZE	POS	ADDR
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Entry: 0

Image Region	IMAGE	17446	0000-4425	0000-4425
		17446	0000-4425	0000-4425
	CSECT CU122TST	3352	0000-0D17	0000-0D17
	CSECT CU12TST	14094	0D18-4425	0D18-4425

STM

FILE NAME

1

/devstor/dev/tests/. /CU12-02-performance.asm

**** NO ERRORS FOUND ****